

## EPC3132-X



- Used in SMPS Flyback Topology
- Designed for use with Power Integration Chip
- Reinforced Insulation (Triple Insulated Wire)
- UL 94V-0 Recognized Components
- UL 1446 Class F Insulation System
- Very Low Core Loss

### Electrical Parameters @ 25° C

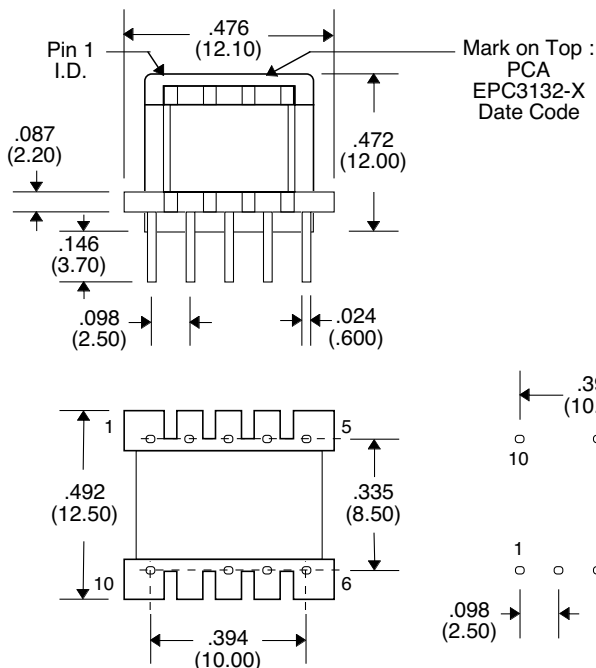
PCA Part Number	Chipset	Voltage (Vdc)				Primary OCL ( $\mu\text{H} \pm 10\%$ ) @ 10 KHz, 0.1 Vrms	Current (Amp.)		
		V in Pin 1-3	V out 1	V out 2	V out 3		Sec. 1	Sec. 2	Sec. 3
EPC3132-1	TNY264P (132 KHz)	120-375	9	N	N	2000	.330	N	N
EPC3132-2	TNY264P(132 KHz)	120-375	5	N	12	1900	.600	N	.05
EPC3132-3	TOP221P (100 KHz)	120-375	5.1	N	N	1400	.100	N	N
*EPC3132-4	LNK501 (42 KHz)	120-375	5.5	N	N	2550	.500	N	N
*EPC3132-5	LNK501 (42 KHz)	120-375	5.5	N	N	1360	.273	N	N
*EPC3132-6	TNY264P (132 KHz)	120-375	5.2	10.5	N	2400	.600	.015	N
EPC3132-7	TNY264P (132 KHz)	120-375	5.2	12.0	N	2400	.600	.015	N
EPC3132-8	TNY264P (132 KHz)	120-375	3.3	N	12	1900	.600	N	.05

• Switching Frequency : 132 KHz/100 KHz/42 KHz • Isolation : 3750 Vdc • \*Copper Flux Band : is connected to Pin #3 •

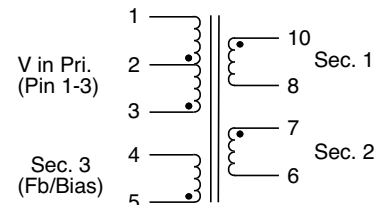
Dielectric Rating (Vdc)		
1 sec. 4500 or 60 sec. 3750	1 sec. 4500 or 60 sec. 3750	1 sec. 1500 or 60 sec. 1000
Pri. to all Sec. Wdg's	Between Sec. Wdg's to Core	Between Pri. Wdg & Bias

• Note : "N" means Not Required/No Connections • Marking : Mark on top or side •

### Package



### Schematic



Pin #2 is not connected in circuit  
Pin #9 is omitted

Unless Otherwise Specified Dimensions are in Inches /mm ± .010 / .25