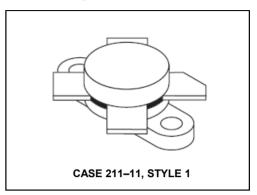


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Designed primarily for high–voltage applications as a high–power linear amplifier from 2.0 to 30 MHz. Ideal for marine and base station equipment.

- Specified 50 V, 30 MHz Characteristics —
   Output power = 150 W (PEP)
   Minimum gain = 13 dB
   Efficiency = 45%
- Intermodulation distortion @ 150 W (PEP) IMD = -32 dB (Max)
- Diffused emitter resistors for superior ruggedness
- 100% tested for load mismatch at all phase angles with 30:1 VSWR @ 150 W CW

#### **Product Image**



#### MAXIMUM RATINGS

Rating	Symbo	l Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	50	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	100	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current — Continuous	Ic	16	Adc
Withstand Current — 10 s	_	20	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	233 1.33	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>eJC</sub>	0.75	°C/W

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 200 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	50	_	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 100 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	100	_	_	Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 100 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	100	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	_	_	Vdc

(continued)

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### **MRF429**



# The RF Line NPN Silicon Power Transistor 150W(PEP), 30MHz, 50V

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#### ELECTRICAL CHARACTERISTICS — continued (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS	•	•	•	•	•
DC Current Gain (I <sub>C</sub> = 5.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	10	30	80	_
DYNAMIC CHARACTERISTICS	•	•	-	•	
Output Capacitance (V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	220	300	pF
FUNCTIONAL TESTS	•	•	•	•	•
Common–Emitter Amplifier Gain (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W (PEP), I <sub>C</sub> (max) = 3.32 Adc, f = 30; 30.001 MHz)	G <sub>PE</sub>	13	15	_	dB
Output Power (V <sub>CE</sub> = 50 Vdc, f = 30; 30.001 MHz)	P <sub>out</sub>	150	_	_	W (PEP)
Collector Efficiency (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W (PEP), I <sub>C</sub> (max) = 3.32 Adc, f = 30, 30.001 MHz)	η	45	_	_	%
Intermodulation Distortion (1) (V <sub>CE</sub> = 50 Vdc, P <sub>out</sub> = 150 W (PEP), I <sub>C</sub> = 3.32 Adc)	IMD	_	-35	-32	dB
Electrical Ruggedness (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W CW, f = 30 MHz, VSWR 30:1 at all Phase Angles)	Ψ	No Degradation in Output Power			

#### NOTE:

<sup>1.</sup> To Mil-Std-1311 Version A, Test Method 2204, Two Tone, Reference each Tone.

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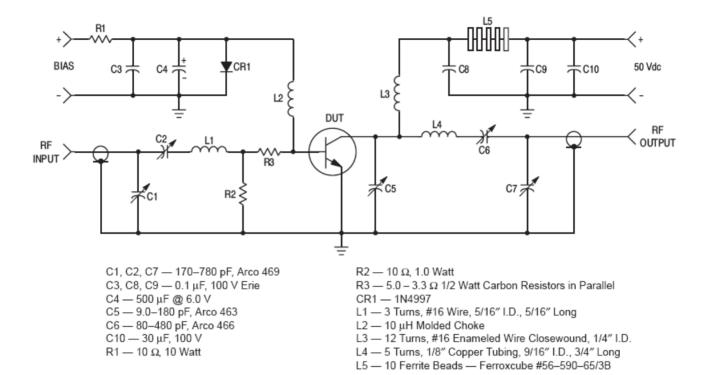
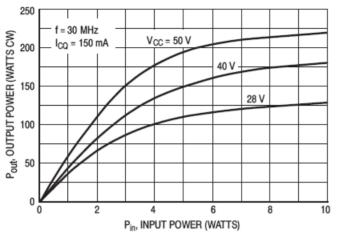


Figure 1. 30 MHz Test Circuit Schematic

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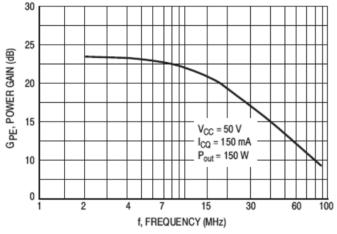


250

— f = 30, 30.001 MHz
— lcq = 150 mA
— IMD = d<sub>3</sub>
— IMD = -30 dB
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— 35 dB
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Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Supply Voltage



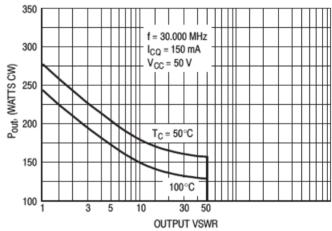


Figure 4. Power Gain versus Frequency

Figure 5. RF Safe Operating Area (SOAR)

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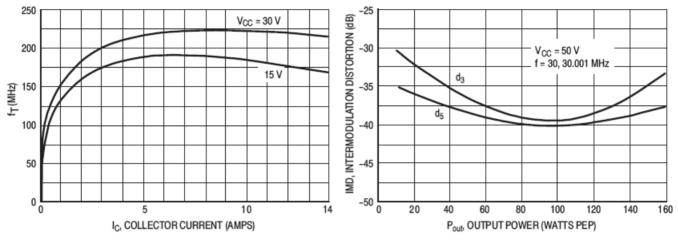


Figure 6. f<sub>T</sub> versus Collector Current

Figure 7. IMD versus Pout

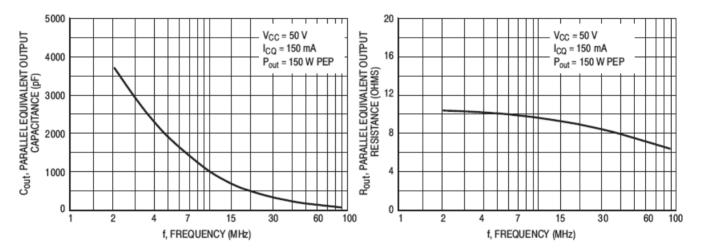


Figure 8. Output Capacitance versus Frequency

Figure 9. Output Resistance versus Frequency

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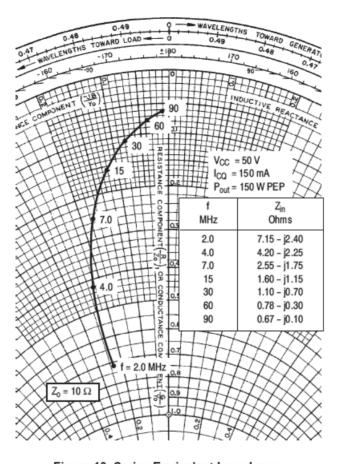
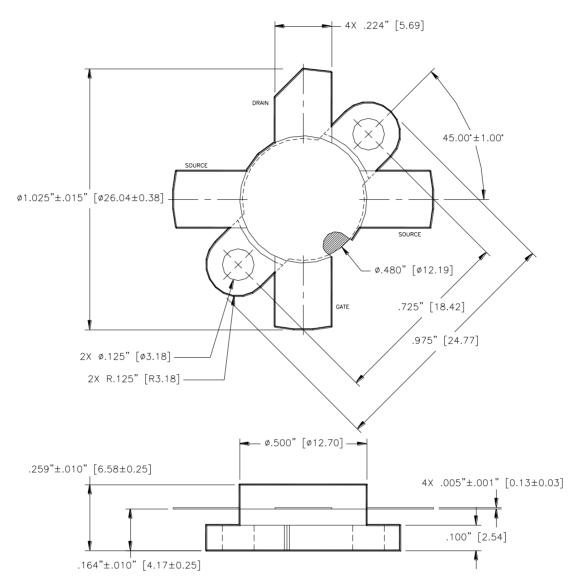


Figure 10. Series Equivalent Impedance

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Unless otherwise noted, tolerances are inches  $\pm .005$ " [millimeters  $\pm 0.13$ mm]

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