TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

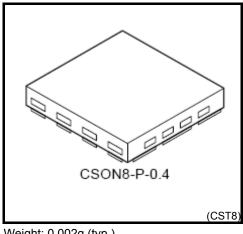
TC7WH157FC

2-Channel Multiplexer

Features

- High-speed
- Low power dissipation
- High noise immunity
- Operation voltage range
- 5.5-V Tolerant inputs.

:t_{pd} = 4.1 ns (typ.) at V_{CC} = 5 V, C_L = 15pF $:I_{CC} = 2\mu A (max)$ at Ta = 25°C :V_{NIH} = V_{NIL} = 28% V_{CC} (min) :V_{CC} = 2 to 5.5 V



Weight: 0.002g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Ratingh	Unit
Supply viltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	VIN	–0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5 (Note1)	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20 (Note2)	mA
DC output current	IOUT	±25	mA
DC V _{CC} /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1 : High or Low State.

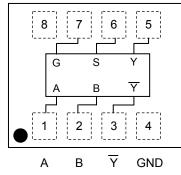
IOUT absolute maximum rating must be observed.

- Note2 : V_{OUT} < GND , V_{OUT} > V_{CC}
- Note3 : Mounted on an FR4 board. $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{Cu Pad: } 11.56 \text{ mm}^2)$

Marking Product name H157

Pin Assignment (top view)

ST SELECT Y Vcc



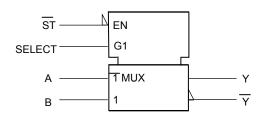
Start of commercial production 2005-10

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Truth Table

	Inpu	Outputs				
IST	SELECT	А	В	Y	Y	
Н	Х	Х	Х	L	Н	
L	L	L	Х	L	Н	
L	L	Н	Х	Н	L	
L	Н	Х	L	L	Н	
L	Н	Х	Н	Н	L	

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Rathing	Unit	
Supply voltage	V _{CC}	2 to 5.5	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V	
	ul/uv	0 to 20 (V_{CC} = 5.0 V \pm 0.5 V)	115/ V	

DC Electrical Characteristics

Characteristic	Symbol	Test condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristic	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				2.0	1.5	_		1.5	_	V
High-level input voltage	VIH		—		V _{CC} × 0.7		_	V _{CC} × 0.7	_	
				2.0	_	_	0.5	_	0.5	
Low-level input voltage V _{IL}			_	3.0 to 5.5	_	_	$V_{CC} \times 0.3$		$V_{CC} \times 0.3$	
		V _{IN =} V _{IL} or V _{IH}	I _{OH} = –50 μA	2.0	1.9	2.0		1.9		V
				3.0	2.9	3.0		2.9	_	
High-level output voltage	V _{OH}			4.5	4.4	4.5		4.4	_	
			I _{OH} = –4 mA	3.0	2.58	_		2.48	_	
			I _{OH} = –8 mA	4.5	3.94	_	_	3.80	—	
		V _{IN =} V _{IL} or V _{IH}	I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	
Low-level output voltage				3.0	—	0.0	0.1	_	0.1	
	V _{OL}			4.5	—	0.0	0.1	_	0.1	
			I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1		±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		5.5	_		2.0		20.0	μA

AC Electrical Characteristics (unless otherwise specified, Input : $t_r = t_f = 3 \text{ ns}$)

Characteristic	Symbol		Test condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol	\ \	/ _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	UIII
		3	3.3 ± 0.3	15	_	6.2	9.7	1.0	11.5	ns
Propagation deley time	t _{pLH}	5		50	_	8.7	13.2	1.0	15.0	
(A,B – Y , Y)	t _{pHL}	5	5.0 ± 0.5	15	_	4.1	6.4	1.0	7.5	
		5		50	_	5.6	8.4	1.0	9.5	
Propagation deley time (SELECT-Y,Y)		2	3.3 ± 0.3	15	_	8.4	13.2	1.0	15.5	ns ns
	t _{pLH}	3		50	_	10.9	16.7	1.0	19.0	
	t _{pHL}	_	5.0 ± 0.5	15	_	5.3	8.1	1.0	9.5	
		5		50	_	6.8	10.1	1.0	11.5	
Propagation deley time (ST-Y,Y)		2	3.3 ± 0.3	15	_	8.7	13.6	1.0	16.0	
	t _{pLH}	3		50	_	11.2	17.1	1.0	19.5	
	t _{pHL}	_	50,05	15	_	5.6	8.6	1.0	10.0	
		5	.0 ± 0.5	50	_	7.1	10.6	1.0	12.0	
Input capacitance	C _{IN}		_		_	4	10		10	pF
Power dissipation capacitanse	C _{PD}			(Note 4)	_	20		_	_	pF

(Note 4): C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

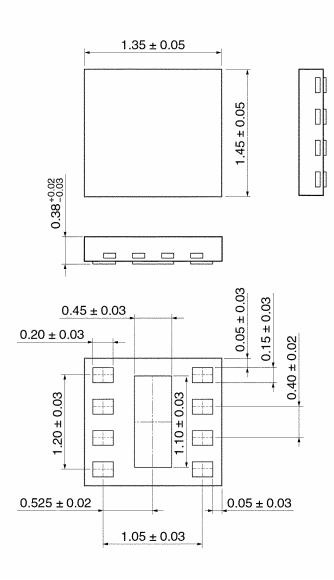
Average operating current can be obtained by the equation:

 $I_{CC (opr..)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

CSON8-P-0.4



Weight : 0.002 g (Typ.)

Unit: mm

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