



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	I _D T _A = +25°C
-12V	$18m\Omega$ @ $V_{GS} = -4.5V$	-7.6 A

Description

This new generation MOSFET has been designed to minimize the onstate resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

Features

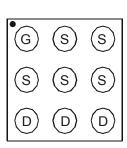
- Low Q_a & Q_{ad}
- Small Footprint 1.5-mm × 1.5-mm
- Gate ESD Protection 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

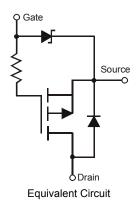
Case: U-WLB1515-9

Terminal Connections: See Diagram Below





Top-View Pin Configuration



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP1018UCB9-7	U-WLB1515-9	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



EW = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

Date Code Key

Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	()	D		Е		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-12	V
Gate-Source Voltage		V_{GSS}	-6	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	I _D	-7.6 -6.0	Α	
Continuous Drain Current (Note 6) V _{GS} = -4.5V	I _D	-5.5 -4.3	Α	
Pulsed Drain Current (Pulse duration 10µs, duty cy	I _{DM}	-60	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	1.0	W
Total Power Dissipation (Note 6)	P _D	1.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Roja	126.8	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	69	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	°C

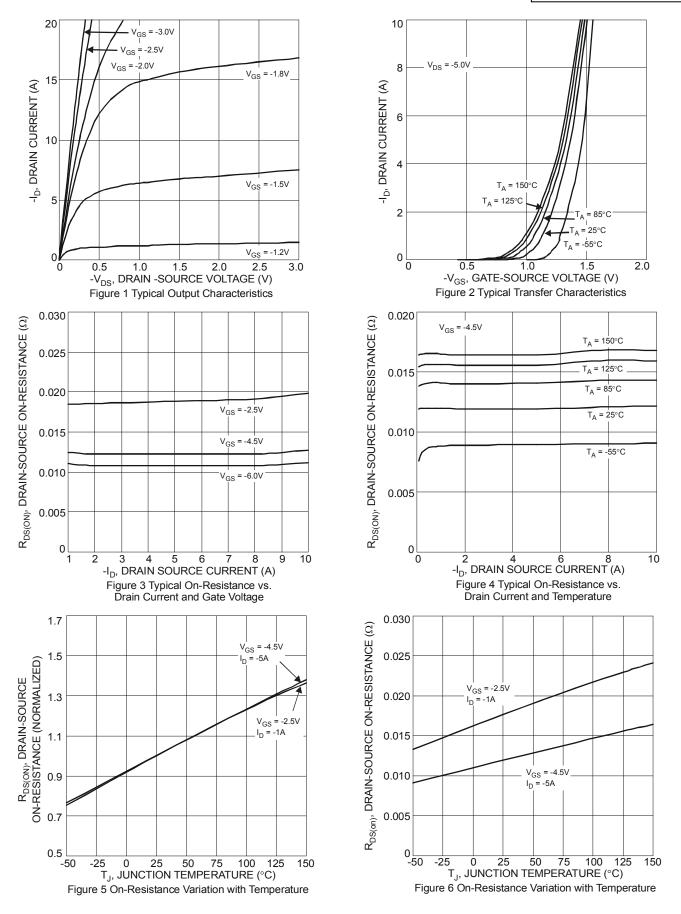
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)				-	-	
Drain-Source Breakdown Voltage	BV _{DSS}	-12	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current @T _C = +25°C	I _{DSS}	-	-	-1	μΑ	$V_{DS} = -9.6V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	-100	nA	$V_{GS} = -6V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	-0.8	-1.3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance			12	18	m0	$V_{GS} = -4.5V, I_D = -2A$
Static Drain-Source On-Resistance	R _{DS} (ON)	-	15	22	mΩ	$V_{GS} = -2.5V, I_D = -2A$
Forward Transfer Admittance	Y _{fs}	-	5.5	-	S	V _{DS} = -6V, I _D = -2A
Diode Forward Voltage (Note 6)	V _{SD}	-	-0.7	-1	V	V _{GS} = 0V, I _S = -2A
Reverse Recovery Charge	Q _{rr}	-	30.2	-	nC	$V_{dd} = -5V, I_F = -2A,$
Reverse Recovery Time	t _{rr}	-	71.4	-	ns	di/dt = 200A/µs
DYNAMIC CHARACTERISTICS (Note 8)				-	-	
Input Capacitance	C _{iss}	ı	457	-	pF	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Output Capacitance	Coss	-	272	-	pF	$V_{DS} = -6V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	120	-	pF	71 = 1.0101112
Series Gate Resistance	R _G		21.23	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (4.5V)	Qq	-	4.9	-	nC	45)///
Gate-Source Charge		-	0.6	-	nC	$V_{GS} = -4.5V, V_{DS} = -6V,$
Gate-Drain Charge	Q _{gs} Q _{gd}	-	1.1	-	nC	I _D = -2A
Turn-On Delay Time	t _{D(on)}	-	4.45	-	ns	
Turn-On Rise Time		-	12.0	-	ns	$V_{DD} = -6V, V_{GS} = -4.5V,$
Turn-Off Delay Time		-	100	-	ns	I_{DS} = -2A, R_G = 2 Ω ,
Turn-Off Fall Time	t _{D(off)}	-	93	-	ns	7

Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout.
- Device mounted on FR4 material with 1-inch² (6.45-cm²), 2-oz. (0.071-mm thick) Cu
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.







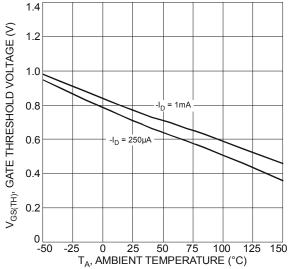
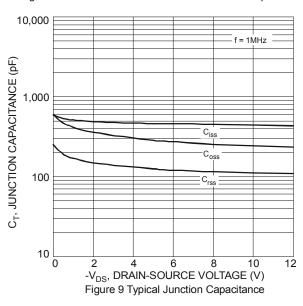
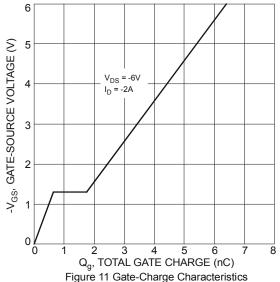
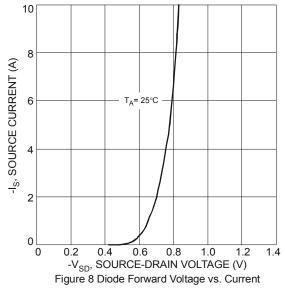


Figure 7 Gate Threshold Variation vs. Ambient Temperature







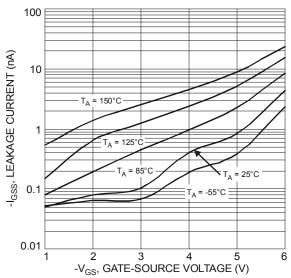
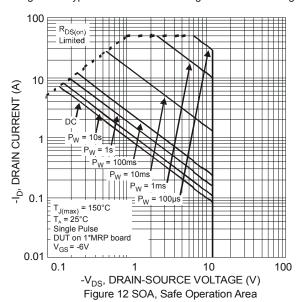
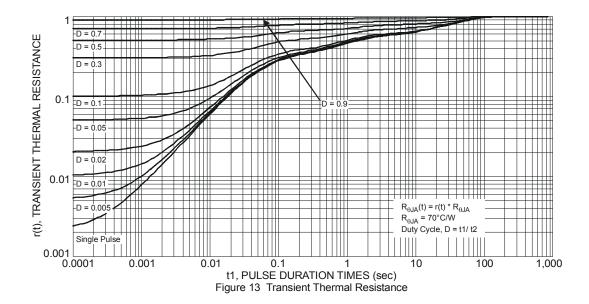


Figure 10 Typical Gate-Source Leakage Current vs. Voltage

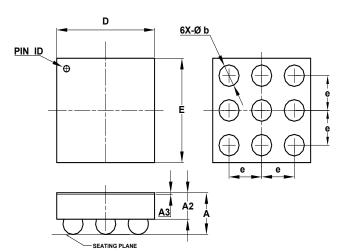






Package Outline Dimensions

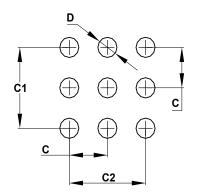
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-WLB1515-9						
Dim	Min	Max	Тур			
Α	-	0.62	-			
A2	-	0.36	0.36			
A3	0.020	0.030	0.025			
b	0.27	0.37	0.32			
D	1.47	1.51	1.49			
Е	1.47	1.51	1.49			
е	-	-	0.50			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.50
C1	1.00
C2	1.00
D	0.25



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