April 2013



FGH40N60SF 600 V, 40 A Field Stop IGBT

Features

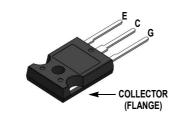
- · High Current Capability
- Low Saturation Voltage: V_{CE(sat)} = 2.3 V @ I_C = 40 A
- High Input Impedance
- Fast Switching: E_{OFF} = 8 uJ/A •
- RoHS Compliant

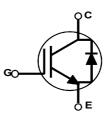
Applications

• Solar Inverter, UPS, Welder, PFC

General Description

Using novel field stop IGBT technology, Fairchild®'s field stop IGBTs offer the optimum performance for solar inverter, UPS, welder and PFC applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V _{CES}	Collector to Emitter Voltage		600	V
V _{GES}	Gate to Emitter Voltage		± 20	V
I _C	Collector Current	@ T _C = 25 ^o C	80	А
	Collector Current	@ T _C = 100°C	40	А
I _{CM (1)}	Pulsed Collector Current	@ T _C = 25 ^o C	120	А
P _D	Maximum Power Dissipation	@ T _C = 25°C	290	W
	Maximum Power Dissipation	@ T _C = 100°C	116	W
TJ	Operating Junction Temperature		-55 to +150	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes: 1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
R _{θJC} (IGBT)	Thermal Resistance, Junction to Case	-	0.43	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	-	40	°C/W

		Device	Packaging Package Type		Qty per Tube		Max Qty per Box	
		FGH40N60SFTU	TO-247	TO-247 Tube		30ea		-
Electric	al Cha	racteristics of th	e IGBT T _C =2	5°C unless otherwise noted				
Symbol		Parameter	Test	Test Conditions		Тур.	Max.	Unit
Off Charac	teristics							
BV _{CES}	Collector	to Emitter Breakdown Volt	age V _{GE} = 0V, I _C	_c = 250μA	600	-	-	V
$\Delta BV_{CES} \ \Delta T_J$	Temperat Voltage	ure Coefficient of Breakdo	WN V _{GE} = 0V, I _C	₂ = 250μA	-	0.6	-	V/ºC
I _{CES}	Collector	Cut-Off Current	V _{CE} = V _{CES}	, V _{GE} = 0V	-	-	250	μA
I _{GES}	G-E Leak	age Current	V _{GE} = V _{GES}	, V _{CE} = 0V	-	-	±400	nA
On Charac	teristics		I					
V _{GE(th)}	1	shold Voltage	I _C = 250μA,	$V_{CE} = V_{CE}$	4.0	5.0	6.5	V
GE((II)		Ū	-	$I_{\rm C} = 40$ A, $V_{\rm GE} = 15$ V		2.3	2.9	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage				-	2.5	-	V
Dynamic C	haractoris	stics			_			
C _{ies}	Input Cap				-	2110	_	pF
C _{oes}		apacitance		V _{CE} = 30V, V _{GE} = 0V, f = 1MHz		200	-	pF
C _{res}	Reverse	Transfer Capacitance	f = 1MHz			60	-	pF
	ļ	· · ·						
Switching					1	1 1		
t _{d(on)}		Delay Time			-	25	-	ns
t _r	Rise Time	-		V_{CC} = 400V, I _C = 40A, R _G = 10 Ω , V _{GE} = 15V, Inductive Load, T _C = 25°C		42	-	ns
t _{d(off)}		Delay Time				115	-	ns
t _f	Fall Time					27	54	ns
E _{on}		Switching Loss				1.13	-	mJ
E _{off}		Switching Loss			-	0.31	-	mJ
E _{ts}		tching Loss			-	1.44	-	mJ
t _{d(on)}		Delay Time			-	24	-	ns
t _r	Rise Time				-	43	-	ns
t _{d(off)}		Delay Time	$V_{\rm CC} = 400V$	V _{CC} = 400V, I _C = 40A, R _G = 10Ω, V _{GE} = 15V,		120	-	ns
t _f	Fall Time		Inductive Lc	oad, T _C = 125°C	-	30	-	ns
E _{on}		Switching Loss		-	-	1.14	-	mJ
E _{off}		Switching Loss tching Loss			-	0.48	-	mJ
г	LIOTAL SWI		1		-	1.62	-	mJ
E _{ts}		-				400		
E _{ts} Q _g Q _{ge}	Total Gate	-	V _{CE} = 400V	, I _C = 40A,	-	120 14	-	nC nC

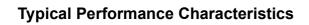


Figure 1. Typical Output Characteristics

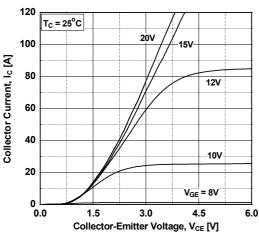


Figure 3. Typical Saturation Voltage Characteristics

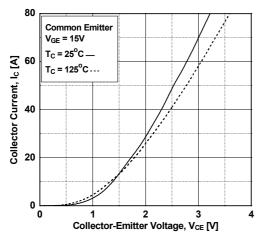


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

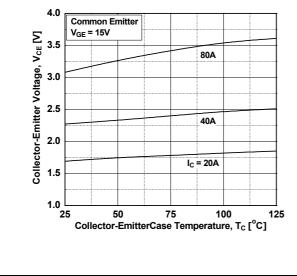


Figure 2. Typical Output Characteristics

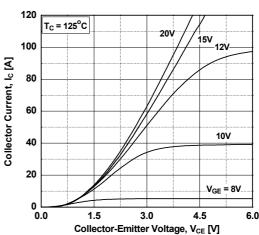


Figure 4. Transfer Characteristics

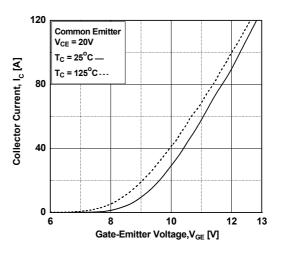
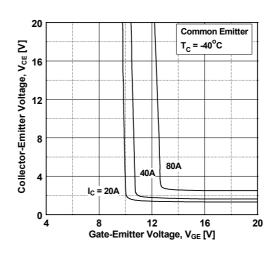


Figure 6. Saturation Voltage vs. V_{GE}



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Figure 7. Saturation Voltage vs. V_{GE} 20 Common Emitter $T_{C} = 25^{\circ}C$ Collector-Emitter Voltage, V_{CE} [V] 16

Typical Performance Characteristics

8 80A 40A ۵ 20A 0 ⊾ 4 8 12 16 Gate-Emitter Voltage, VGE [V]

20

Figure 9. Capacitance Characteristics

12

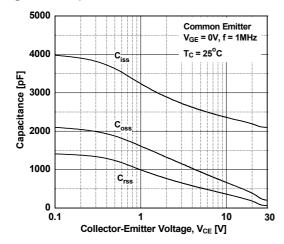


Figure 11. SOA Characteristics

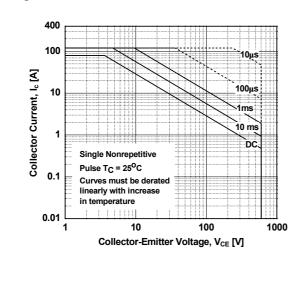


Figure 8. Saturation Voltage vs. V_{GE}

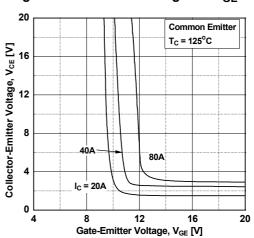


Figure 10. Gate charge Characteristics

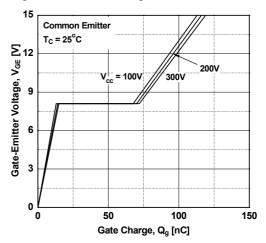
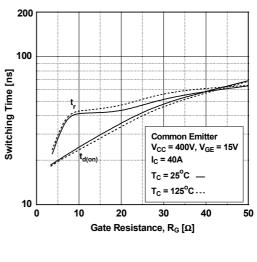
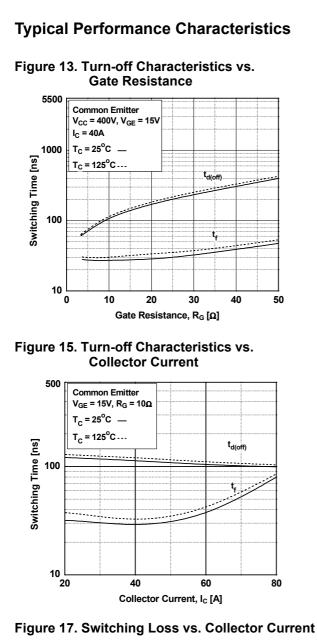
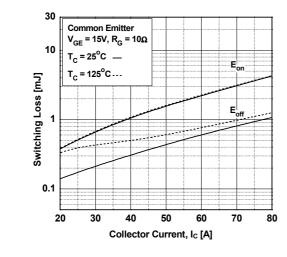


Figure 12. Turn-on Characteristics vs. **Gate Resistance**







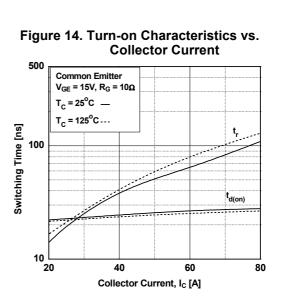
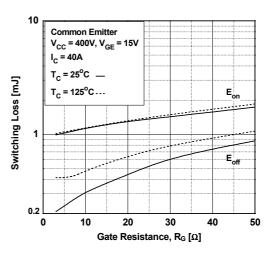
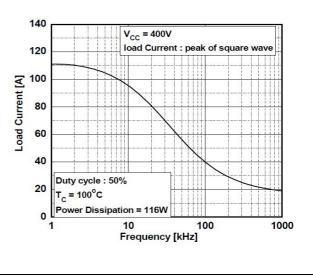


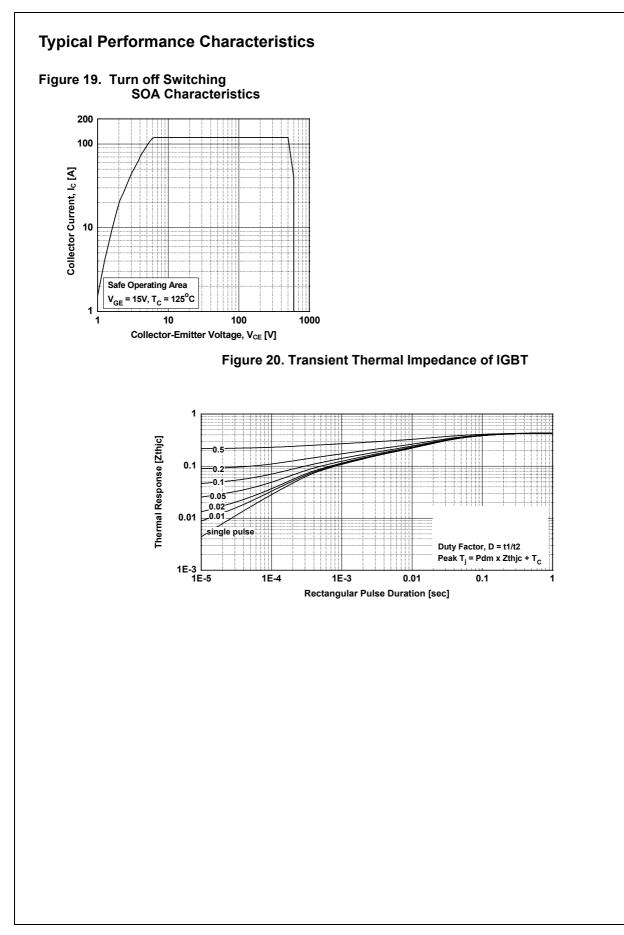
Figure 16. Switching Loss vs. Gate Resistance

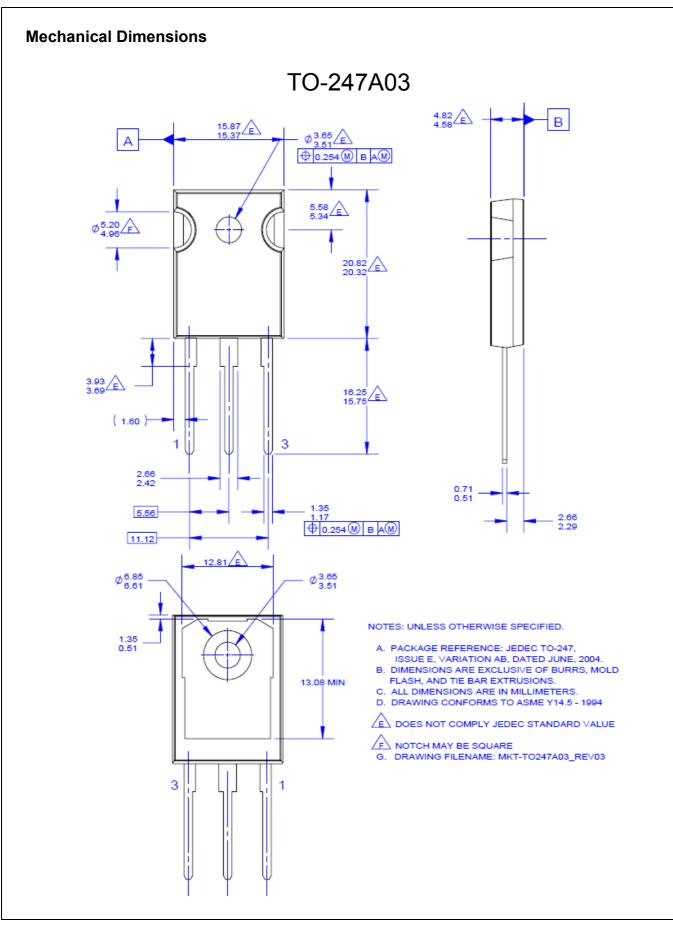






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