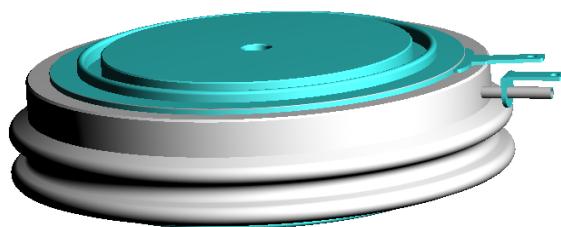


# GPTR4154



## PHASE CONTROLLED SCR

High reliability operation  
DC power supply  
AC drives

<b>VOLTAGE UP TO</b>	<b>3200 V</b>
<b>AVERAGE CURRENT</b>	<b>1540 A</b>
<b>SURGE CURRENT</b>	<b>18 kA</b>

## BLOCKING CHARACTERISTICS

Characteristic	Conditions	Value
V <sub>RRM</sub>	Repetitive peak reverse voltage	3200 V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage	3300 V
V <sub>DRM</sub>	Repetitive peak off-state voltage	3200 V
I <sub>DRM</sub>	Repetitive peak off-state current, max.	V <sub>DRM</sub> , single phase, half wave, T <sub>j</sub> = T <sub>jmax</sub> 70 mA
I <sub>IRRM</sub>	Repetitive peak reverse current, max.	V <sub>RRM</sub> , single phase, half wave, T <sub>j</sub> = T <sub>jmax</sub> 70 mA

## ON-STATE CHARACTERISTICS

I <sub>T(AV)</sub>	Average on-state current	Sine wave, 180° conduction, Th = 55 °C	1540 A
I <sub>T(RMS)</sub>	R.M.S. on-state current	Sine wave, 180° conduction, Th = 55 °C	2419 A
I <sub>TSM</sub>	Surge on-state current	Non rep. half sine wave, 50 Hz, V <sub>R</sub> = 0 V, T <sub>j</sub> = T <sub>jmax</sub>	18 kA
I <sup>2</sup> t	I <sup>2</sup> t for fusing coordination		1620 kA <sup>2</sup> s
V <sub>T(TO)</sub>	Threshold voltage	T <sub>j</sub> = T <sub>jmax</sub>	1.27 V
r <sub>T</sub>	On-state slope resistance	T <sub>j</sub> = T <sub>jmax</sub>	0.37 mΩ
V <sub>TM</sub>	Peak on-state voltage, max	On-state current I <sub>T</sub> = 3200 A, T <sub>j</sub> = 25 °C	2.16 V
I <sub>H</sub>	Holding current, max	T <sub>j</sub> = 25 °C	mA
I <sub>L</sub>	Latching current, typ	T <sub>j</sub> = 25 °C	mA

## TRIGGERING CHARACTERISTICS

V <sub>GT</sub>	Gate trigger voltage	T <sub>j</sub> = 25 °C, V <sub>D</sub> = 5 V	3 V
I <sub>GT</sub>	Gate trigger current	T <sub>j</sub> = 25 °C, V <sub>D</sub> = 5 V	400 mA
V <sub>GD</sub>	Non-trigger voltage	V <sub>D</sub> = 67% V <sub>RRM</sub> , T <sub>j</sub> = T <sub>jmax</sub>	V
P <sub>GM</sub>	Peak gate power dissipation	Pulse width 0.5 ms	W
P <sub>G(AV)</sub>	Average gate power dissipation		W
I <sub>FGM</sub>	Peak gate current		A
V <sub>FGM</sub>	Peak gate voltage (forward)		V
V <sub>RGM</sub>	Peak gate voltage (reverse)		V

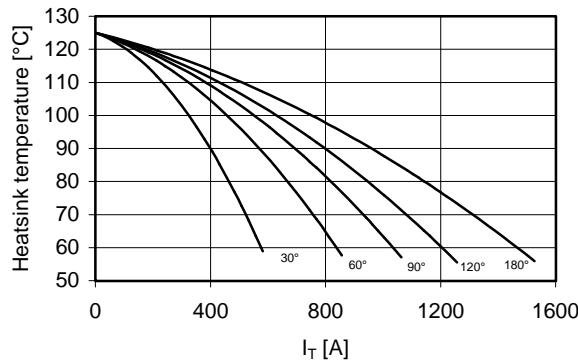
## SWITCHING CHARACTERISTICS

di/dt	Critical rate of rise of on-state current	T <sub>j</sub> = T <sub>jmax</sub>	200 A/μs
dV/dt	Critical rate of rise of off-state voltage	T <sub>j</sub> = T <sub>jmax</sub>	1000 V/μs
t <sub>q</sub>	Turn-off time, typ	T <sub>j</sub> = T <sub>jmax</sub> , I <sub>T</sub> = 800 A, di/dt = -12.5 A/μs VR = 100 V, VD = 67% V <sub>DRM</sub> , dV/dt = 20 V/μs	400 μs

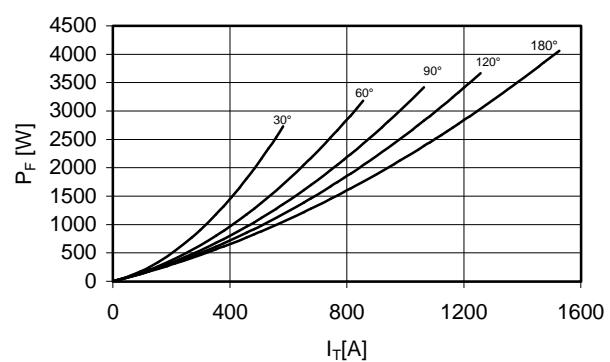
## THERMAL AND MECHANICAL CHARACTERISTICS

R <sub>th(j-c)</sub>	Thermal resistance (junction to case)	Double side cooled	0.016 °C/W
R <sub>th(c-h)</sub>	Thermal resistance (case to heatsink)	Double side cooled	0.001 °C/W
T <sub>jmax</sub>	Max operating junction temperature		125 °C
T <sub>stg</sub>	Storage temperature		-40 / 125 °C
F	Clamping force ± 10%		30 kN
	Mass		1.4 g

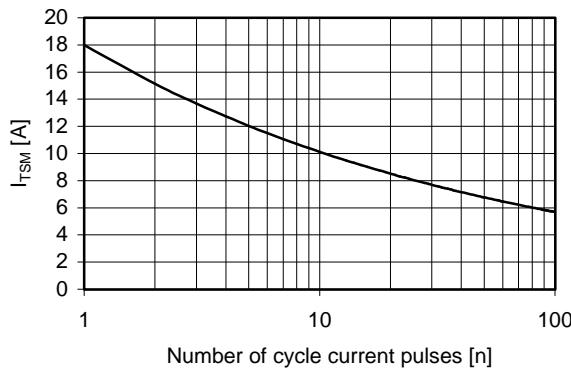
**Current rating - sine wave**



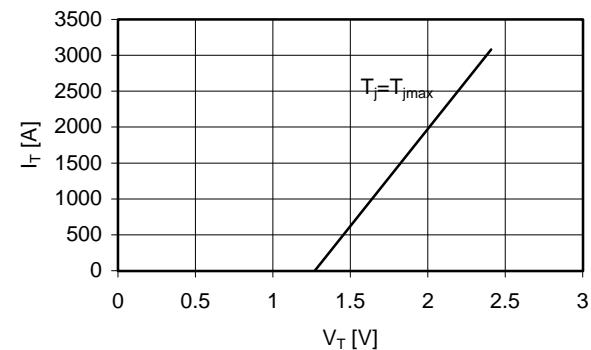
**Power loss - sine wave**



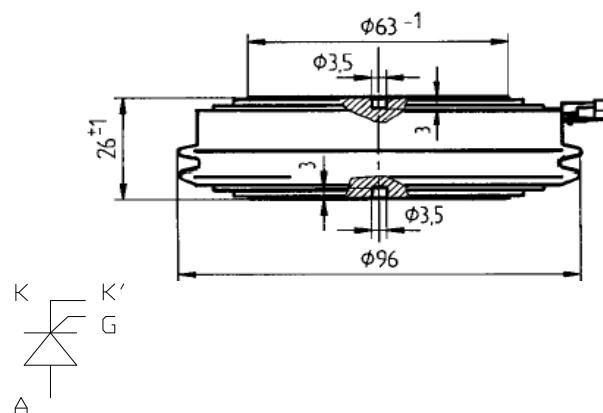
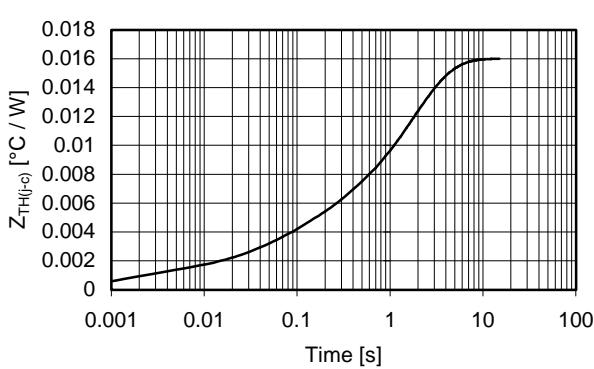
**Maximum surge current  
d.s. cooled**



**On-state voltage drop**



**Thermal impedance (j-c)**



In the interest of product improvement Green Power Semiconductors reserves the right to change any specification given in this data sheet without notice.