TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

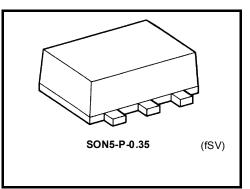
TC7SZ86AFS

2-Input EXCLUSIVE OR Gate

Features

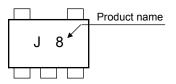
- High output current : ±24mA (min) at V_{CC} = 3.0V
- Super high speed operation : t_{pd} = 2.6ns (typ.)
 - at V_{CC} = 5 V, C_L = 50 pF
- Operating voltage range
- 5.5-V tolerant inputs.
- ESD performance
- : Machine model ≥ ±200 V Human body model ≥ ±2000 V

: V_{CC} = 1.65 to 5.5V



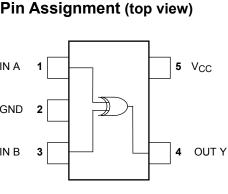
Weight: 0.001 g (typ)

Marking



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	Pin Assi
Supply voltage	V _{CC}	–0.5 to 6	V	
DC input voltage	V _{IN}	–0.5 to 6	V	
DC output voltage	V _{OUT}	–0.5 to V _{CC} +0.5	V	
Input diode current	lıĸ	-20	mA	GND 2
Output diode current	lок	±20 (Note1)	mA	
DC output current	lout	±50	mA	
DC VCC/ground current	Icc	±50	mA	
Power dissipation	PD	50	mW	
Storage temperature	T _{stg}	–65 to 150	°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

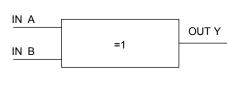
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Start of commercial production 2008-09

<u>TOSHIBA</u>

IEC Logic Symbol



А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	1.65 to 5.5	V
Supply voltage	VCC	1.5 to 5.5 (Note 2)	v
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 20 (V_{CC} = 1.80 V \pm 0.15V, 2.5 V \pm 0.2 V)	
Input rise time fall time	dt/dv	0 to 10 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V_{CC} = 5.0 V \pm 0.5 V)	

Note 2: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Tost	Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level	VIH			1.65 to 1.95	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$	_	_	V _{CC} × 0.75	_	v
input voltage				2.3 to 5.5	V _{CC} × 0.7			$V_{CC} \times 0.7$	_	
Low-level	VIL			1.65 to 1.95			V _{CC} ×0.25	_	V _{CC} ×0.25	v
input voltage	۷IL		_				$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	_	V _{CC} × 0.3	
				1.65	1.55	1.65	_	1.55	—	
			I _{OH} = −100 μA	2.3	2.2	2.3	_	2.2	—	
				3.0	2.9	3.0	_	2.9	—	
	Vон	V _{IN} = V _{IH} or V _{IL}		4.5	4.4	4.5	_	4.4	—	
High-level output voltage			I _{OH} = -4 mA	1.65	1.29	1.52		1.29	_	
			I _{OH} = -8 mA	2.3	1.9	2.15		1.9	_	
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
			I _{OH} = -32 mA	4.5	3.8	4.2		3.8	_	
	V _{OL} V _{IN} = V or V _{IL}		I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
				2.3		0	0.1	—	0.1	
				3.0	_	0	0.1	_	0.1	
				4.5	_	0	0.1	_	0.1	
Low-level output voltage		$V_{IN} = V_{IH}$ or V_{IL}		1.65	_	0.08	0.24	_	0.24	
		12	I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0		0.22	0.55		0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±1		±10	μA
Quiescent supply current	ICC	$V_{IN} = 5.5V \text{ or } GND$		5.5			2	_	20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Sv	Symbol	Test Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Characteristics Symbol		Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
			1.80 ± 0.15	1.0	6.4	11.5	1.0	12.0	- ns
		$C_{L} = 15 \text{ pF}, R_{L} = 1 \text{ M}\Omega$	$\textbf{2.5}\pm\textbf{0.2}$	0.8	3.8	8.0	0.8	8.5	
	t _{pLH} t _{pHL} -	Ο[= 15 pr, κ[= 1 Μις	$\textbf{3.3}\pm\textbf{0.3}$	0.5	3.0	5.7	0.5	6.0	
			5.0 ± 0.5	0.5	2.4	5.0	0.5	5.4	
		$C_L = 50 \text{ pF}, \text{ R}_L = 500 \Omega$	3.3 ± 0.3	1.2	3.5	6.2	1.2	6.5	
			5.0 ± 0.5	0.8	2.6	5.4	0.8	5.8	
Input capacitance	C _{IN}	—	0 to 5.5		4		—		pF
Power dissipation CPI	Con	C _{PD} (Note 3)	3.3		21	_	—	_	pF
	CPD		5.5		24	_	—	_	μr

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

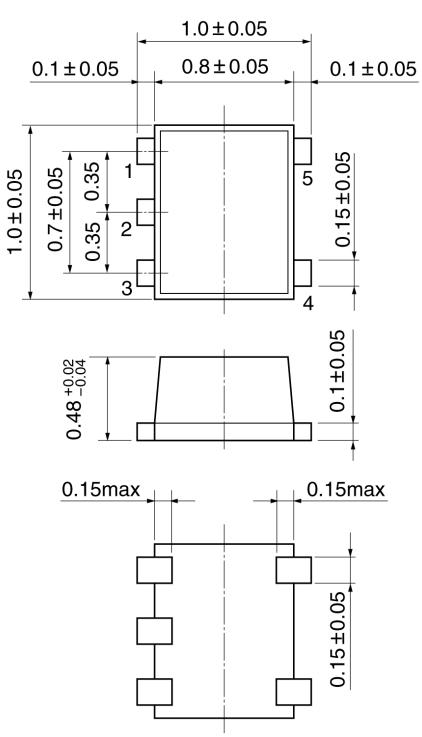
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit: mm



Weight: 0.001 g (typ.)

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