TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSVI)

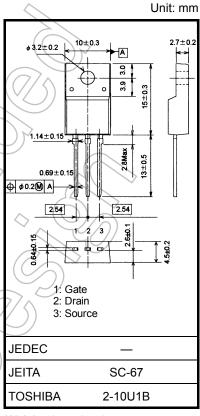
# 2SK3947

### **Switching Regulator Applications**

- Low drain-source ON-resistance:  $R_{DS}$  (ON) = 1.1  $\Omega$  (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 5.0 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A (V_{DS} = 600 V)$
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

## Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	600	V
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	600	À
Gate-source voltage		$V_{GSS}$	±30	> v
Drain current	DC (Note 1)	ΙD	6	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	24	A
Drain power dissipati	on (Tc = 25°C)	PD	40	<\w
Single-pulse avalanc	he energy (Note 2)	EAS	345	mJ
Avalanche current		TAR	6	A
Repetitive avalanche	energy (Note 3)	EAR	4	JmJ
Channel temperature		7 Ch	150	°C
Storage temperature	range	T <sub>stg</sub>	-55 to 150	>°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

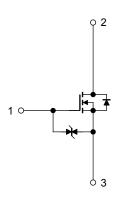
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 16.8 mH,  $I_{AR} = 6 \text{ A}$ ,  $R_G = 25 \Omega$ 

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

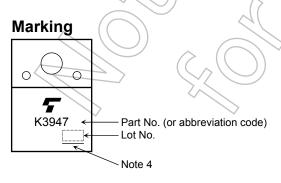


#### **Electrical Characteristics (Ta = 25°C)**

Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rent	I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cutoff curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	/_	_	100	μА
Drain-source bre	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_		٧
Gate threshold ve	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	)/_	4.0	٧
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A	//×	1.1	1.4	Ω
Forward transfer	admittance	Y <sub>fS</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	1.5	5.0	_	S
Input capacitance	е	C <sub>iss</sub>		_	1050	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	<sup>7</sup> —	10	_	pF
Output capacitance		C <sub>oss</sub>		_	110		
Switching time	Rise time	t <sub>r</sub>	10 V ID = 3 A VOUT		20	$\gamma$	
	Turn-on time	t <sub>on</sub>	0 V — R <sub>L</sub> = 66 Ω	((	40	) —	
	Fall time	t <sub>f</sub>	V <sub>DD</sub> ≈ 200 V	7	35	_	ns
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>w</sub> = 10 μs		130	_	
Total gate charge	9	Qg		) —	28		
Gate-source cha	rge	Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	16	_	nC
Gate-drain charge Q <sub>gd</sub>			_	12	_		

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	)) I <sub>DR</sub>			_	6	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	$( \vee / ) )$ $-$	_	_	24	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 6 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	tri	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	140	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs		0.3	_	μС

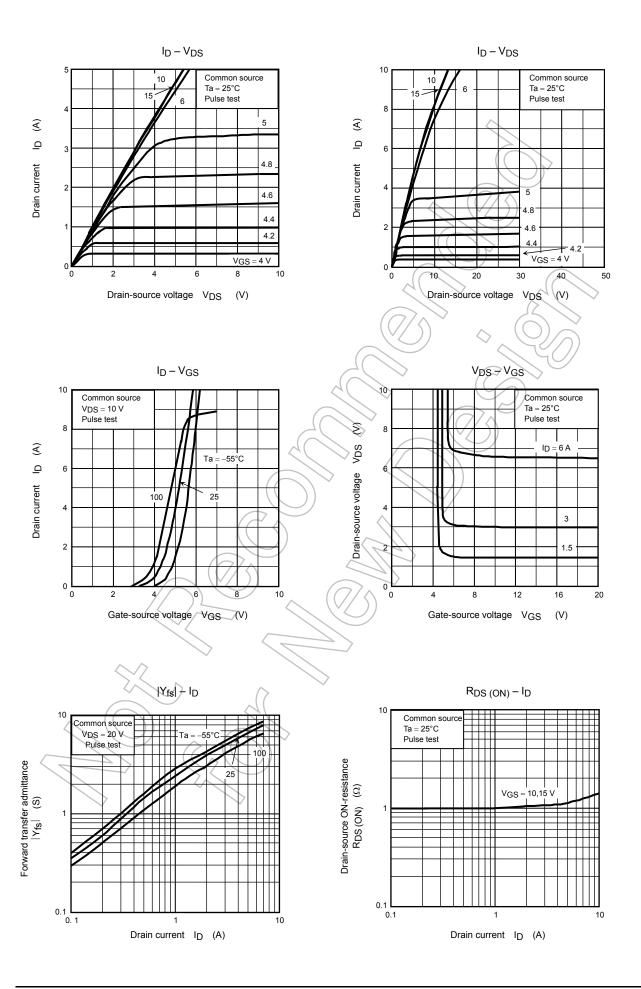


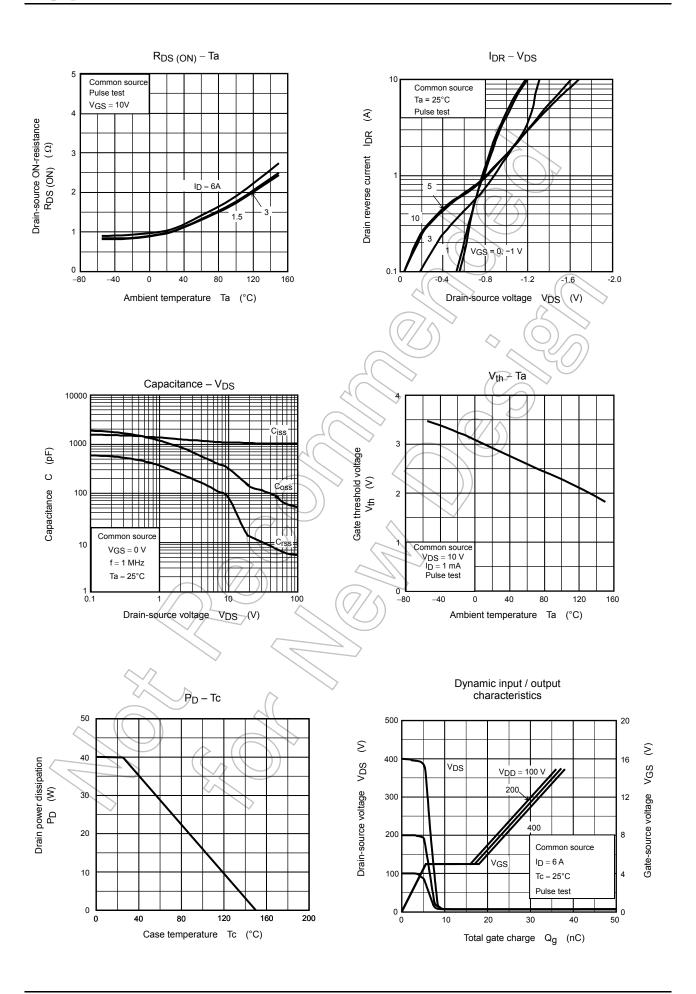
Note 4: A line under a Lot No. identifies the indication of product Labels.

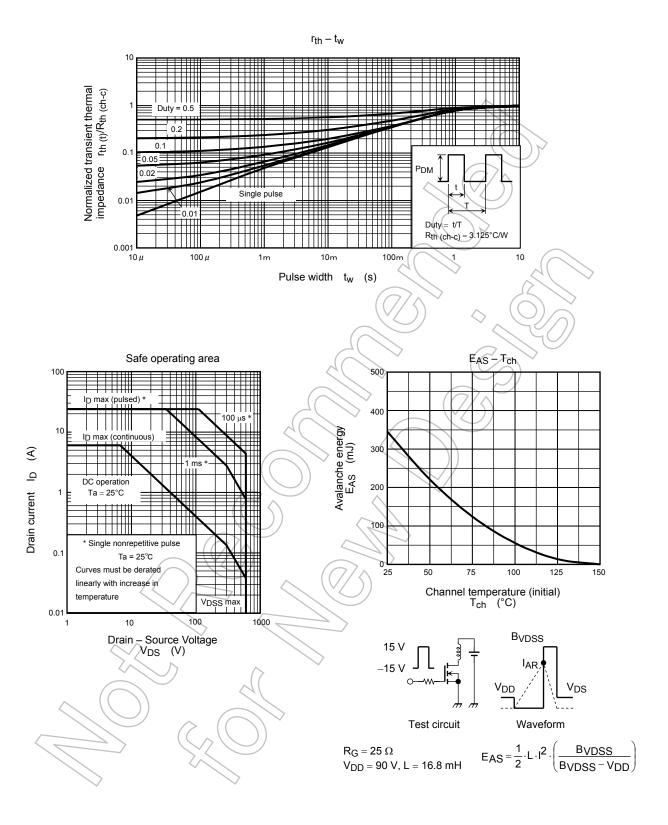
Not underlined: [[Pb]]/INCLUDES > MCV

 $\label{thm:compatible} \mbox{Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$ 

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.







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