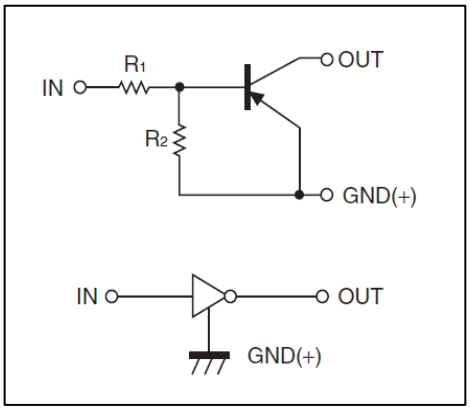
Digital Transistors (Built-in Resistors)

DTA113ZM/DTA113ZE/DTA113ZUA
DTA113ZKA /DTA113ZCA/DTA113ZSA

DIGITAL TRANSISTOR (PNP)

• Equivalent Circuit

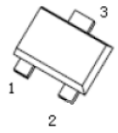


FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

PIN CONNENCTIONS and MARKING

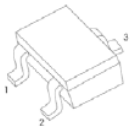
DTA113ZM **SOT-723**



1. IN
2. GND
3. OUT

MARKING:E11

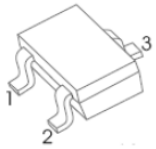
DTA113ZE **SOT-523**



1. IN
2. GND
3. OUT

MARKING:E11

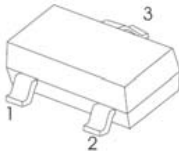
DTA113ZUA **SOT-323**



1. IN
2. GND
3. OUT

MARKING:E11

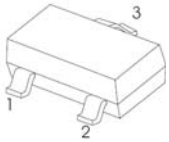
DTA113ZKA **SOT-23-3L**



1. IN
2. GND
3. OUT

MARKING:E11

DTA113ZCA **SOT-23**



1.IN
2.GND
3.OUT

MARKING:E11

DTA113ZSA **TO-92S**

1. GND
2. OUT
3. IN

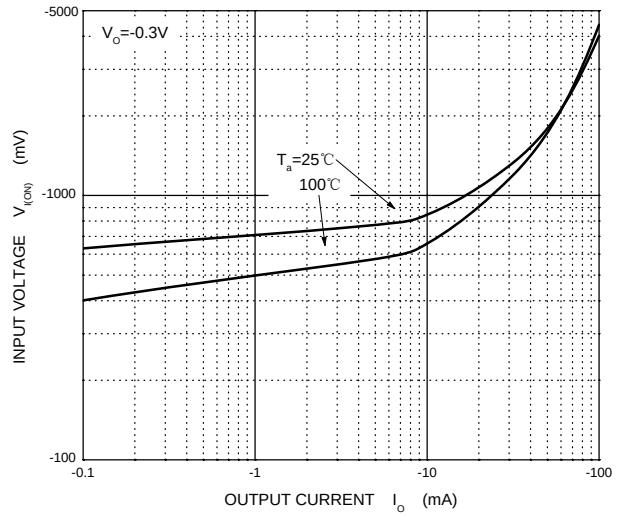
MAXIMUM RATINGS(Ta=25℃ unless otherwise noted)

Symbol	Parameter	Limits(DTA113Z□)						Unit
		M	E	UA	CA	KA	SA	
V_{CC}	Supply Voltage	-50						V
V_{IN}	Input Voltage	-10～+5						V
I_O	Output Current	-100						mA
P_D	Power Dissipation	100	150	200	200	200	300	mW
T_j	Junction Temperature	150						℃
T_{stg}	Storage Temperature	-55～+150						℃

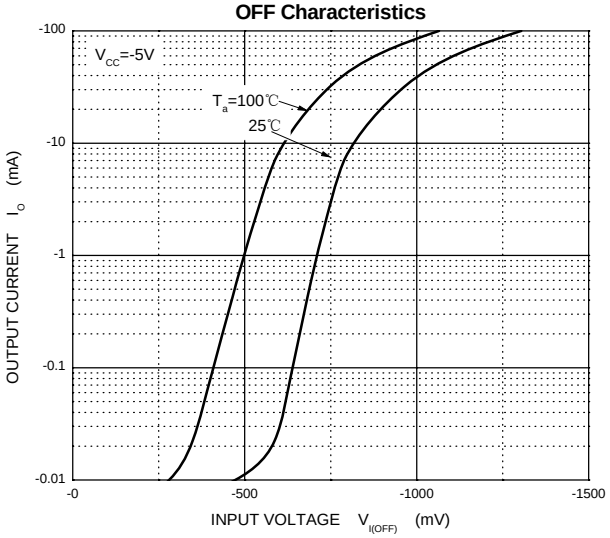
ELECTRICAL CHARACTERISTICS (Ta=25℃ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	V_{CC}=-5V,I_O=-100μA	-0.3			V
	$V_{I(on)}$	V_O=-0.3V,I_O=-20mA			-3	V
Output voltage	$V_{O(on)}$	I_O/I_I=-10mA/-0.5mA			-0.3	V
Input current	I_I	V_I=-5V			-7.2	mA
Output current	$I_{O(off)}$	V_{CC}=-50V,V_I=0			-0.5	μA
DC current gain	G_I	V_O=-5V,I_O=-5mA	33			
Input resistance	R_1		0.7	1	1.3	kΩ
Resistance ratio	R_2/R_1		8	10	12	
Transition frequency	f_T	V_O=-10V,I_O=-5mA,f=100MHz		250		MHz

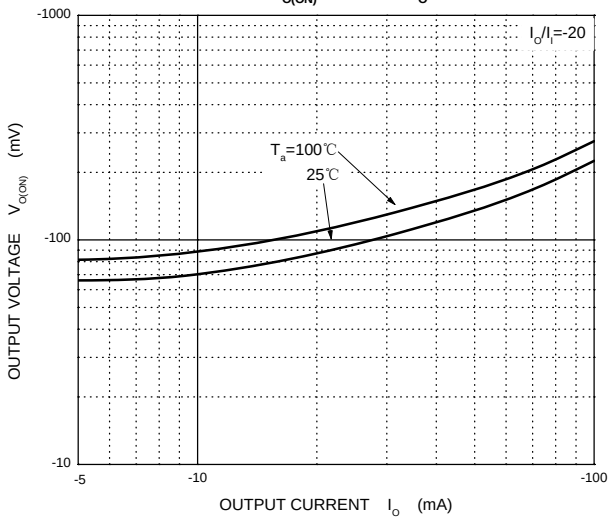
ON Characteristics



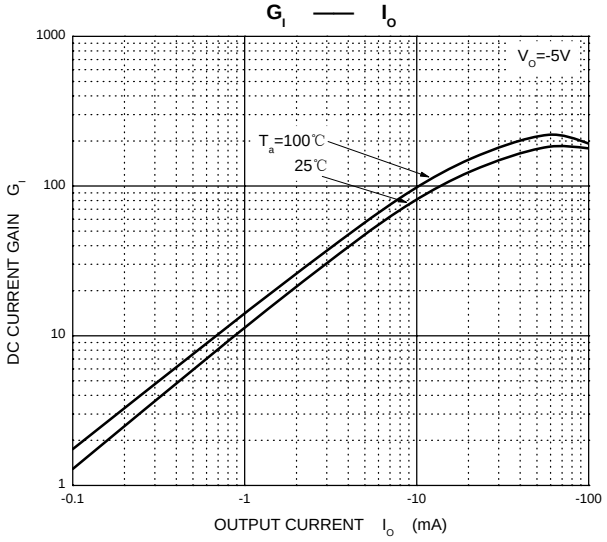
OFF Characteristics



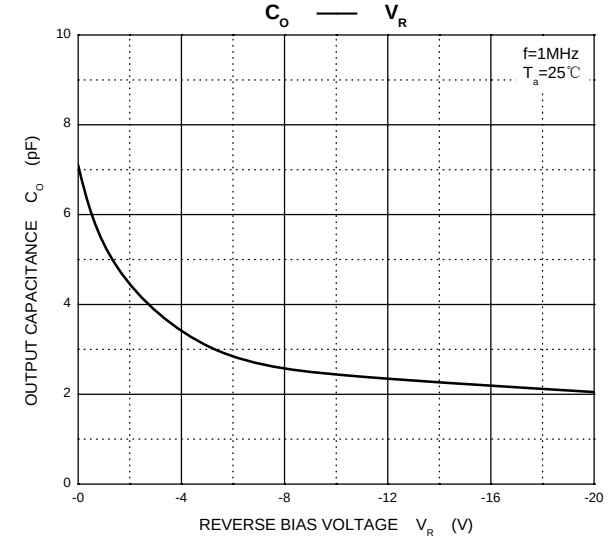
$V_{O(ON)}$ — I_O



G_I — I_O



C_O — V_R



P_D — T_a