



# 74LVC2G07

#### **DUAL BUFFERS with OPEN DRAIN OUTPUTS**

## Description

The 74LVC2G07 is a dual buffer gate with open drain outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The input is tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32mA.

## Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- -24mA Output Drive at 3.0V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115)
  - Exceeds 2000-V Human Body Model (A114)
  - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- DFN1409 package designed as a direct replacement for chip scale packaging.
- Range of Package Options SOT26, SOT353, DFN1010, DFN1409 and DFN1410
- Leadless packages per JESD30E
  - DFN1410 denoted as X2-DFN1410-6
  - DFN1409 denoted as X2-DFN1409-6
  - DFN1010 denoted as X2-DFN1010-6
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### Notes:

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

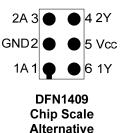
#### Click here for ordering information, located at the end of datasheet

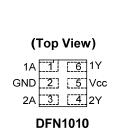
# Pin Assignments (Top View) (Top View) 1A 1 16 1Y 1A 1 17

GND [2] 15 Vcc 2A [3] 14 2Y

SOT26 / 363







GND 2 5 Vcc

2A 3 4 2Y

**DFN1410** 

# Applications

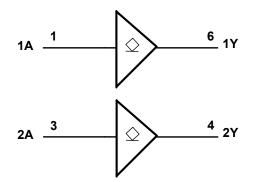
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, networking, notebooks, netbooks, tablets
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box
  - Cell Phones, Personal Navigation / GPS
  - MP3 players ,Cameras, Video Recorders



# **Pin Descriptions**

Pin Name	Pin NO.	Function			
1A	1	Data Input			
GND	2	Ground			
2A	3	Data Input			
2Y	4	Data Output Open Drain			
V <sub>CC</sub>	5	Supply Voltage			
1Y	6	Data Output Open Drain			

# Logic Diagram



# Function Table

Inputs	Output
Α	Y
Н	Z
L	L

# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +6.5	V
VI	Input Voltage Range	-0.5 to +6.5	V
Vo	Voltage applied to output in high impedance or IOFF state	-0.5 to +6.5	V
Vo	Voltage applied to output in high or low state	-0.3 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current VI < 0	-50	mA
Ι <sub>ΟΚ</sub>	Output Clamp Current V <sub>O</sub> < 0	-50	mA
lo	Continuous Output Current	-50	mA
	Continuous current through Vdd or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Symbol		Parameter	Min	Max	Unit	
M		Operating	1.65	5.5	V	
Vcc	Operating Voltage	Data retention only	1.5		V	
		V <sub>CC</sub> = 1.65V to 1.95V	$0.65 \times V_{CC}$			
Maria	High-Level Input Voltage	V <sub>CC</sub> = 2.3 V to 2.7V	1.7		V	
V <sub>IH</sub>		V <sub>CC</sub> = 3V to 3.6V	2		v	
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 X V <sub>CC</sub>			
		V <sub>CC</sub> = 1.65V to 1.95V		$0.35 \times V_{CC}$		
	Low-Level Input Voltage	V <sub>CC</sub> = 2.3V to 2.7V		0.7	V	
VIL		V <sub>CC</sub> = 3V to 3.6V		0.8		
		V <sub>CC</sub> = 4.5V to 5.5V		0.3 X V <sub>CC</sub>		
VI	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		V <sub>CC</sub> = 1.65V		4		
		V <sub>CC</sub> = 2.3V		8		
I <sub>OL</sub>	Low-Level Output Current	V <sub>CC</sub> = 3V		16	mA	
		VCC = 3V		24		
		V <sub>CC</sub> = 4.5V		32		
		V <sub>CC</sub> = 1.8V ± 0.15V, 2.5V ± 0.2V		20		
Δt/ΔV	Input transition rise or fall rate	$V_{CC} = 3.3V \pm 0.3V$		10	ns/V	
		$V_{CC} = 5V \pm 0.5V$		10		
TA	Operating free-air temperature		-40	125	°C	

# Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Note: 5. Unused inputs should be held at  $V_{CC}$  or Ground.

# **Electrical Characteristics**

Sumbal	Devemeter	Test Conditions	N N	-40°C to	o +85°C	-40°C to	+125°C	
Symbol	Parameter	Test Conditions	Vcc	Min	Max	Min	Max	Unit
		I <sub>OL</sub> = 100μA	1.65V to 5.5V		0.1		0.1	
		I <sub>OL</sub> = 4mA	1.65V		0.45		0.70	
		I <sub>OL</sub> = 8mA	2.3V		0.3		0.45	v
V <sub>OL</sub> Low Le	Low Level Output Voltage	I <sub>OL</sub> = 16mA	3V		0.4		0.60	V
		I <sub>OL</sub> = 24mA	3V		0.55		0.80	
		I <sub>OL</sub> = 32mA	4.5V		0.55		0.80	
lı	Input Current	V <sub>I</sub> = 5.5V or GND	0 to 5.5V		± 5		± 20	μA
loz	Z State Leakage Current	V <sub>O</sub> = 0 to 5.5V	3.6V		± 10		± 10	μA
I <sub>OFF</sub>	Power Down Leakage Current	$V_{\rm I}$ or $V_{\rm O}$ = 5.5V	0V		± 10		± 20	μA
Icc	Supply Current	$V_{I} = 5.5V \text{ or GND}, I_{O} = 0$	1.65V to 5.5V		10		40	μA
ΔI <sub>CC</sub>	Additional Supply Current	Input at V <sub>CC</sub> –0.6V	3V to 5.5V		500		5000	μA



Symbol	Parameter	Package	Conditions	Min	Тур	Max	Unit
Cı	Input Capacitance	Typical of all packages	Vcc = 3.3V $V_1 = V_{CC} - or GND$		3.5		pF
	θ <sub>JA</sub> Thermal Resistance Junction-to- Ambient	SOT26			204		
		SOT363	(Note 6)		371		°C/W
$\theta_{JA}$		X2-DFN1410-6			430		
	Ambient	X2-DFN1409-6			450		
		X2-DFN1010-6			510		
		SOT26			52		
		SOT363			143		
$\theta_{\rm JC}$	Thermal Resistance Junction-to- Case	X2-DFN1410-6	(Note 6)		190		°C/W
	Case	X2-DFN1409-6			225		
		X2-DFN1010-6			250		1

# Package Characteristics (@T<sub>A</sub> = +25°C, V<sub>CC</sub> = 3.3V unless otherwise specified.)

Note: 6. Test condition for SOT26, SOT363, X2-DFN1410-6, X2-DFN1409-6 and X2-DFN1010 -6: Device mounted on FR-4 substrate PC board, 2oz copper with minimum recommended pad layout.

# **Switching Characteristics**

T <sub>A</sub> =-40°C to +85°C	Γ <sub>A</sub> =-40°C to +85°C, C <sub>L</sub> = 30 or 50pF (see Figure 1)										
Parameter From (Input)		TO (OUTPUT)		1.8V 15V		= 2.5V ).2V		= 3.3V ).3V	: V <sub>CC</sub> ± 0		Unit
	(input)	(001101)	Min	Max	Min	Max	Min	Max	Min	Max	
t <sub>pd</sub>	A	Y	0.5	6.7	0.5	4.3	0.5	3.7	0.5	2.9	ns

#### **T<sub>A</sub> = -40°C to +125°C**, C<sub>L</sub> = 30 or 50pF (see Figure 1)

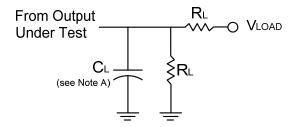
Parameter	From (Input)	TO (OUTPUT)		= 1.8V .15V		= 2.5V ).2V		= 3.3V ).3V	: V <sub>CC</sub> ± 0		Unit
	(input)	(001201)	Min	Max	Min	Мах	Min	Max	Min	Max	
t <sub>pd</sub>	А	Y	0.5	8.4	0.5	5.5	0.5	4.7	0.5	3.7	ns

# **Operating Characteristics**

T <sub>A</sub> = +25°C							
	Parameter		V <sub>CC</sub> = 1.8V	V <sub>CC</sub> = 2.5V	V <sub>CC</sub> = 3.3V	V <sub>CC</sub> = 5V	Unit
			Тур	Тур	Тур	Тур	Unit
C <sub>pd</sub>	Power dissipation capacitance	f = 10 MHz	3	3	4	6	pF

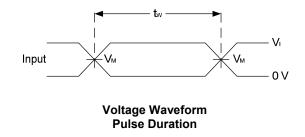


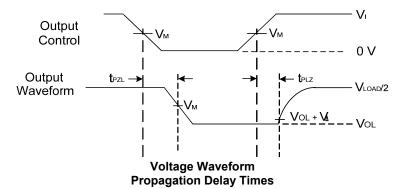
# **Parameter Measurement Information**



TEST	Condition
t <sub>PLZ</sub> (see Notes D and E)	Vload
t <sub>PZL</sub> (see Notes D and F)	Vload

N N	Inputs		V	N .	6		MA	
V <sub>cc</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	V <sub>LOAD</sub>	C∟	RL	VA	
1.8V±0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	1kΩ	0.15V	
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	500Ω	0.15V	
3.3V±0.3V	3V	≤2.5ns	1.5 V	6 V	50pF	500Ω	0.3V	
5V±0.5V	Vcc	≤2.5ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	50pF	500Ω	0.3V	





#### Figure 1. Load Circuit and Voltage Waveforms

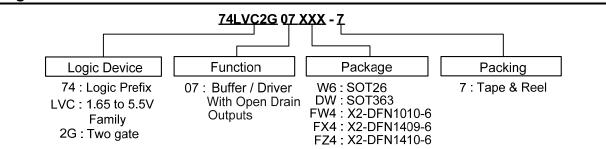
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device  $t_{PLZ}$  and  $t_{PZL}$  are the same as  $t_{PD}$ .
- E.  $t_{PZL}$  is measured at  $V_{M}$ .

F.  $t_{PLZ}\,$  is measured at V\_OL +V\_{\Delta}.



# **Ordering Information**

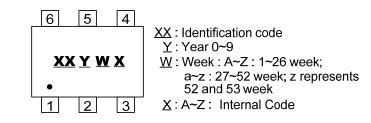


Dovice	Baakaga Cada	Packaging	7" Тар	e and Reel
Device	Package Code	(Note 7)	Quantity	Part Number Suffix
74LVC2G07W6-7	W6	SOT26	3000/Tape & Reel	-7
74LVC2G07DW-7	DW	SOT363	3000/Tape & Reel	-7
74LVC2G07FW4-7	FW4	X2-DFN1010-6	5000/Tape & Reel	-7
74LVC2G07FX4-7	FX4	X2-DFN1409-6	5000/Tape & Reel	-7
74LVC2G07FZ4-7	FZ4	X2-DFN1410-6	5000/Tape & Reel	-7

Note: 7. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

## **Marking Information**

#### (1) SOT26, SOT363



Part Number	Package	Identification Code
74LVC2G07W6	SOT26	Z4
74LVC2G07DW	SOT363	Z4

#### (2) X2-DFN1010-6, X2-DFN1409-6, X2-DFN1410-6

(Top View) XX Y: Year : 0~9 W: Week : A~Z : 1~26 week; a~z: 27~52 week; z represents 52 and 53 week X: A~Z : Internal code

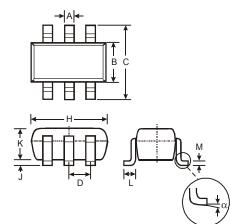
Part Number	Package	Identification Code
74LVC2G07FW4	X2-DFN1010-6	Z4
74LVC2G07FX4	X2-DFN1409-6	X4
74LVC2G07FZ4	X2-DFN1410-6	Z4



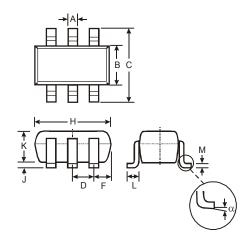
# Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(1) Package Type: SOT26



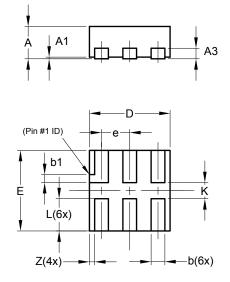
#### (2) Package Type: SOT363



SOT26			
Dim	Min	Max	Тур
Α	0.35	0.50	0.38
В	1.50	1.70	1.60
С	2.70	3.00	2.80
D			0.95
Н	2.90	3.10	3.00
J	0.013	0.10	0.05
κ	1.00	1.30	1.10
L	0.35	0.55	0.40
Μ	0.10	0.20	0.15
α	0°	8°	_
All Dimensions in mm			

	SOT363			
Dim	Min	Max	Тур	
Α	0.10	0.30	0.25	
в	1.15	1.35	1.30	
С	2.00	2.20	2.10	
D		0.65 Ty	р	
F	0.40	0.45	0.425	
н	1.80	2.20	2.15	
J	0	0.10	0.05	
κ	0.90	1.00	1.00	
L	0.25	0.40	0.30	
М	0.10	0.22	0.11	
α	0°	8°	-	
All Dimensions in mm				

#### (3) Package Type: X2-DFN1010-6



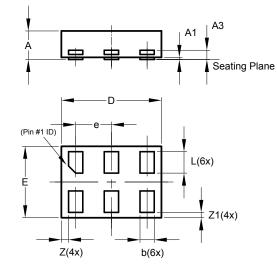
	X2-DFN1010-6		
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3		_	0.13
b	0.14	0.20	0.17
b1	0.05	0.15	0.10
D	0.95	1.05	1.00
Е	0.95	1.05	1.00
е		_	0.35
L	0.35	0.45	0.40
ĸ	0.15		
Z			0.065
All Dimensions in mm			



# Package Outline Dimensions (cont.) (All dimensions in mm.)

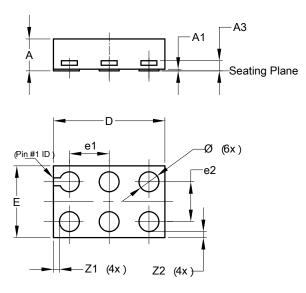
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### (4) Package Type X2-DFN1410-6



	X2-DFN1410-6		
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
E	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z			0.10
Z1	0.045	0.105	0.075
All	All Dimensions in mm		

#### (5) Package Type: X2-DFN1409-6 Chip Scale Replacement



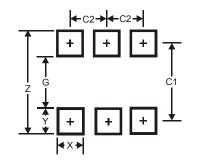
)	X2-DFN1409-6			
Dim	Min	Max	Тур	
Α	_	0.40	0.39	
A1	0	0.05	0.02	
A3			0.13	
Ø	0.20	0.30	0.25	
D	1.35	1.45	1.40	
E	0.85	0.95	0.90	
e1	_		0.50	
e2	_		0.50	
Z1	_	_	0.075	
Z2		_	0.075	
All D	All Dimensions in mm			



# Suggested Pad Layout

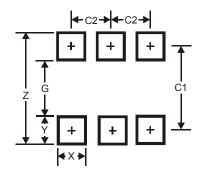
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### (1) Package Type: SOT26



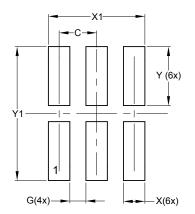
Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

#### (2) Package Type: SOT363



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

#### (3) Package Type: X2-DFN1010-6



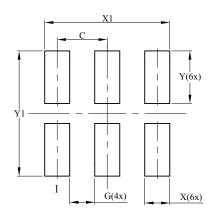
Dimensions	Value (in mm)
С	0.350
G	0.150
X	0.200
X1	0.900
Y	0.550
Y1	1.250



# Suggested Pad Layout (cont.)

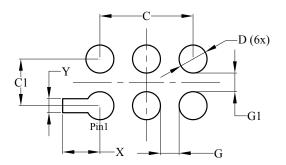
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### (4) Package Type X2-DFN1410-6



Dimensions	Value (in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250

#### (5) Package Type: X2-DFN1409-6 Chip Scale Replacement



Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
Х	0.400
Y	0.150



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