## GaAs SPST Switch DC-6 GHz

## Features

- Low Insertion Loss, 0.6 dB Typical @ 1 GHz
- Fast Switching Speed, 10 ns Typical
- Ultra Low DC Power Consumption
- Integral Static Protection


## Guaranteed Specifications** @25 ${ }^{\circ} \mathbf{C}^{* * *}$

| Frequency Range |  | DC -6000 MHz |  |
| :---: | :---: | :---: | :---: |
| Insertion Loss | (L) Low Loss | Low Loss Matched | (H) High Isolation |
| DC-1.0 GHz | 0.8 dB | 1.0 dB | 0.9 dB |
| DC-2.0 GHz | 0.9 dB | 1.1 dB | 1.0 dB |
| DC-6.0 GHz | 2.5 dB | 2.7 dB | 2.5 dB |
| Isolation | (L) Low Loss | Low Loss Matched | (H) High Isolation |
| DC-1.0 GHz | 30 dB | 63 dB | 64 dB |
| DC-2.0 GHz | 22 dB | 46 dB | 52 dB |
| DC-6.0 GHz | 11 dB | 14 dB | 19 dB |
| VSWR | (L) Low Loss | Low Loss Matched | (H) High Isolation |
| DC-1.0 GHz | $1.1: 1$ | $1.1: 1$ | $1.1: 1$ |
| DC-2.0 GHz | $1.3: 1$ | $1.2: 1$ | $1.1: 1$ |
| DC-6.0 GHz | $2.0: 1$ | $2.7: 1$ | $2.0: 1$ |

Operating Characteristics
Impedance $50 \Omega$ Nominal

| Switching Characteristics |  |  |
| :---: | :---: | :---: |
| Trise, Tfall ( $10 \% / 90 \%$ or $90 \% / 10 \%$ RF) |  | 10 ns Typ |
| Ton, Toff (50\% CTL to 90\%/10\% |  | 10 ns Typ |
| Transients (In-Band) |  | 10 mV Typ |
| Input Power for 1 dB Compression |  |  |
| Control Voltages (VDC) | 0/-5 | 0/-8 |
| Above 500 MHz | +27 dBm | +33 dBm Typ |
| 100 MHz | +21 dBm | +26 dBm Typ |
| Intermodulation Intercept Point (for two-tone input power up to +5 dBm ) |  |  |
| Intercept Points | IP2 | IP3 |
| Above 500 MHz | $+68 \mathrm{dBm}$ | +46 dBm Typ |
| 100 MHz | +62 dBm | +40 dBm Typ |


| Control Voltages (Complementary Logic) <br> Vin Low | 0 to $-0.2 \mathrm{~V} @ 20 \mu \mathrm{~A} \mathrm{Max}$ |
| :--- | ---: |
| Vin Hi | $-5 \mathrm{~V} @ 50 \mu \mathrm{~A}$ Typ to $-8 \mathrm{~V} @ 300 \mu \mathrm{~A} \mathrm{Max}$ |
| Die Size | $0.031 " \times 0.051 " \times 0.010 "$ |
|  | $(0.80 \mathrm{~mm} \times 01.30 \mathrm{~mm} \times 0.25 \mathrm{~mm})$ |

** Equivalent to Microelectronics Division (ANZAC) SW210H
** All specifications apply with $50 \Omega$ impedance connected to all RF ports, 0 and -8 VDC control voltages.
${ }^{* * *}$ Loss change $0.0025 \mathrm{~dB} /{ }^{\circ} \mathrm{C}$. (From $-55^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ )


## Typical Performance



Schematic


## Handling Precautions

Permanent damage to the MASW6020G may occur if the following precautions are not adhered to:
A. Cleanliness - The MASW6020G should be handled in a clean environment. DO NOT attempt to clean unit after the MASW6020G is installed.
B. Static Sensitivity - All chip handling equipment and personnel should be DC grounded.
C. Transient - Avoid instrument and power supply transients while bias is applied to the MASW6020G. Use shielded signal and bias cables to minimize inductive pick-up.
D. Bias - Apply voltage to either control port A/B or only when the other is grounded. Neither port should be allowed to "float."
E. General Handling - It is recommended that the MASW6020G chip be handled along the long side of the die with a sharp pair of bent tweezers. DO NOT touch the surface of the chip with fingers or tweezers.

## Mounting

The MASW6020G is back-metallized with $\mathrm{Pd} / \mathrm{Ni} / \mathrm{Au}(100 / 1,000 /$ $30,000 \AA$ ) metallization. It can be die-mounted with AuSn eutectic preforms or with thermally conductive epoxy. The package surface should be clean and flat before attachment.

Eutectic Die Attach:
A. A $80 / 20$ gold/tin preform is recommended with a work surface temperature of approximately $255^{\circ} \mathrm{C}$ and a tool temperature of $265^{\circ} \mathrm{C}$. When hot $90 / 10$ nitrogen/hydrogen gas is applied, tool tip temperature should be approximately $290^{\circ} \mathrm{C}$.
B. DO NOT expose the MASW6020G to a temperature greater than $320^{\circ} \mathrm{C}$ for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

Epoxy Die Attach:
A. Electrically conductive epoxy must be used.
B. Apply a minimum amount of epoxy and place the MASW6020G into position. A thin epoxy fillet should be visible around the perimeter of the chip.
C. Cure epoxy per manufacturer's recommended schedule.

## Wire Bonding

A. Ball or wedge bond with 1.0 mil diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of $150^{\circ} \mathrm{C}$ and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Ultrasonic energy and time should be adjusted to the minimum levels to achieve reliable wirebonds.
B. Wirebonds should be started on the chip and terminated on the package.

## Truth Table

| Option | Control Voltage |  | Switch Condition \& Bonding |  |  | Ground Bonds |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | A | B | RF1 | RF2 | ALT | GND1 | GND2 | Term |
|  | V Hi | V Low | on | on |  | G |  | G |
|  | V Low | V Hi | off | off |  | G |  | G |
|  | V Hi | V Low |  | on | on | G | G |  |
|  | V Low | $\checkmark \mathrm{Hi}$ |  | off | off | G | G |  |
|  | $\checkmark \mathrm{Hi}$ | V Low | on | on |  | G | G |  |
|  | V Low | V Hi | off | off |  | G | G |  |

## Maximum Ratings

| Control Voltage (A/B): | -8.5 VDC |
| :--- | :--- |
| Max Input RF Power: | +34 dBm <br> $(500 \mathrm{MHz}-4 \mathrm{GHz})$ |
| Storage Temperature: | $-65^{\circ} \mathrm{C}$ to $+175^{\circ} \mathrm{C}$ |
| Maximum Operating Temperature: | $+175^{\circ} \mathrm{C}$ |

## Bond Pad Dimensions - Inches (mm)

| RF1, RF2: | $0.004 \times 0.006(0.100 \times 0.150)$ |
| :---: | :--- |
| Alt RF: | $0.004 \times 0.005(0.100 \times 0.125)$ |
| A,B: | $0.004 \times 0.004(0.100 \times 0.100)$ |
| GND1: | $0.012 \times 0.007(0.300 \times 0.175)$ |
| GND2 : | $0.009 \times 0.008(0.225 \times 0.200)$ |
| Term: | $0.004 \times 0.008(0.100 \times 0.200)$ |

## Bond Pad Layout



