

## SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

### **LB1638MC** -

# Monolithic Digital IC Low-Voltage, Low-Saturation Bidirectional Motor Driver

#### Overview

The LB1638MC are low-saturation bidirectional motor driver ICs for use in low-voltage applications. At an IO of 500mA, they have a low saturation output of  $V_O(\text{sat}) = 0.75V$ . They are especially suited for use in compact motor of portable equipment.

#### **Features**

- Low voltage operation (2.5V min.)
- Low saturation voltage (upper transistor + lower transistor residual voltage; at  $I_O = 500 \text{mA}$ ,  $V_O(\text{sat}) = 0.75 \text{V typ.}$ )
- Low current drain at standby mode ( $I_{CCO} = 0.1 \mu A$  typ. or less)
- Separate logic power supply and motor power supply
- Brake function
- Built-in spark killer diodes

#### **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		-0.3 to +10.5	V
	V <sub>S</sub> max		-0.3 to +10.5	V
Output applied voltage	Vout		-0.3 to V <sub>CC</sub> + V <sub>SF</sub>	V
Input applied voltage	V <sub>IN</sub>		-0.3 to +10.0	V
Ground pin flow-out current	I <sub>GND</sub>		1.0	Α
Allowable power dissipation	Pd max	Mounted on a specified board	820	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

<sup>\*</sup> Specified board: 114.3mm  $\times$  76.1mm  $\times$  1.6mm, glass epoxy board.

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

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#### **LB1638MC**

#### Allowable Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage range	VCC		2.5 to 9.0	V
	٧s		2.2 to 9.0	V
Input high-level voltage	V <sub>IH</sub>		2.0 to 9.0	V
Input low-level	V <sub>IL</sub>		-0.3 to +0.7	V

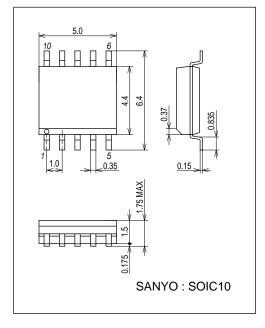
#### **Electrical Characteristics** at Ta = 25°C, $V_{CC} = 5V$

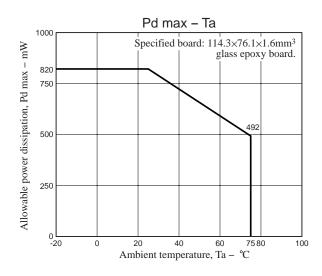
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Parameter	Symbol Conditions		min	typ	max	Unit	
Current drain	ICC0	V <sub>IN</sub> 1,2	ICC + IS			10	μА
	I <sub>CC</sub> 1	$V_{1N}1 = 3V, V_{1N}2 = 0V$	I <sub>CC</sub> + I <sub>S</sub>			20	mA
	I <sub>CC</sub> 2	V <sub>IN</sub> 1,2 = 3V	ICC + IS			40	mA
Output saturation voltage (upper + lower)	V <sub>OUT</sub> 1	I <sub>OUT</sub> = 200mA			0.25	0.5	V
	V <sub>OUT</sub> 2	I <sub>OUT</sub> = 500mA			0.70	1.3	V
Output pin voltage difference		I <sub>O</sub> = 200mA				0.1	V
Output sustain voltage	V <sub>O</sub> (sus)	I <sub>OUT</sub> = 500mA		9			V
Input current	I <sub>IN</sub>	V <sub>IN</sub> = 7V, V <sub>CC</sub> = 7V				0.5	mA
Spark killer diode							
Reverse current	I <sub>S</sub> (leak)	V <sub>CC</sub> , V <sub>S</sub> = 7V				10	μΑ
Forward voltage	V <sub>SF</sub>	I <sub>OUT</sub> = 200mA				1.7	٧

#### **Package Dimensions**

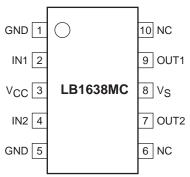
unit: mm (typ)

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#### **Pin Assignment**

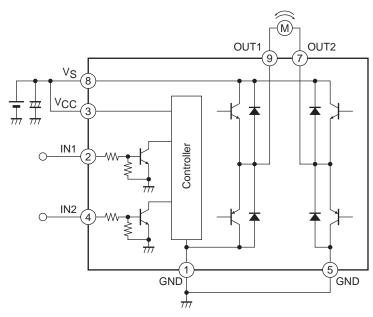


Note: both ground pins must be grounded.

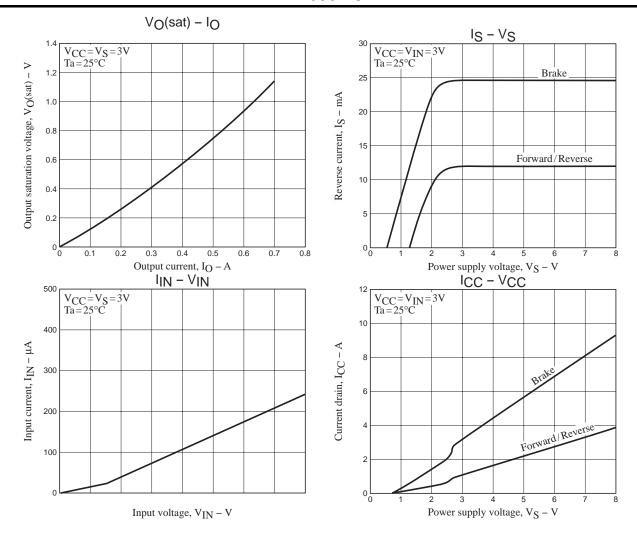
#### **Truth Table**

IN1	IN2	OUT1	OUT2	MOde
Н	L	Н	L	Forward
L	Н	L	Н	Reverse
Н	Н	L	L	Brake
L	L	OFF	OFF	Standby

#### **Block Diagram and Sample Application Circuit**



Note: When using the same power supply for  $V_S$  and  $V_{CC}$ , short the  $V_{CC}$  and  $V_S$  pins to each other or insert a capacitor in the  $V_{CC}$  line.



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