



**Automotive Display ICs** 

## TW8826

Ultra low cost, highly integrated LCD controller for analog panels

he TW8826 is a low cost highly integrated mutli-purpose LCD display solution with a high quality NTSC/PAL/SECAM 2D video decoder and a 2D De-interlacer/Scaler supporting analog panels. To reduce BOM cost, TW8826 integrates an 8-bit MCU and a CCFL controller. Through multiple input ports, TW8826 can directly display video and graphic content from a variety of devices including TV Tuners, DVD players, back-up cameras, DTV/DMB receivers and navigation/GPS receivers.

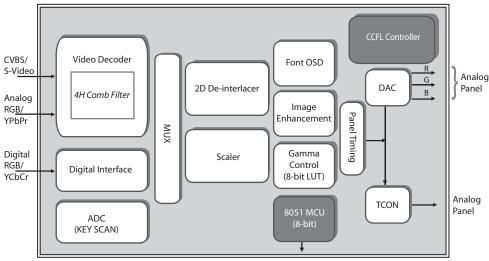
## **Target Applications**

- Navigation + DVD + TV
- Back up Camera
- Rear Seat Entertainment
- CarTV
- Portable DVD
- PMP
- Portable TV
- Door Phone
- Photo Frame
- DMBTV Baby Monitor
- Home Audio
- In Flight Entertainment

## **Key Features**

- Supports analog inputs including CVBS, S-Video, YPbPr & RGB signals and digital inputs including 24 bit RGB & 8/16/24 bit YCbCr. Interlaced and progressive ITU 656 inputs are supported
- Supports analog panels up to WQVGA resolutions
- Integrates cost saving features including a CCFL controller, charge pump booster, programmable panel offset control and on-chip 8 bit 8051 MCU with SPI interface
- Built-in 8 colors Font OSD with 202 ROM and programmable 227 RAM fonts. Supports Multi-color fonts by combining 3 single color fonts
- Embedded Image Enhancement
  - = Programmable CTI, hue, brightness, saturation, contrast & sharpness control
  - Black/White Stretch
  - = Programmable favorite color enhancement- up to three colors
  - = Programmable Gamma Correction tables

## **TW8826 Functional Block Diagram**



SPI Interface

#### **Order Information**

Part #	Name	Description	Pin Count	Body Size
TW8826	LQFP 128	Low Profile Quad Flat Package	128	14 x 20 mm^2



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## **Analog Video Decoder**

- NTSC (M, 4.43) and PAL (B, D, G, H, I, M, N, N combination),
  PAL (60), SECAM with automatic format detection
- Advanced synchronization processing for VCR trick play signal
- Three 10-bit ADCs and analog clamping circuit
- = Built-in analog anti-aliasing filter
- Fully programmable static gain or automatic gain control for the Y or CVBS channel
- Programmable white peak control for the Y or CVBS channel
- Software selectable analog inputs allows any of the following
  - Up to 4 composite video
  - = UP to 3 S-Video
  - Up to 2 analog YPbPr and RGB
- = 4-H adaptive comb filter Y/C separation
- = PAL delay line for color phase error correction
- Digital PLL for both color and horizontal locking
- Programmable hue, brightness, saturation, contrast, sharpness, Gamma control, and noise suppression
- Automatic color control and color killer

#### **Analog RGB / YPbPr input**

- Built-in sync processor for SOG support
- Built-in Line-locked PLL
- Built-in input measurement function
- Support directly sampling up to SVGA

#### 8/16/24-bits Digital RGB/YCbCr Inputs

- = Allows connection to 8/16/24-bit RGB/YCbCr
- Support both interlaced and progressive ITU 656

#### **TFT Panel Support**

- Supports a variety of Analog active matrix TFT panels
- Supports analog panel up to WQVGA (480 x 234), 20 MHz

#### **Font OSD**

- Built-in Font OSD with 202 ROM and programmable 227 RAM fonts. Supports Multi-color fonts by combining 3 single color fonts
- Multi-window (4) OSD support with color pallet
- = 16 font & window colors available
- Support OSD overlay with alpha blending

#### **Built-in Microcontroller**

- Supports external SPI Interface and I2C Master interface with GPIO
- Supports UART interface with GPIO
- Support IR or interrupt with GPIO

#### **CCFL Controller**

- Single channel CCFL controller based on push-pull architecture
- Lamp fault monitoring- Lamp Open, Lamp Over-current, Failure to Strike and Over-voltage
- Programmable Lamp Frequency to move EMI spurs out of band
- Analog or digital brightness control. 300:1 dimming range with the digital brightness control
- Low power stand-by mode

## **Image Processing & Enhancement**

- Programmable hue, brightness, saturation, and contrast controls
- Sharpness control with vertical peaking
- Programmable CTI control
- Built-in de-interlacing engine
- Independent RGB gain and offset controls
- Panorama / Water-glass scaling
- YCbCr hue adjustment
- Programmable Gamma correction tables

### **Power Management**

- Supports Panel power sequencing.
- Supports DPMS for monitor power management
- 1.8 / 3.3 V operation

#### Timing Controller (TCON)

- Support programmable interface signals for control
- Column (source) driver / row (gate) driver

#### Miscellaneous

- Supports 2-wire serial bus interface
- Temperature rating: -40 C to +85 C
- Spread spectrum PLL
- Low-speed ADC for KEY scan
- Programmable panel VCOM offset control
- = 5V tolerant I/O
- Power-down mode
- Typical power consumption < 500mW</li>
- Single 27MHz crystal

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