MORNSUN®

H_S-1W & G_S-1W Series 1W, FIXED INPUT, ISOLATED & UNREGULATED • 6000 VDC isolation SINGLE / DUAL OUTPUT



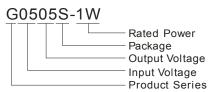


Patent Protected RoHS

FEATURES

- SIP package
- Operating temperature range: -40℃~+105℃
- Efficiency up to 80%
- Internal SMD construction
- No external component required
- Industry standard pinout

PART NUMBER SYSTEM



APPLICATIONS

The H_S-1W & G_S-1W Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage rang:±10%Vin;
- 2) 6000VDC input and output isolation;
- 3) Where do not has high requirement of line regulation and the ripple & noise of the output voltage;

Such as: digital circuit, low frequency analog circuit, and relay drive circuit.

Model	Input Voltage(VDC)	Output Voltage		Current nA)	Input Curren	t (mA,Typ.)	Reflected Ripple	Max. Capacitive	Efficiency	Approva
Wodel	Nominal (Range)	(VDC)	Max.	Min.	@Max. Load	@No Load	Current (mA,Typ.)	Load(µF)	(%, Typ.)	
H0503S-1W		3.3	303	30	278				72	
H0505S-1W		5	200	20	256				78	UL
H0509S-1W		9	111	12	253			220	79	UL
H0512S-1W		12	84	9	253			220	79	UL
H0515S-1W		15	67	7	253				79	UL
H0524S-1W	5 (4.5-5.5)	24	42	4	250	30	15		80	
30505S-1W	(4.0 0.0)	±5	±100	±10	256			100	78	_
G0507S-1W		±7.2	±70	±7	256				78	
G0509S-1W		±9	±56	±6	253				79	
30512S-1W		±12	±42	±5	253				79	_
30515S-1W		±15	±33	±4	253				79	
H1205S-1W		5	200	20	104		5	220	80	UL
H1207S-1W		7.2	139	14	104				80	
H1209S-1W		9	111	12	102				82	UL
H1212S-1W		12	84	9	103				81	UL
H1215S-1W	12	15	67	7	102	20			82	UL
G1205S-1W	(10.8-13.2)	±5	±100	±10	104	20	5		80	
G1207S-1W		±7.2	±70	±7	104				80	_
G1209S-1W		±9	±56	±6	102			100	82	_
G1212S-1W		±12	±42	±5	103				81	
G1215S-1W		±15	±33	±4	102				82	
H1505S-1W	45	5	200	20	82			220	80	
G1505S-1W	15 (13.5-16.5)	±5	±100	±10	82	15	5	100	80	
G1515S-1W	(10.0 10.0)	±15	±33	±4	81			100	81	
H2403S-1W		3.3	303	30	58				72	
12405S-1W	24	5	200	20	52	1		220	80	
H2412S-1W	(21.6- 26.4)	12	84	9	52	10	5	220	80	_
H2415S-1W	(21.0-20.4)	15	67	7	52				80	
G2412S-1W		±12	±42	±5	52			100	80	_

INPUT SPECIFICATIONS						
Item	Test Conditions	Min.	Тур.	Max.	Unit	
	5VDC Input	-0.7		9	VDC	
Input Surge Voltage	12VDC Input	-0.7		18		
(1 sec. max.)	15VDC Input	-0.7		21		
	24VDC Input	-0.7		30		
Input Filter			Capacitor			

Item	Test Conditions	Min.	Тур.	Max.	Unit	
Output Voltage Accuracy		See tolerance envelope curve				
Line Regulation	For Vin change of ±1%				±1.2	-
	10% to 100% load	3.3VDC output		15		%
		5VDC output		12		
Load Population		7.2&9VDC output		8		
Load Regulation		12VDC output		7		
		15VDC output		6	7	
		24VDC output		5		
Temperature coefficient	100% load		/	A -	±0.03	%/°C
Dinale & Maise*	20MHz Bandwidth Output Voltage ≤12VDC Output Voltage :15VDC, 24VDC	Output Voltage ≤12VDC	_	100	-	m)/n n
Ripple & Noise*			150)	mVp-p	
Short Circuit Protection			Continuous,	automatic reco	very	

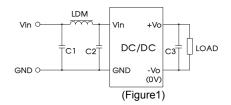
COMMON SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Isolation Voltage	Input-Output, tested for 1 minute and leakage current less than 1 mA	6000			VDC		
Isolation Resistance	Input-Output, test at 500VDC	1000			ΜΩ		
Isolation Capacitance	Input-Output, 100KHz/0.1V		10		pF		
Switching Frequency	Full load, nominal input		50		KHz		
MTBF	MIL-HDBK-217F@25°C	3500			K hours		
Case Material			Plastic (UL94-V0)			
Weight			4.2		g		

ENVIRONMENTAL SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Storage Humidity Non condensing				95	%		
Operating Temperature	Power derating (≥100°C, see Figure 2)	-40		105			
Storage Temperature		-55		125	°C		
Temperature rise	Ta=25°C,100% Load		25		C		
Lead Temperature	1.5mm from case for 10 seconds			300			
Cooling			Free air convection				

EMC SPECIFICATIONS						
ENAL.	CE	CISPR22/EN55022 CLASS B(Typical Recommended Circuit Refer to Figure1)				
EMI	RE	CISPR22/EN55022 CLASS B (Typical Recommended Circuit Refer to Figure 1)				
EMS	ESD	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B				

EMC RECOMMENDED CIRCUIT

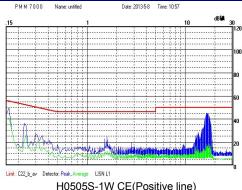
EMI Typical Recommended Circuit (CLASS B):



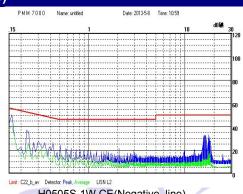
Recommended typical circuit parameters:

Input	voltage (V)	5/12/15/24
	C1, C2	4.7µF /50V
EMI	C3	Refer to the Cout in Fig.3
	LDM	6.8µH

EMC TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1)

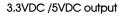


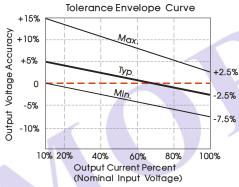
H0505S-1W CE(Positive line)



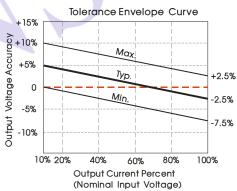
H0505S-1W CE(Negative, line)

PRODUCT TYPICAL CURVE



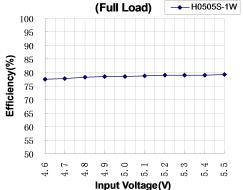


Other output



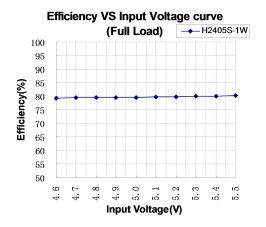
Temperature Derating Graph . Percent(%) 100 80 60 Safe Operating Area 40 20 0└ -40 80 100 105 40 Ambient Temp.(℃) (Figure 2)

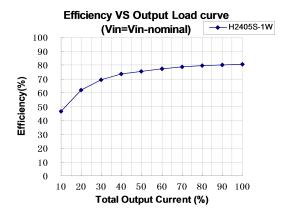
Efficiency VS Input Voltage curve



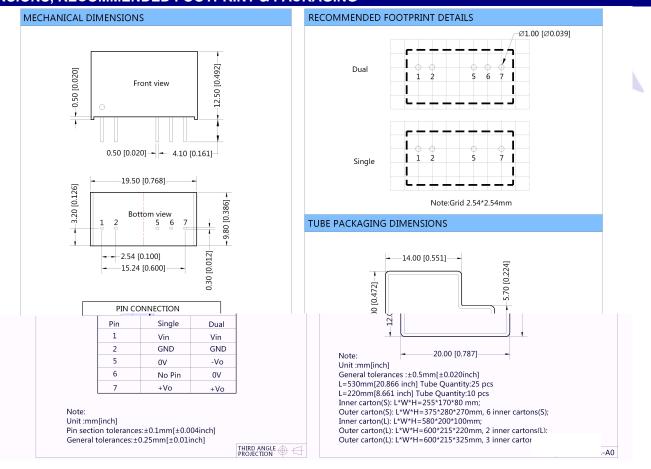
Efficiency VS Output Load curve → H0505S-1W (Vin=Vin-nominal) 100 90 80 70 60

Efficiency(%) 50 40 30 20 10 10 20 30 40 50 60 70 80 90 100 **Total Output Current (%)**





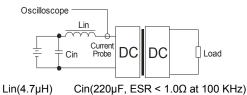
DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING



TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate the source impedance .



DESIGN CONSIDERATIONS

1) Requirement for output load

To ensure this module can operate efficiently and reliably, the minimum output load could not be less than 10% of the full load. If the actual output power is very small, please connect a resistor to the output in parallel to increase the load.

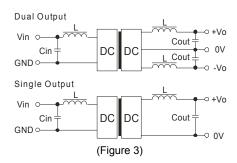
2) Overload Protection

Under normal operating conditions, the output circuit of these products have not overload protection. The simplest method is to add a breake circuit in the circuit.

3) Recommended circuit

If you want to further decrease the input/output ripple, an capacitor filtering network may be connected to the input and output ends of the DC/DC converter, refer to Figure 3.

It should also be noted that the capacitance of the capacitor must be proper. If the capacitance is too large, a startup problem might arise. For ensuring every channel of output can provide a safe and reliable operation, the recommended capacitance of the capacitor refer to Table 1.



EXTERNAL CAPACITOR TABLE (Table 1)

				,	
Vin	Cin	Single Vout	Cout	Dual Vout	Cout
(VDC)	(µF)	(VDC)	(µF)	(VDC)	(µF)
5	4.7	3.3	10		-
5	4.7	5	10	±5	4.7
12	2.2	9	4.7	±9	2.2
15	2.2	12	2.2	±12	2.2
24	1	15	1	±15	1

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

- 4) The input and the output of the product are recommended to be connected to ceramic capacitor or electrolytic capacitor. Using tantalum capacitor may cause risk of failure.
- 5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
- 2. Max. Capacitive Load is tested at nominal input voltage and full load.
- 3. Unless otherwise noted, All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load.
- 4. In this datasheet, all test methods are based on our corporate standards.
- 5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
- 6. Please contact our technical support for any specific requirement.
- 7. Specifications of this product are subject to changes without prior notice.

MORNSUN Science & Technology Co.,Ltd.

Address: No. 5, Kehui St. 1, Kehui development center, Science Ave., Guangzhou Science City, Luogang district, Guangzhou, P.R. China.

Tel: 86-20-38601850 Fax:86-20-38601272 E-mail: info@mornsun.cn Http://www.mornsun-power.com