

To all our customers

Regarding the change of names mentioned in the document, such as Hitachi Electric and Hitachi XX, to Renesas Technology Corp.

The semiconductor operations of Mitsubishi Electric and Hitachi were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Hitachi, Hitachi, Ltd., Hitachi Semiconductors, and other Hitachi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Renesas Technology Home Page: <http://www.renesas.com>

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
2. Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein.
The information described here may contain technical inaccuracies or typographical errors. Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (<http://www.renesas.com>).
4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.

HA12231FP

Audio Signal Processor for Car Deck (PB 1 Chip)



ADE-207-327A (Z)

2nd Edition
Jan. 2001

Description

HA12231FP is silicon monolithic bipolar IC providing PB equalizer system and music sensor system in one chip.

Functions

- PB equalizer × 2 channel
- Music sensor × 1 channel
- Line amp. × 2 channel
- Line mute × 2 channel

Features

- No use external parts for PB equalizer. (Fixed characteristics built-in)
- Available to change music sensing level by external resistor.
- Available to change frequency response of music sensor by external capacitor.
- Different type of PB equalizer characteristics selection (120 μ s/70 μ s) is available.
- Line mute ON/OFF is available.
- This IC is strong for a cellular phone noise.

Ordering Information

Product	Package	PBOUT-Level	Functions		
			PB-EQ	Music Sensor	Mute
HA12231FP	FP-20DA	450 mVrms	○	○	○

Pin Description, Equivalent Circuit

($V_{CC} = 9\text{ V}$, A system of single supply voltage, $T_a = 25^\circ\text{C}$, No Signal,
The value in the table shows typical value.)

Pin No.	Pin Name	Note	Equivalent Circuit	Description
16	TAI(L)	$V = V_{CC}/2$		Tape input
5	TAI(R)			
14	RIP	$V = V_{CC}/2$		Ripple filter
13	MS DET	$V = V_{CC}$		Time constant pin for rectifier
15	PBOUT(L)	$V = V_{CC}/2$		PB output
6	PBOUT(R)			
1	VREF	$V = V_{CC}/2$		Reference output
17	EQOUT(L)	$V = V_{CC}/2$		Equalizer output (120 μ)
4	EQOUT(R)			

Note: MS: Music Sensor

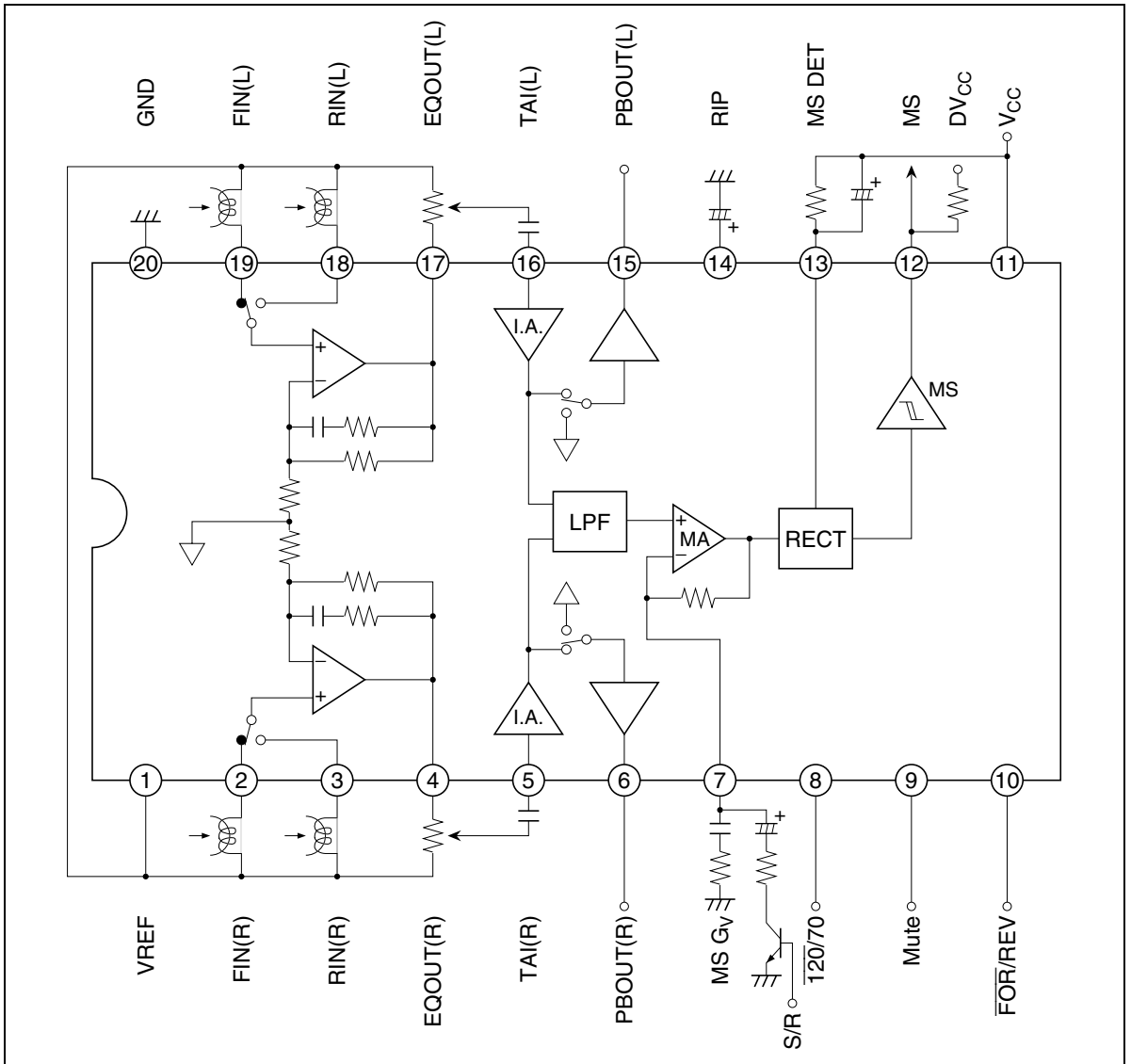
Pin Description, Equivalent Circuit

($V_{CC} = 9\text{ V}$, A system of single supply voltage, $T_a = 25^\circ\text{C}$, No Signal, The value in the table shows typical value.) (cont.)

Pin No.	Pin Name	Note	Equivalent Circuit	Description
11	V_{CC}	—		Power supply
19	FIN(L)	—		Equalizer input
18	RIN(L)			
3	RIN(R)			
2	FIN(R)			
9	Mute	—		Mode control input
10	FOR/REV			
8	120/70			
12	MS	—		MS output (to MPU) *
7	$MS G_v$	$V = V_{CC}/2$		MS gain pin *
20	GND	—		GND pin

Note: MS: Music Sensor

Block Diagram



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Rating	Unit	Note
Supply voltage	V _{cc} Max	15	V	
Power dissipation	Pd	400	mW	Ta ≤ 85°C
Operating temperature	Topr	-40 to +85	°C	
Storage temperature	Tstg	-55 to +125	°C	

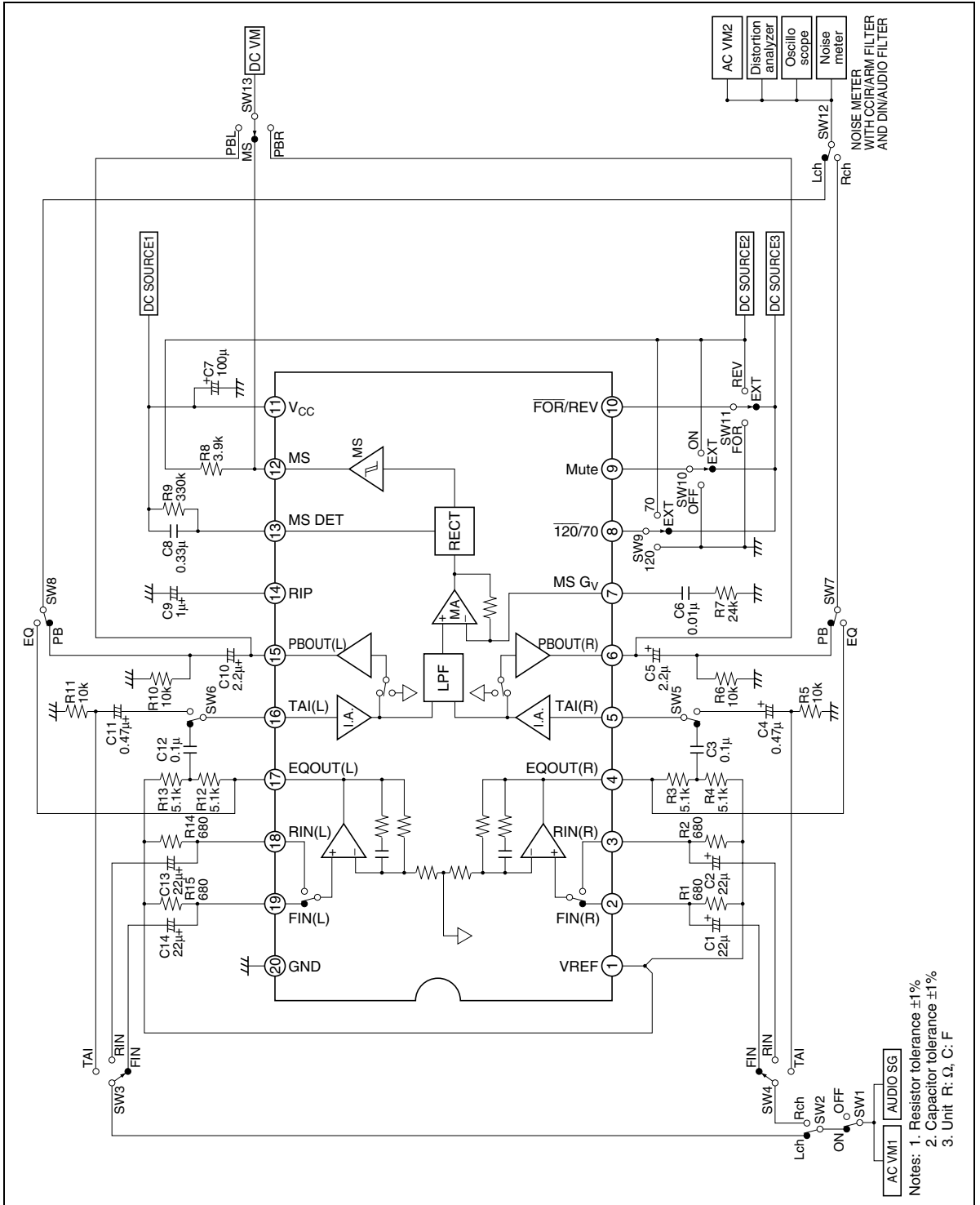
Electrical Characteristics

(Ta = 25°C, V_{CC} = 9 V, PBOULT Level = 450 mVrms (= 0 dB))

Item	Symbol	Test Condition						Specification				Application Terminal				Re-mark			
		IC Condition			Other			Min	Typ	Max	Input		Output		R		L	R	L
		INPUT	OUTPUT	fin(Hz)	PBOULT level(dB)	Other	R				L	R	L						
Quiescent current	I _Q	—	—	—	—	No signal	—	6.0	9	—	—	—	—	—	—	—	—	11	
Input AMP gain	G _v /A	TAI	PBOUT	1k	0	—	22.5	23.5	24.5	dB	5	16	6	15	—	—	—	—	
Signal handling	V _{omax}	TAI	PBOUT	1k	—	THD = 1%	12.0	13.0	—	dB	5	16	6	15	—	—	—	*1	
T.H.D.	THD	TAI	PBOUT	1k	0	—	—	0.05	0.3	%	5	16	6	15	—	—	—	—	
Channel separation	CT RL	FIN	PBOUT	1k	12	—	50.0	60.0	—	dB	2	19	6	15	6	15	—	—	
PB-EQ gain	G _v EQ 1k	FIN/RIN	EQOUT	1k	0	120μs	37.0	40.0	43.0	dB	2/3	19/18	4	17	—	—	—	—	
	G _v EQ 10k(1)	FIN	EQOUT	10k	0	120μs	33.0	36.0	39.0	dB	2	19	4	17	—	—	—	—	
	G _v EQ 10k(2)	FIN	EQOUT	10k	0	70μs	29.0	32.0	35.0	dB	2	19	4	17	—	—	—	—	
PB-EQ maximum output	V _{OM}	FIN/RIN	EQOUT	1k	—	THD = 1%	300	600	—	mVrms	2/3	19/18	4	17	—	—	—	*1	
PB-EQ THD	THD-EQ	FIN/RIN	EQOUT	1k	0	—	—	0.1	0.5	%	2/3	19/18	4	17	—	—	—	—	
Noise voltage level converted in input	V _N	FIN/RIN	EQOUT	(1k)	(0)	Rg = 680Ω, Din-Audio Filter	—	1.2	2.0	μVrms	2/3	19/18	4	17	—	—	—	—	
MS sensing level	V _{ON}	TAI	PBOUT MSOUT	5k	—	—	-18.0	-14.0	-10.0	dB	5	16	6	15	12	—	—	—	
MS output low level	V _{OL}	TAI	PBOUT MSOUT	5k	0	—	—	1.0	1.5	V	5	16	6	15	12	—	—	—	
MS output leak current	I _{OH}	—	MSOUT	—	—	No signal	—	0.0	2.0	μA	—	—	12	12	—	—	—	—	
MUTE attenuation	Mute	TAI	PBOUT	1k	12	—	70.0	80.0	—	dB	5	16	6	15	—	—	—	—	
Control voltage	V _{IL}	—	—	—	—	—	-0.2	—	1.0	V	—	—	—	—	—	—	—	8, 9,	
	V _{IH}	—	—	—	—	—	3.5	—	V _{CC}	—	—	—	—	—	—	—	—	10	

Note: 1. V_{CC} = 7.2 V

Test Circuit



Functional Description

Power Supply Range

HA12231FP is designed to operate on single supply only.

Table 1 Supply Voltage Range

Product	Single Supply
HA12231FP	7.2 V to 12.0 V

Reference Voltage

HA12231FP provides the reference voltage of half the supply voltage that is the signal grounds. As the peculiarity of this device, the capacitor for the ripple filter is very small about 1/100 compared with their usual value. The block diagram is shown as figure 1.

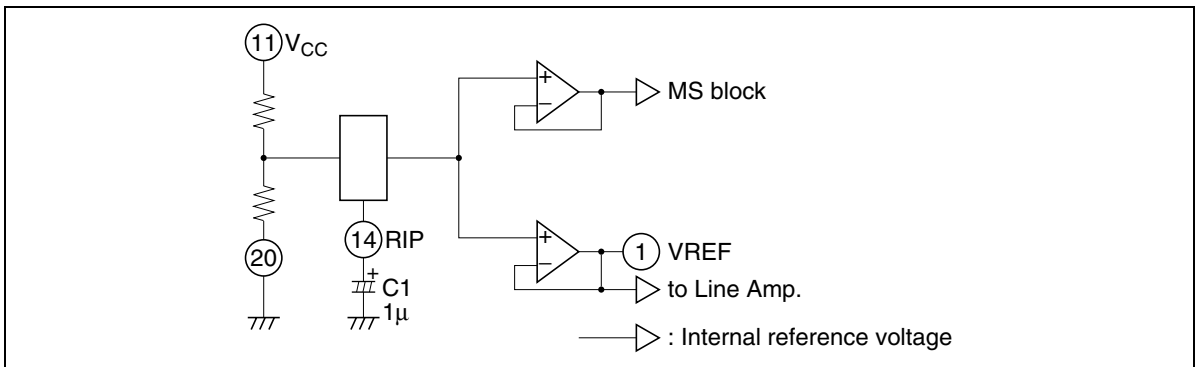


Figure 1 The Block Diagram of Reference Supply Voltage

Operating Mode Control

HA12231FP provides fully electronic switching circuits. And each operating mode control are controlled by parallel data (DC voltage).

When a power supply of this IC is cut off, for a voltage, in addition to a mode control terminal even though as do not destruct it, in series for resistance.

Table 2 Threshold Voltage (V_{TH})

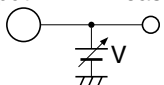
Pin No.	Lo	Hi	Unit	Test Condition
8, 9, 10	-0.2 to 1.0	3.5 to V_{CC}	V	Input Pin Measure 

Table 3 Switching Truth Table

Pin No.	Pin Name	Low	High
8	$\overline{120/70}$	120 μ (Normal)	70 μ (Metal or Chrome)
9	Mute	Mute OFF	Mute ON
10	$\overline{FOR/REV}$	Forward	Reverse

- Notes:
1. Each pins are on pulled down with 100 k Ω internal resistor. Therefore, it will be low-level when each pins are open.
 2. Over shoot level and under shoot level of input signal must be the standardized. (High: V_{CC} , Low: -0.2 V)
 3. Reducing pop noise is so much better for 10 k Ω to 22 k Ω resistor and 1 μ F to 22 μ F capacitor shown figure 2.

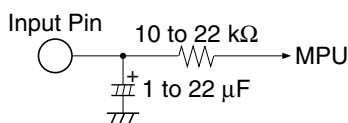


Figure 2 Interface for Reduction of Pop Noise

Input Block Diagram and Level Diagram

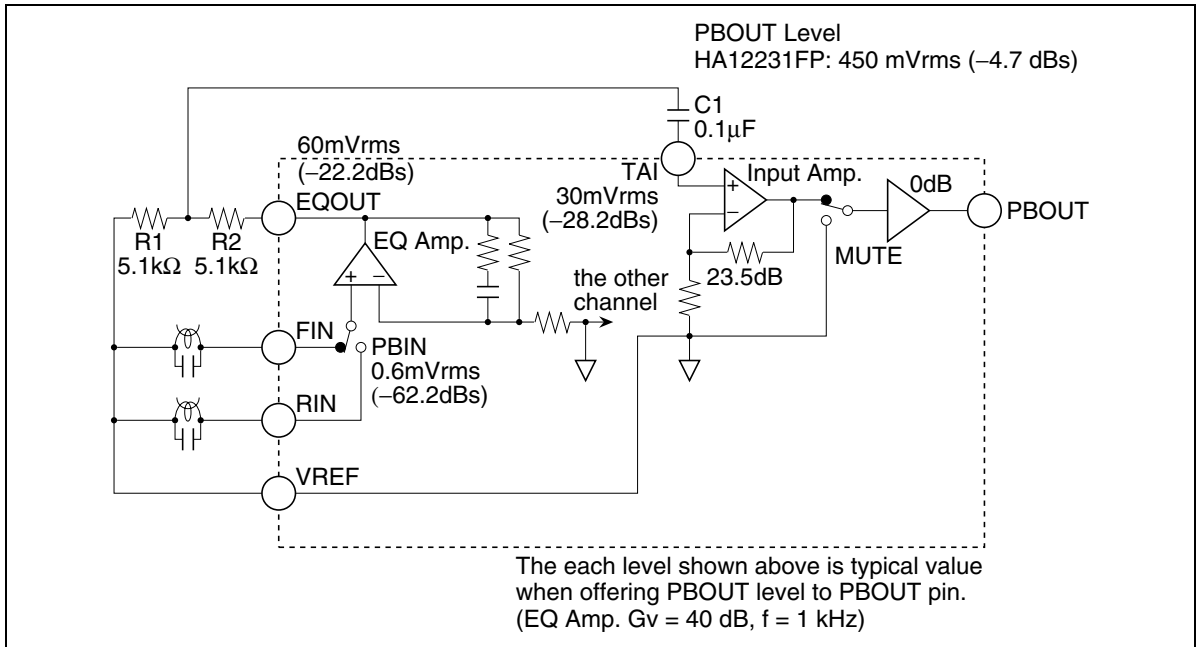


Figure 3 Input Block Diagram

Adjustment of Playback Reference Operate Level

After replace R1 and R2 with a half-fix volume of 10 kΩ, adjust playback reference operate level.

The Sensitivity Adjustment of Music Sensor

Adjusting MS Amp. gain by external resistor, the sensitivity of music sensor can set up. The music sensor block diagram is shown in figure 4, and frequency response is shown in figure 5.

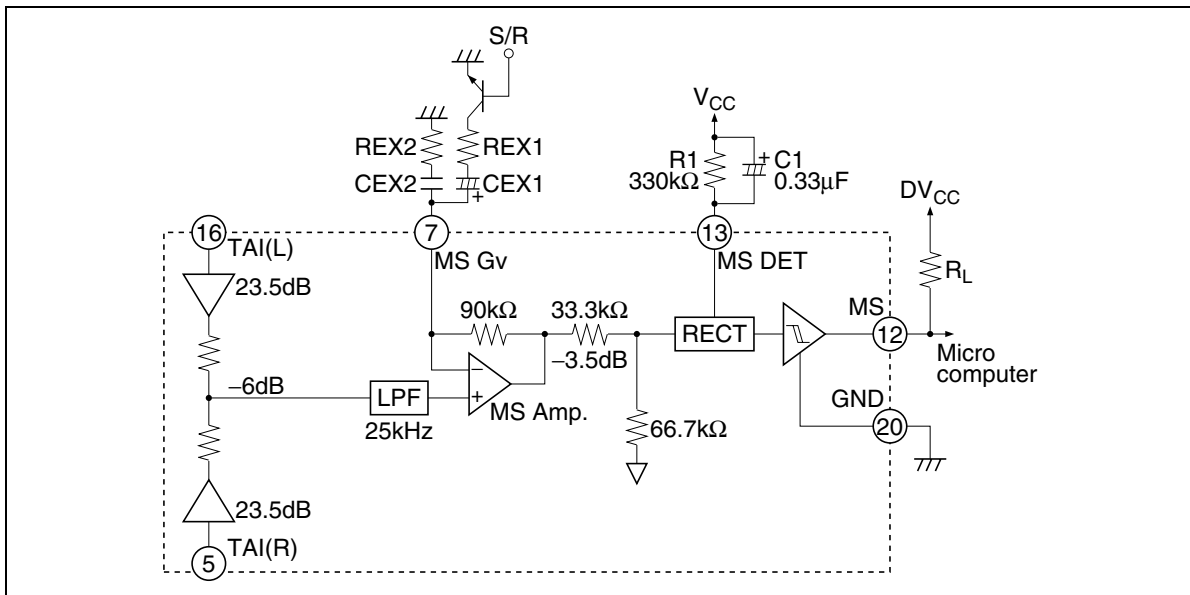


Figure 4 Music Sensor Block Diagram

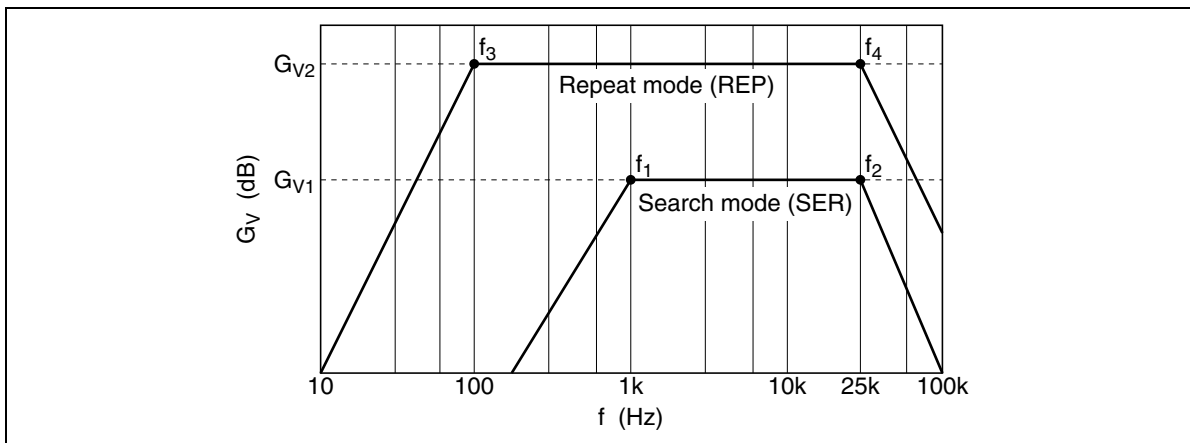


Figure 5 Frequency Response

1. Search mode

$$G_{V1} = (23.5\text{dB} - 3.5\text{dB}) + 20\log\left(1 + \frac{90\text{k}}{\text{REX2}}\right) \text{ [dB]}$$

$$f_1 = \frac{1}{2\pi \cdot \text{CEX2} \cdot \text{REX2}} \text{ [Hz]}, f_2 = 25\text{k} \text{ [Hz]}$$

2. Repeat mode

$$G_{V2} = (23.5\text{dB} - 3.5\text{dB}) + 20\log\left(1 + \frac{90\text{k}}{\text{REX1}}\right) \text{ [dB]}$$

$$f_3 = \frac{1}{2\pi \cdot \text{CEX1} \cdot \text{REX1}} \text{ [Hz]}, f_4 = 25\text{k} \text{ [Hz]}$$

The sensitivity of music sensor (S) is computed by the formula mentioned below.

$$S = 12.7 - G_V \text{ [dB]}$$

S is 6 dB down in case of one-side channel.

Notes: 1. Search mode: G_{V1} , Repeat mode: G_{V2}

2. Standard level of TAI pin (Dolby level correspondence) = 30 mVrms

3. Standard sensing level of music sensor = 130 mVrms

Item	REX1, 2	CEX1, 2	$G_{V1, 2}$	$f_{1, 3}$	$f_{2, 4}$	S (one side channel)	S (both channel)
Search mode	24 k Ω	0.01 μ F	33.5 dB	663 Hz	25 kHz	-14.8 dB	-20.8 dB
Repeat mode	2.4 k Ω	1 μ F	51.7 dB	66.3 Hz	25 kHz	-33.0 dB	-39.0 dB

Note: This MS presented hysteresis lest MS(OUT) terminal should turn over again High level or Low level, in case of thresh S level constantly.

Music Sensor Time Constant

1. Sensing no signal to signal (Attack) is determined by C1, 0.01 μ F to 1 μ F capacitor C1 can be applicable.
2. Sensing signal to no signal (Recovery) is determined by C1 and R1, however preceding (1), 100 k Ω to 1 M Ω can be applicable.

Music Sensor Output (MS(OUT))

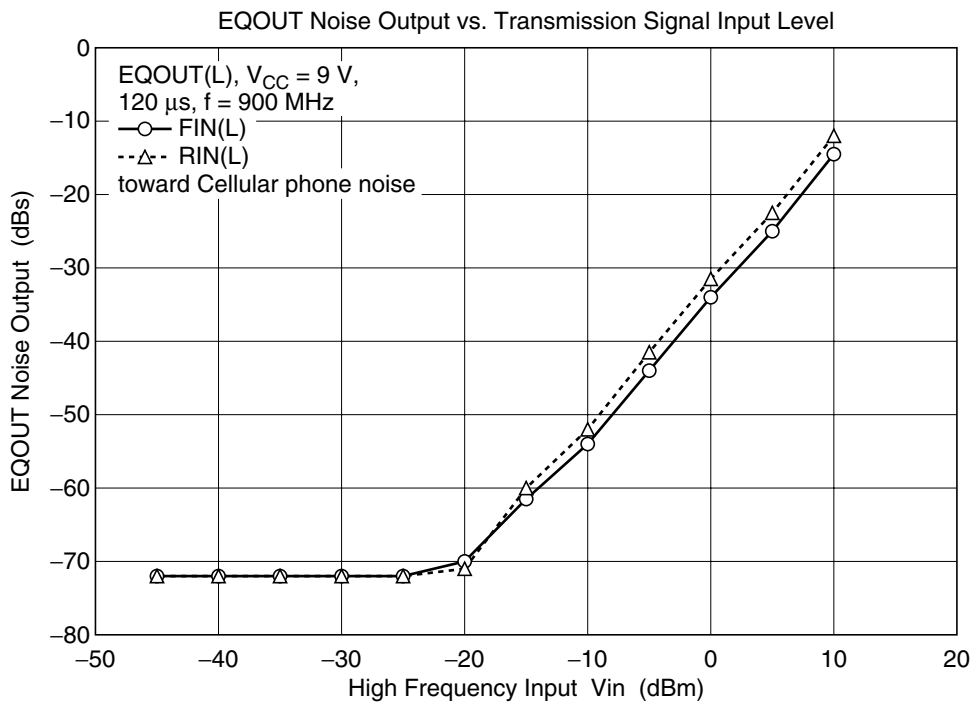
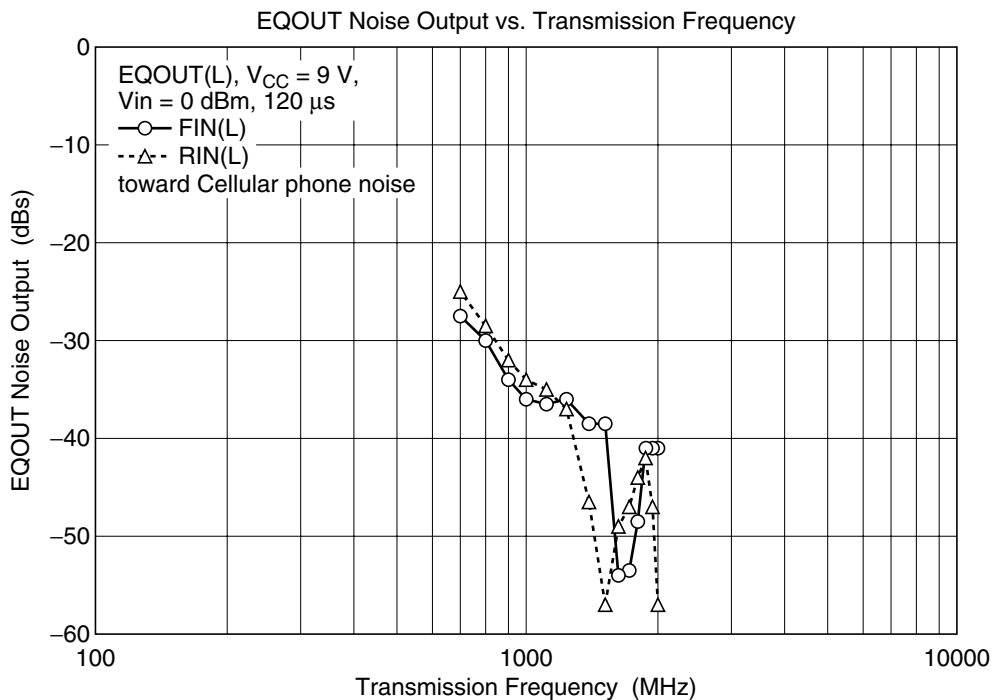
As for the internal circuit of music sensor block, music sensor output pin is connected to the collector of NPN type directly, therefore, output level will be “high” when sensing no signal. And output level will be “low” when sensing signal.

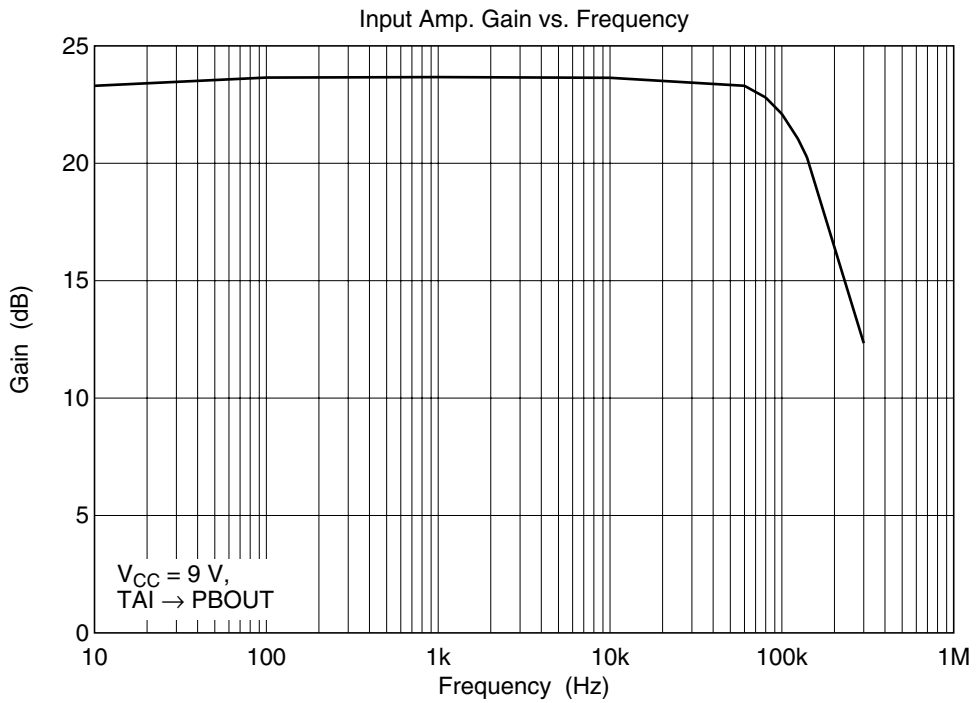
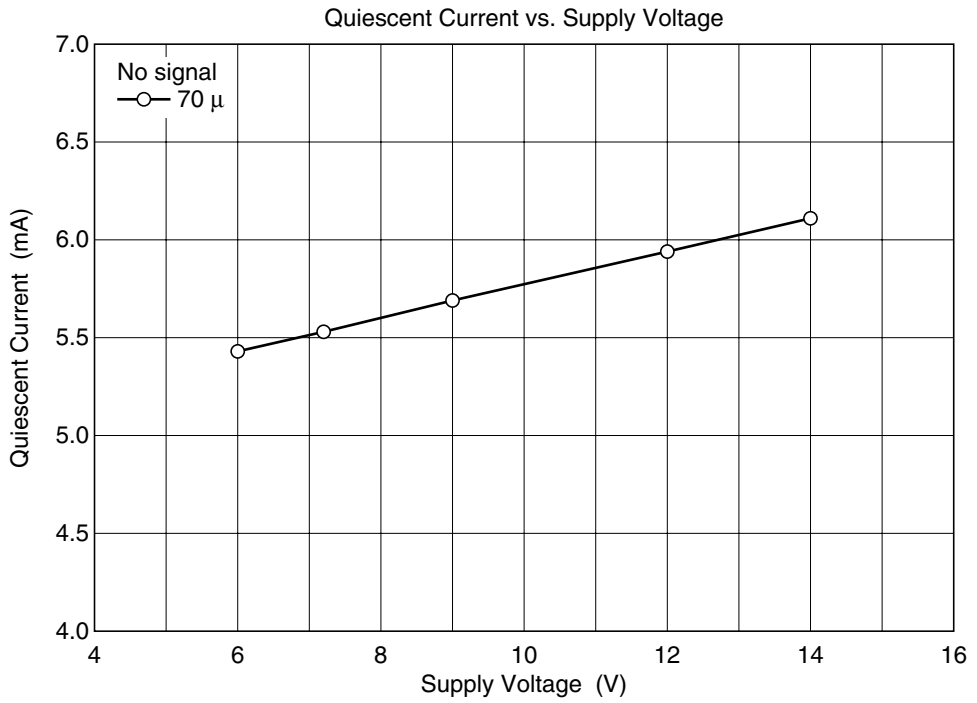
$$I_L = \frac{DV_{CC} - \text{MS(OUT)}_{LO}^*}{R_L}$$

* MS(OUT)_{LO} : Sensing signal (about 1V)

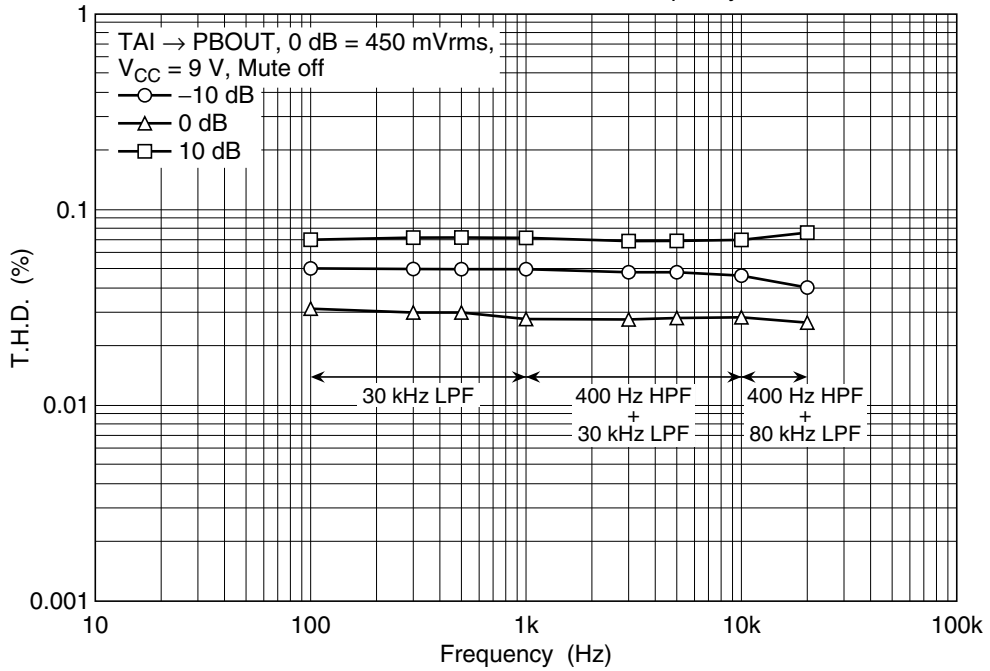
Note: Supply voltage of MS(OUT) pin must be less than V_{CC} voltage.

Characteristic Curves

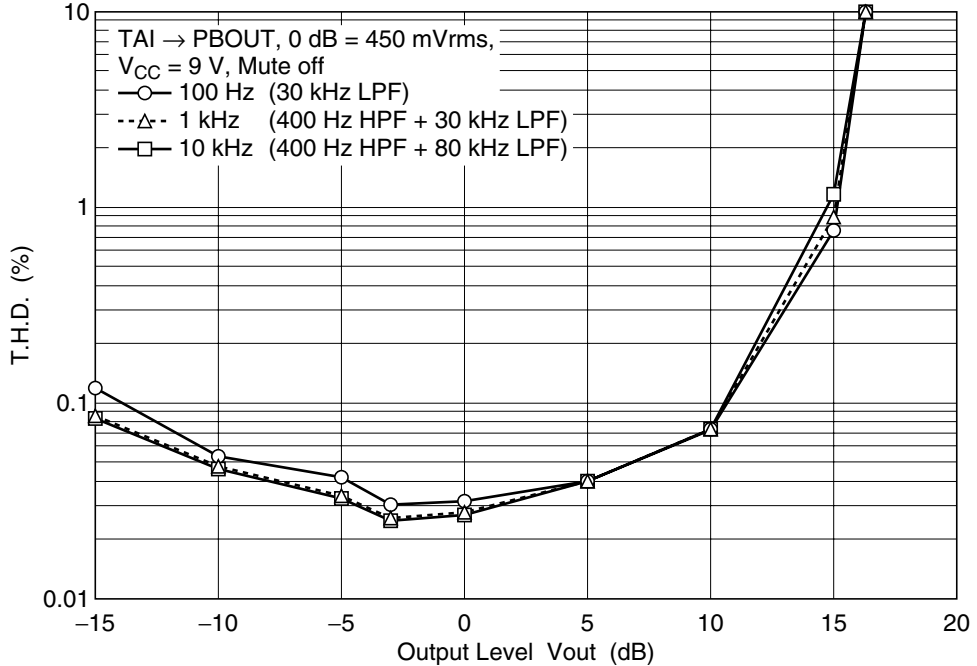




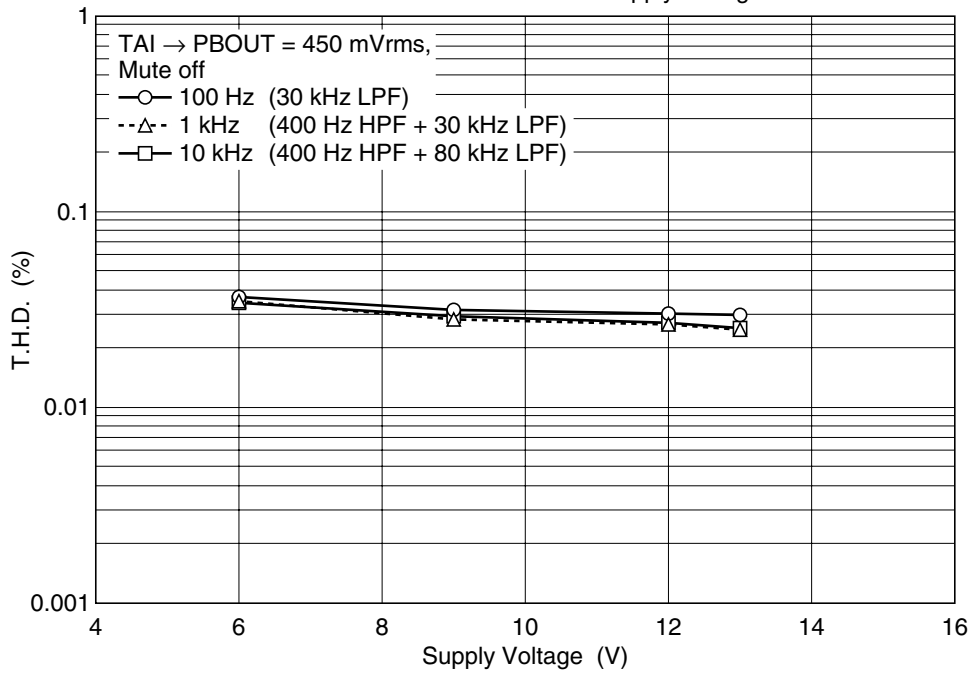
Total Harmonic Distortion vs. Frequency



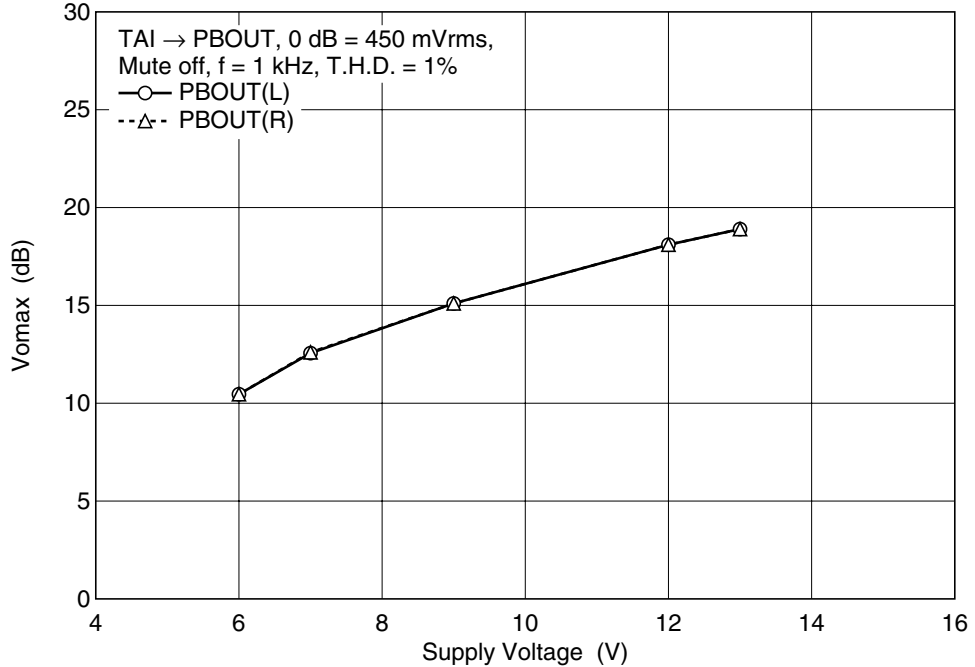
Total Harmonic Distortion vs. Output Level

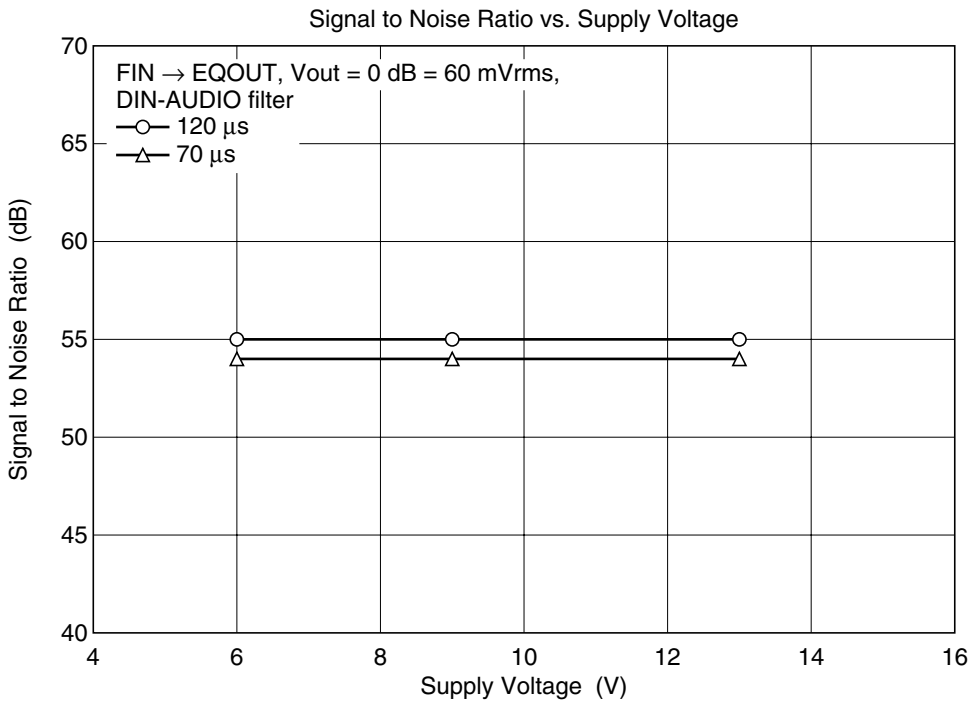
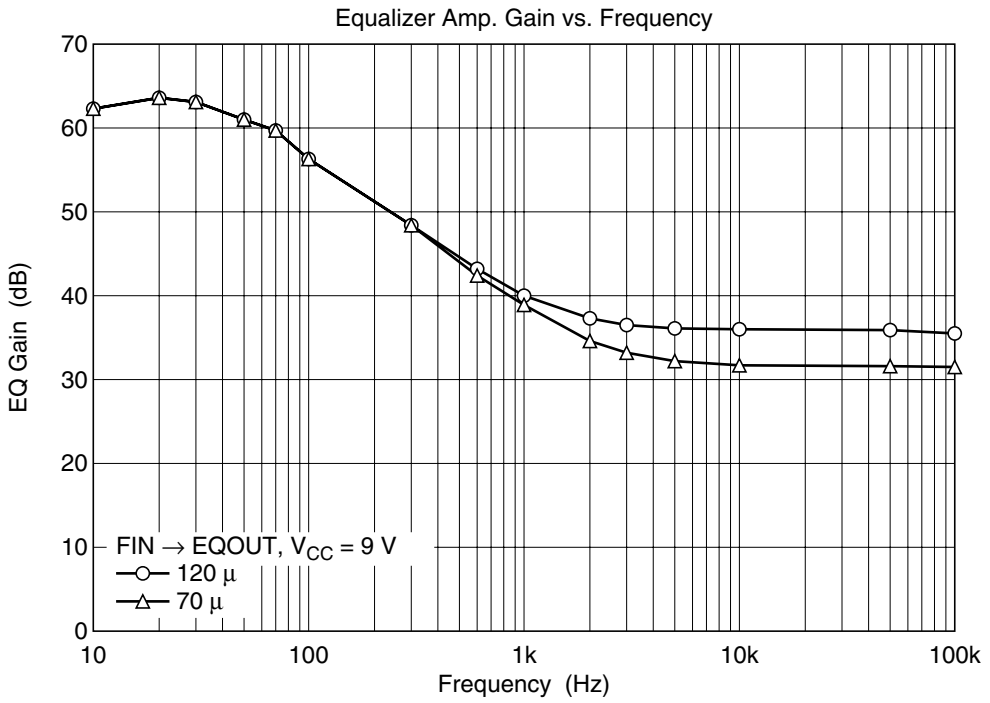


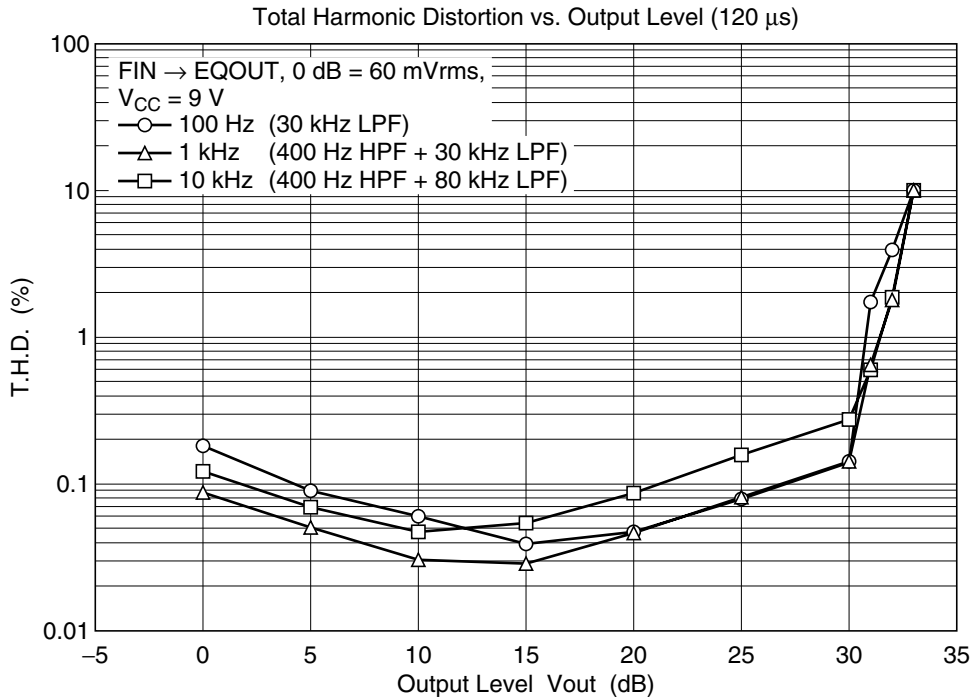
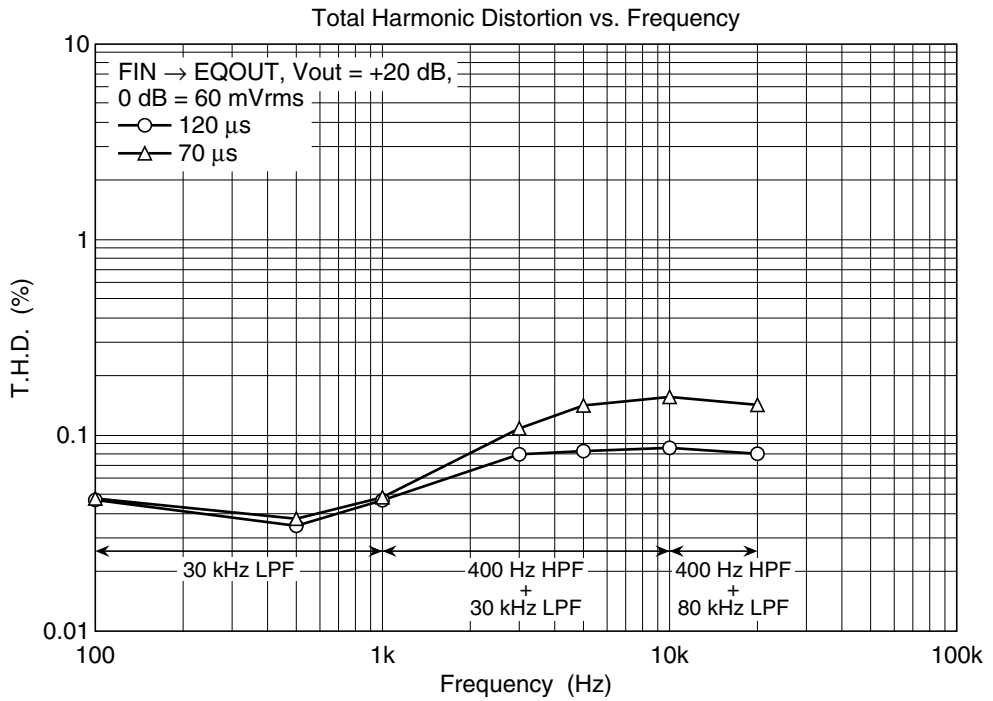
Total Harmonic Distortion vs. Supply Voltage

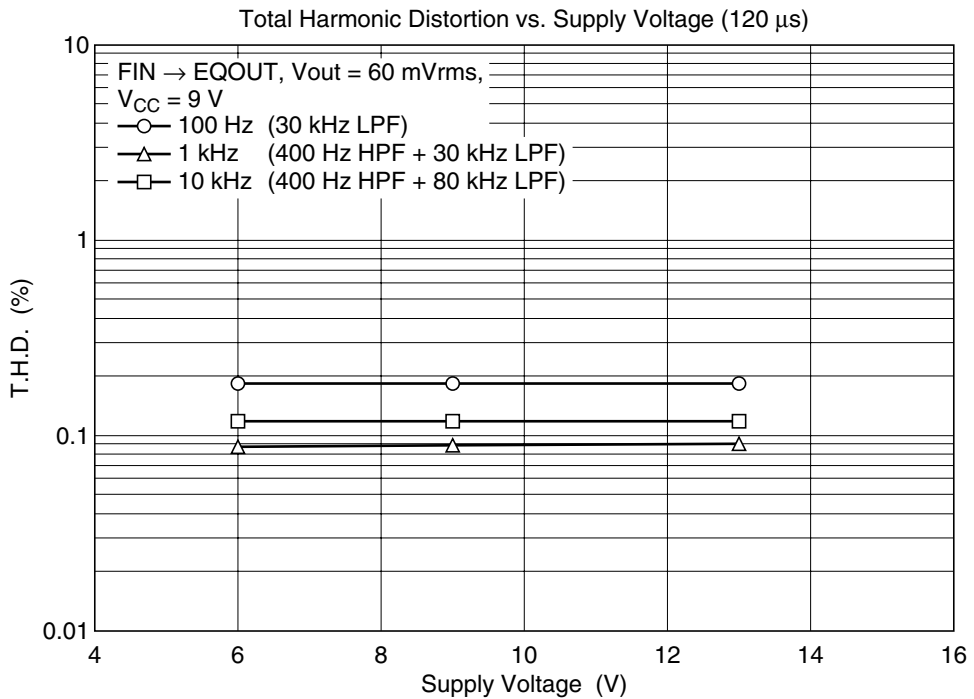
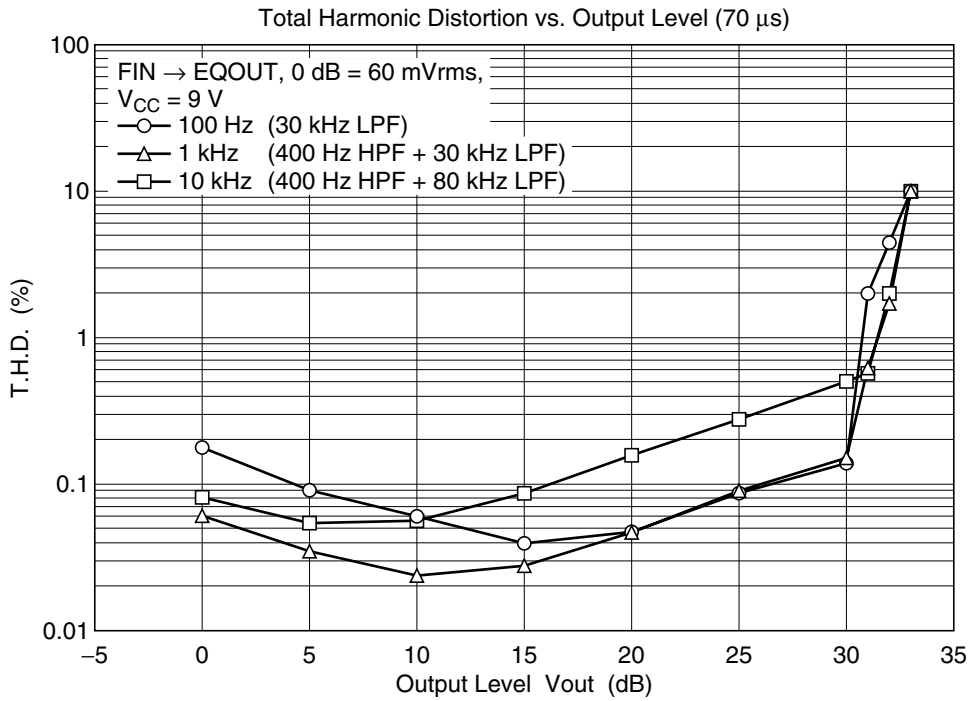


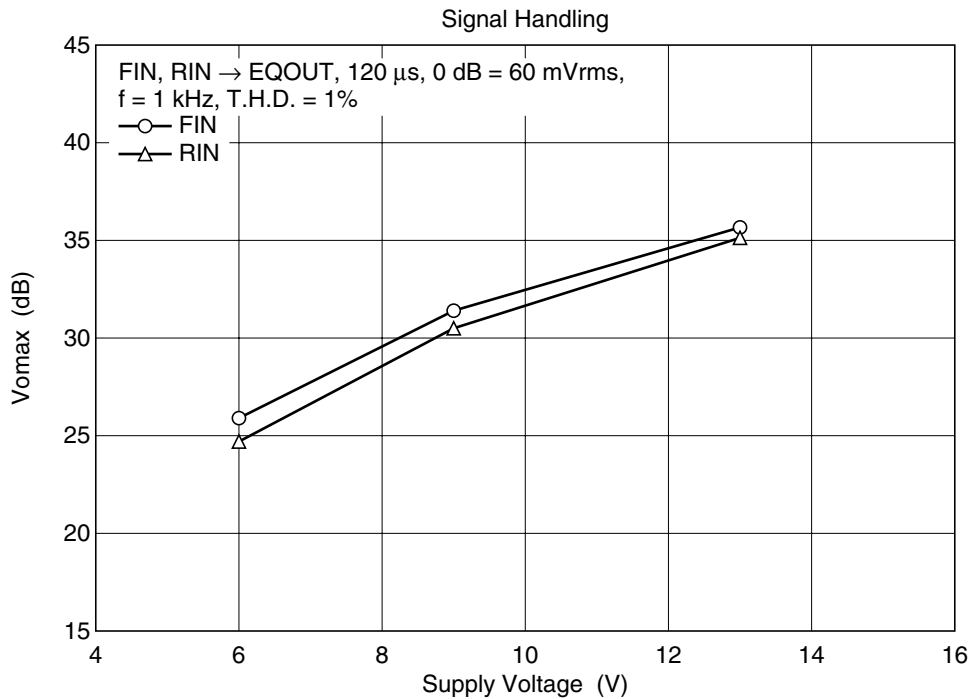
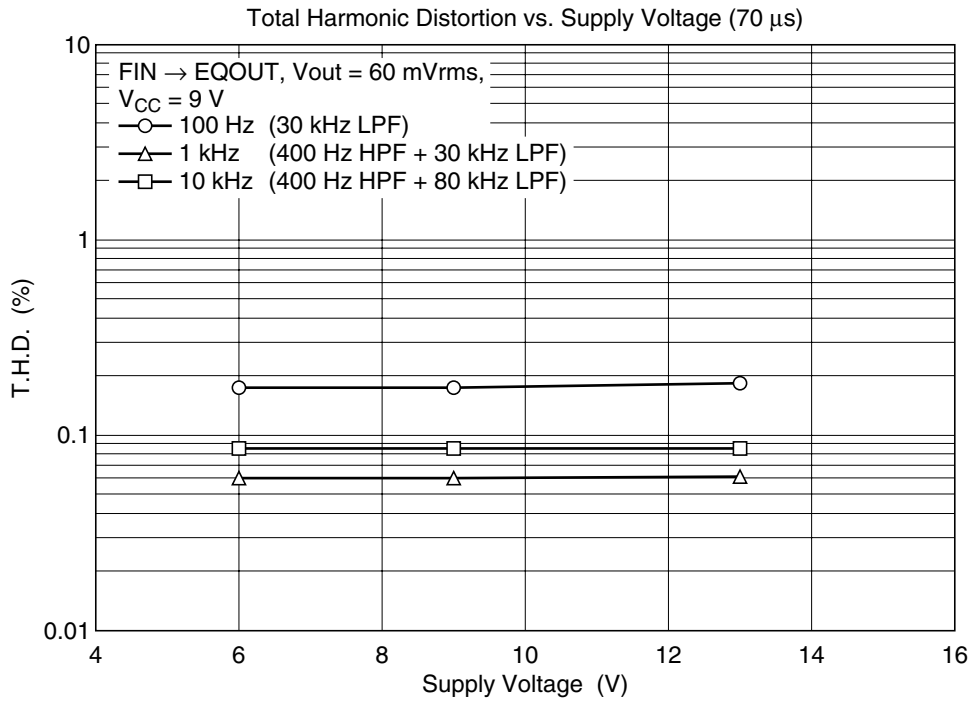
Signal Handling

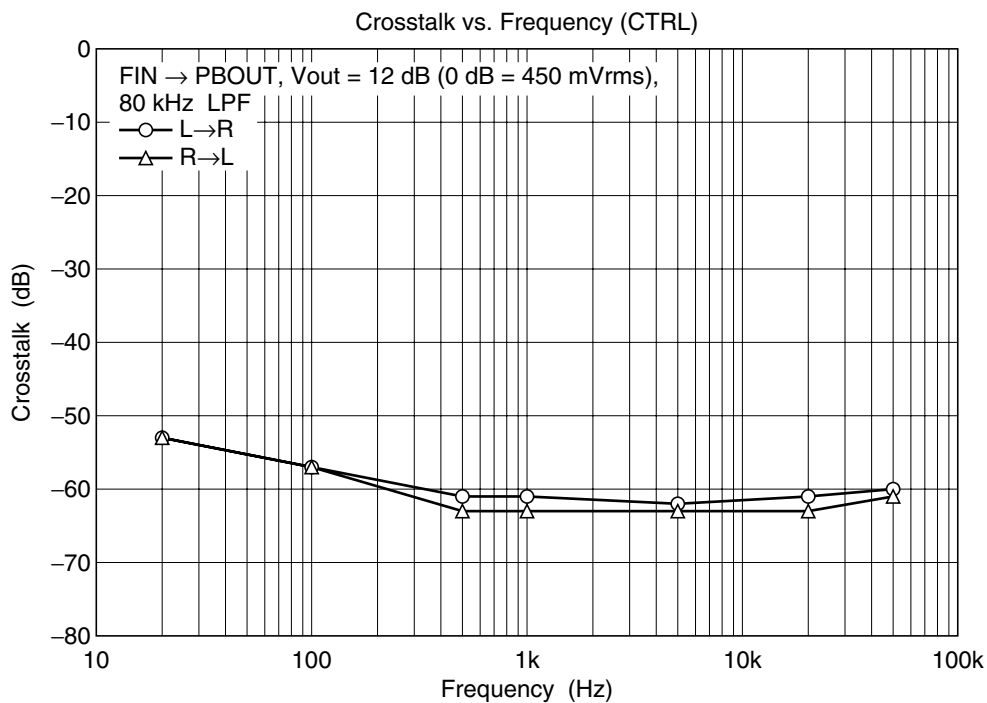
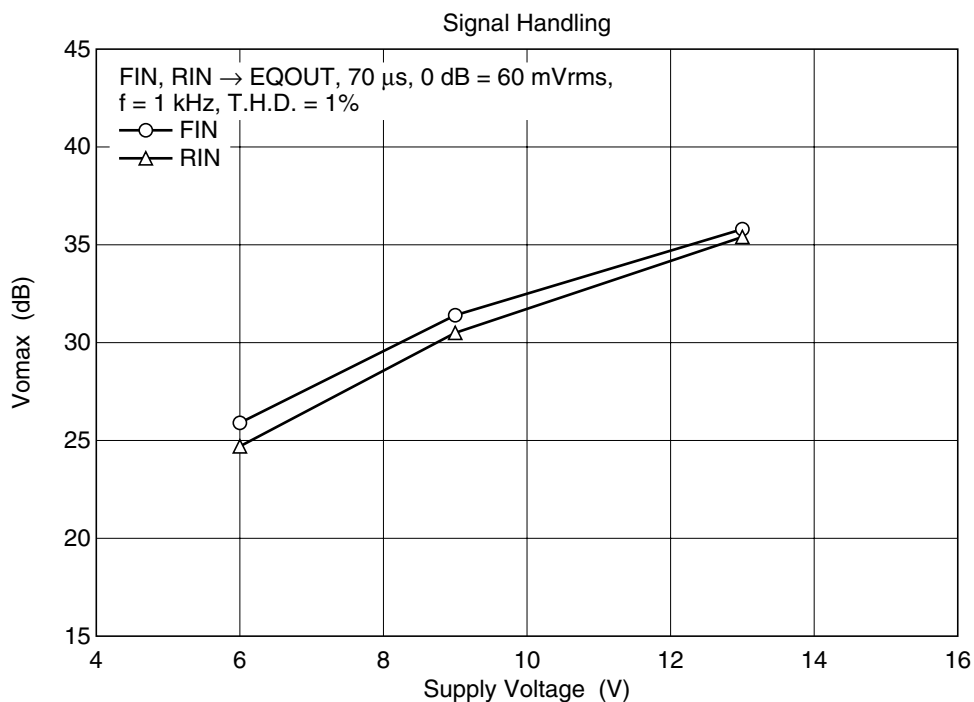


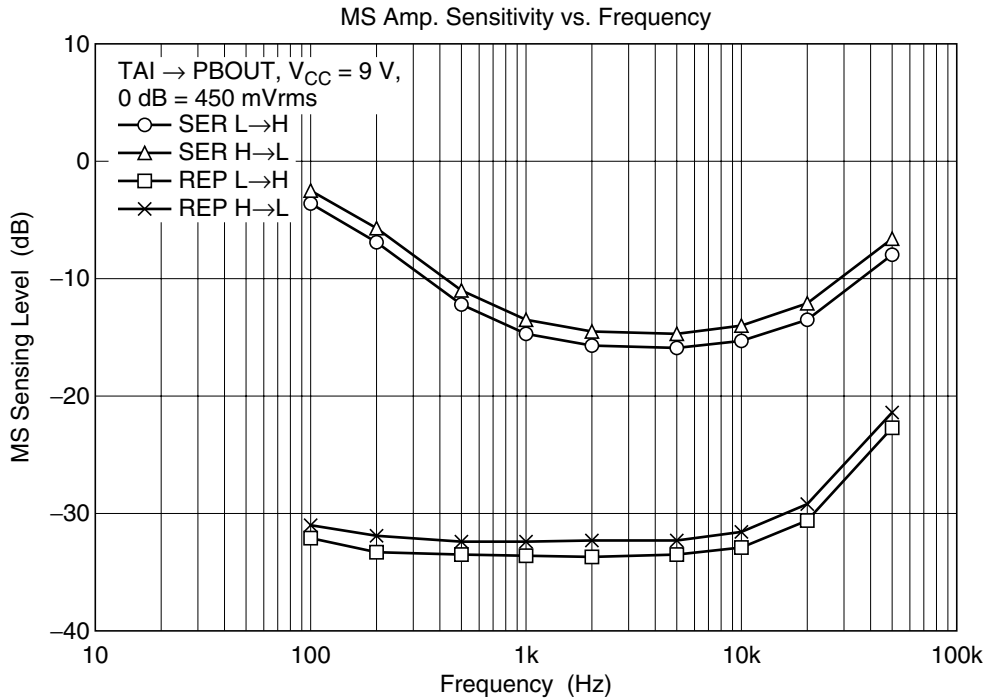
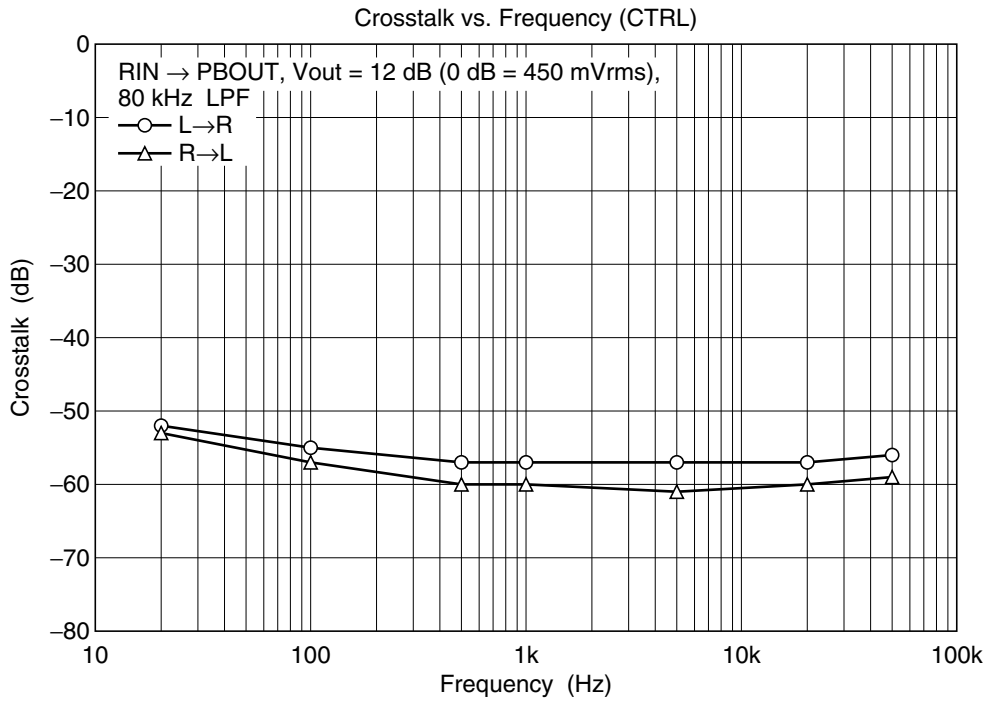


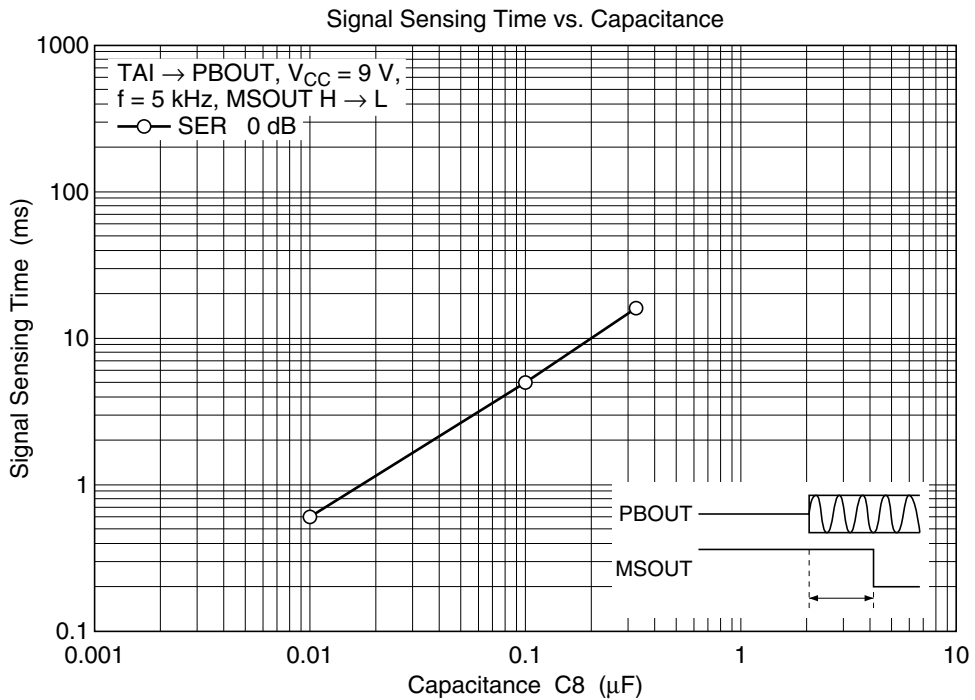
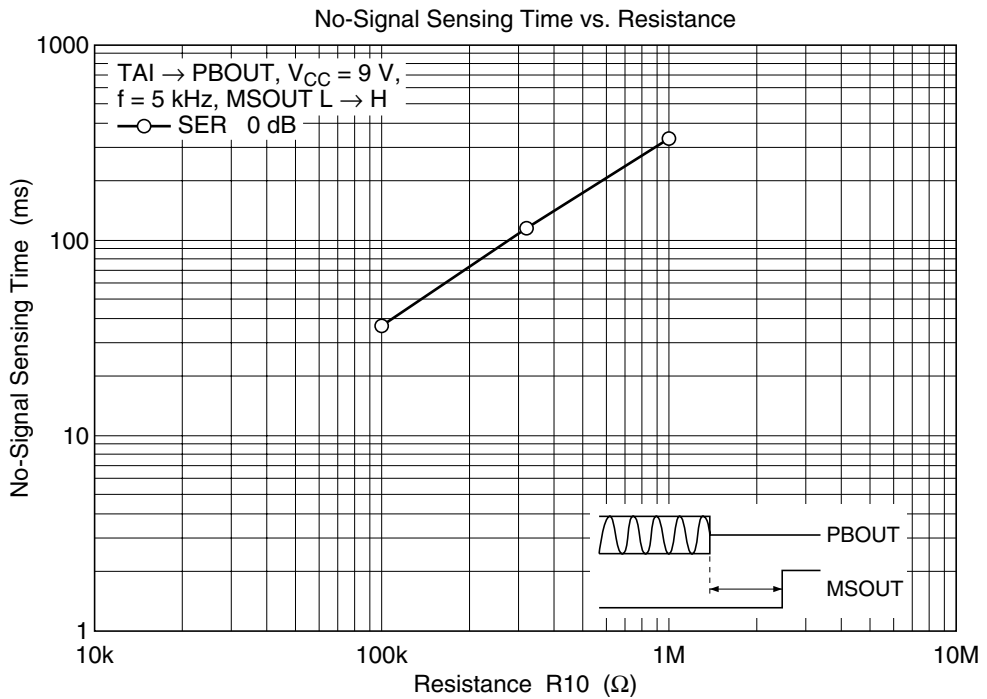








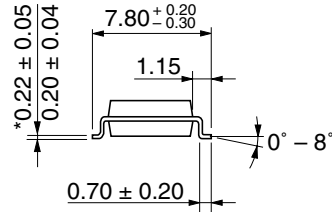
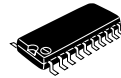
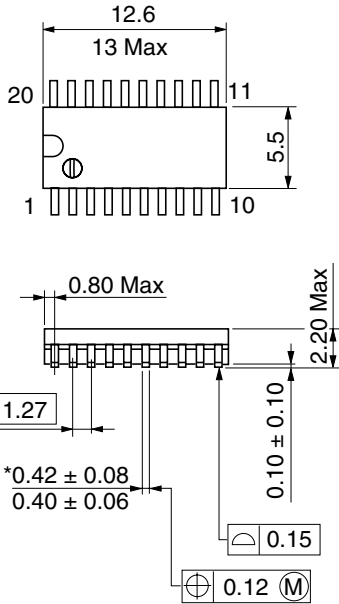




Package Dimensions

As of July, 2002

Unit: mm



*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-20DA
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.31 g

Disclaimer

1. Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent, copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as fail-safes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
5. This product is not designed to be radiation resistant.
6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

Sales Offices

HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL <http://www.hitachisemiconductor.com/>

For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe Ltd.
Electronic Components Group
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 778322

Hitachi Europe GmbH
Electronic Components Group
Dornacher Str 3
D-85622 Feldkirchen
Postfach 201, D-85619 Feldkirchen
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00
Singapore 049318
Tel: <65>-6538-6533/6538-8577
Fax: <65>-6538-6933/6538-3877
URL: <http://semiconductor.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road
Hung-Kuo Building
Taipei (105), Taiwan
Tel: <886>-(2)-2718-3666
Fax: <886>-(2)-2718-8180
Telex: 23222 HAS-TP
URL: <http://semiconductor.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon Hong Kong
Tel: <852>-2735-9218
Fax: <852>-2730-0281
URL: <http://semiconductor.hitachi.com.hk>

Copyright © Hitachi, Ltd., 2002. All rights reserved. Printed in Japan.
Colophon 7.0