MA2J1130G

Silicon epitaxial planar type

For switching circuits

■ Features

- Allowing high-density mounting
- Ensuring the forward current (Average) capacity $I_{F(AV)} = 200 \text{ mA}$
- High breakdown voltage: $V_R = 80 \text{ V}$

Package

- Code
 - SMini2-F3
- Pin Name
 - 1: Anode
- 2: Cathode

■ Absolute Maximum Ratings $T_a = 25$ °C

| Parameter | Symbol | Rating | Unit |
|------------------------------|--------------------|-------------|------|
| Reverse voltage | V_R | 80 | V |
| Maximum peak reverse voltage | V_{RM} | 80 | V |
| Forward current (Average) | I _{F(AV)} | 200 | mA |
| Peak forward current | I_{FM} | 600 | mA |
| Non-repetitive peak forward | I_{FSM} | 1 | A |
| surge current * | | | |
| Junction temperature | T _j | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

Note) *: t = 1 s

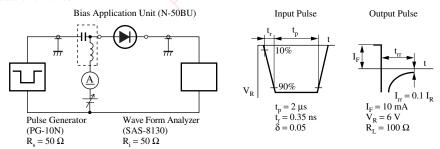
■ Marking Symbol: 1D

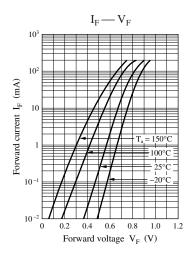
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

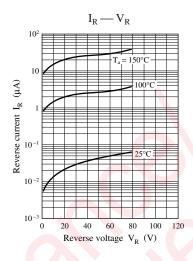
| Parameter | Symbol | Conditions | Min | Тур | Max | Unit |
|-------------------------|-----------------|---|-----|-----|-----|------|
| Forward voltage | $V_{\rm F}$ | $I_F = 200 \text{ mA}$ | 10 | SO. | 1.1 | V |
| Reverse current | I_{R1} | V _R = 15 V | | , | 50 | nA |
| | I_{R2} | $V_R = 75 \text{ V}$ | 7.9 | | 500 | |
| | I_{R3} | $V_R = 75 \text{ V}, T_a = 100^{\circ}\text{C}$ |) | | 100 | μΑ |
| Terminal capacitance | C _t | $V_R = 0 V, f = 1 MHz$ | | | 4 | pF |
| Reverse recovery time * | t _{rr} | $I_F = 10 \text{ mA}, V_R = 6 \text{ V}$ | | | 10 | ns |
| | | $I_{rr} = 0.1 I_{R}, R_{L} = 100 \Omega$ | | | | |

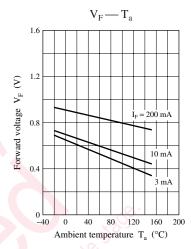
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

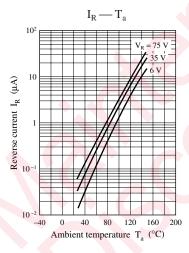
- 2. Absolute frequency of input and output is 100 MHz.
- 3. *: t_{rr} measurement circuit

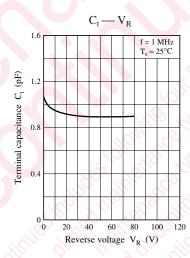


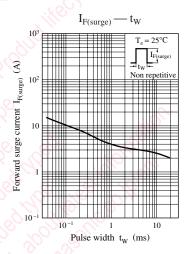






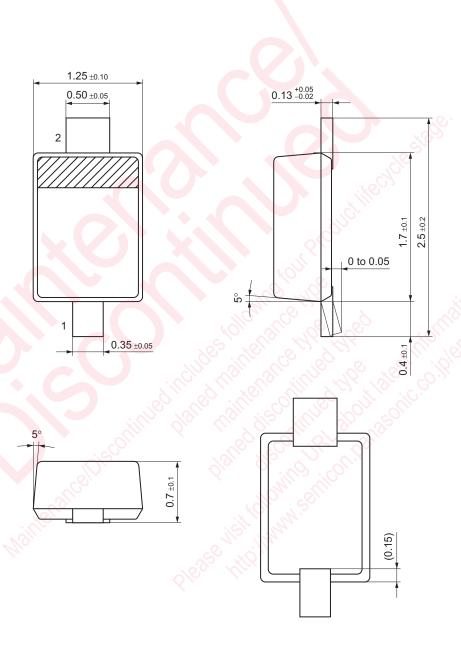






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SMini2-F3 Unit: mm



SKF00092AED 3

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