

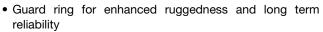
High Performance Schottky Rectifier, 300 A

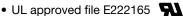


PRODUCT SUMMARY					
I _{F(AV)}	300 A				
V_{R}	100 V				
Package	TO-244				
Circuit	Two diodes common cathode				

FEATURES

- 175 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation





- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-303CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES UN				
I _{F(AV)}	Rectangular waveform	300	Α		
V_{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	22 000	Α		
V _F	150 A _{pk} , T _J = 125 °C (per leg)	0.72	V		
T _J	Range	-55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-303CNQ100PbF	UNITS		
Maximum DC reverse voltage	V_{R}	100	V		
Maximum working peak reverse voltage	V_{RWM}	100	V		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg	1			50.0/ distributed at T = 100.00 master collections		150	
See fig. 5	per device	I _{F(AV)}			300	A		
Maximum peak one cycle non-repetitive			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	22 000			
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	2500			
Non-repetitive avalanche energy per leg E _{AS}		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 13 \text{A}, L = 0.2 \text{mH}$		15	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α		

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES			UNITS
		150 A	T _{.1} = 25 °C	0.91	V
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	300 A	1J=25 C	1.09	
See fig. 1	V _{FM} (1)	150 A	T _{.1} = 125 °C	0.72	
		300 A	1j = 125 C	0.85	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V_{R} = Rated V_{R}	4.5	mA
See fig. 2	'RM \"	T _J = 125 °C	VR = nateu VR	80	IIIA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C 415		4150	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane 6.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/			V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage tempe	rature range	T _J , T _{Stg}	- 55	-	175	°C	
Thermal registeres in action to come	per leg	В	-	-	0.28	°C/W	
Thermal resistance, junction to case	per module	R_{thJC}	-	-	0.14		
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-		
Matala			-	68	-	g	
Weight			-	2.4	-	OZ.	
Mounting torque			35.4 (4)	-	53.1 (6)		
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque			30 (3.4)	-	44.2 (5)		
Vertical pull			-	-	80	llef in	
2" lever pull			-	-	35	lbf ⋅ in	

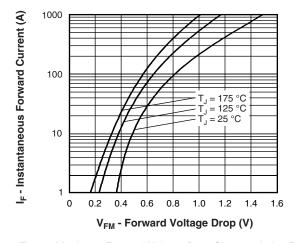


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

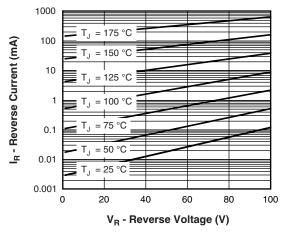


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)



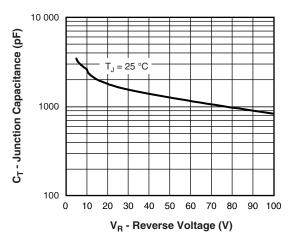


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

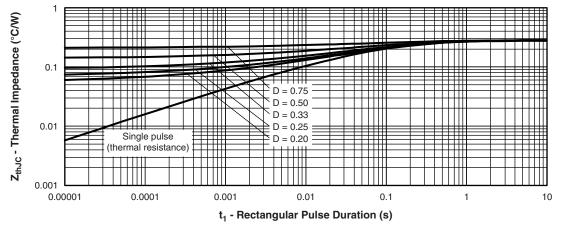


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

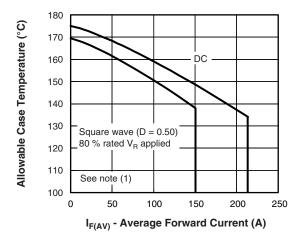


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

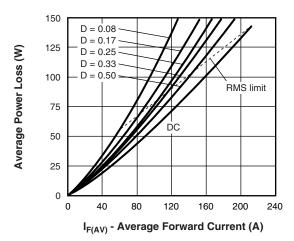


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

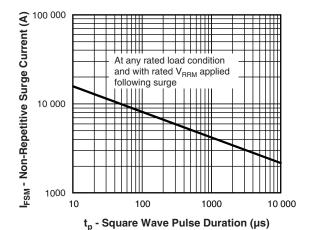


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

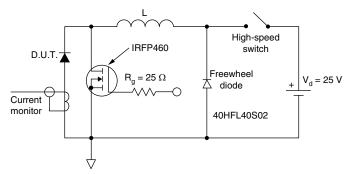


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

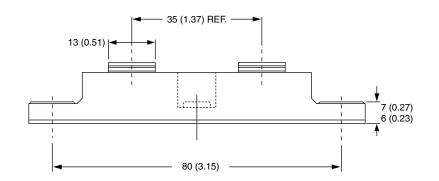
Device code	VS-	30	3	С	N	Q	100	PbF
	1	2	3	4	5	6	7	8
	1 - 2 - 3 - 4 - 5 - 6 - 7 - 8	Ave - Pro C = - N = - Q = - Volt	nay Semerage cu duct silid Circuit Not iso Schottle tage rati	rrent rat con ider configur lated ky rectific ing (100	ing (x 1 itification ation	0) n		

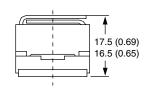
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

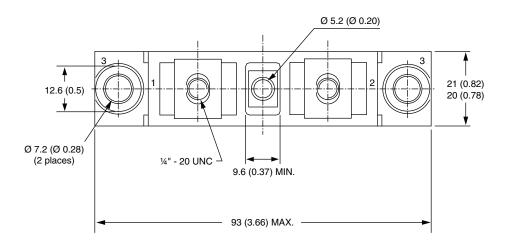


TO-244

DIMENSIONS in millimeters (inches)









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