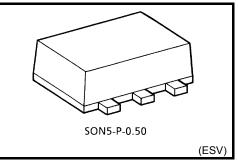
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ00FE

2-Input NAND Gate

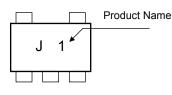
Features

- High output current : ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation : t_{pd} = 2.4 ns (typ.)
 - at V_{CC} = 5 V, 50pF : V_{CC} = 1.65 to 5.5 V
- Operating voltage range
- 5.5-V tolerant inputs
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

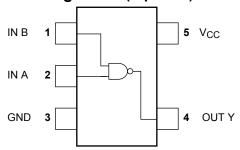


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	–0.5 to 6	V
DC input voltage	VIN	–0.5 to 6	V
DC output voltage	Vour	-0.5 to 6 (Note 1)	V
	V _{OUT}	–0.5 to V _{CC} +0.5 V (Note 2)	v
Input diode current	IIK	-20	mA
Output diode current	IOK	-20 (Note 3)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

Start of commercial production 2008-02

TOSHIBA

IEC Logic Symbol



А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
	VCC	1.5 to 5.5 (Note 4)	v
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V
		0 to V _{CC} (Note 6)	v
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time		0 to 20 (V_{CC} = 1.80 V \pm 0.15 V, 2.5 V \pm 0.2 V)	
	dt/dv	0 to 10 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V_{CC} = 5.0 V \pm 0.5 V)	

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low State

Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition		Toot Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit		
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit			
High		VIH			1.65 to 1.95	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
level	VIH				V _{CC} × 0.7		_	V _{CC} × 0.7	_	- V	
Input Voltage	Ma			1.65 to 1.95	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
	level	VIL		—	2.3 to 5.5	_		$V_{CC} \times 0.3$	_	$V_{CC} \times 0.3$	
					1.65	1.55	1.65	_	1.55	_	
				2.3	2.2	2.3	_	2.2	_		
				I _{OH} = -100 μA	3.0	2.9	3.0	_	2.9	_	
				4.5	4.4	4.5	_	4.4	_		
	High Ievel	Vон (VIN =VIH or VIL	I _{OH} = -4 mA	1.65	1.29	1.52	_	1.29	_	-
				I _{OH} = -8 mA	2.3	1.9	2.15	_	1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
				I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
O the total the sec			I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	_	v	
Output voltage					1.65	_	0	0.1	_	0.1	V
Low level	V _{OL} V _{IN} =	V _{IN} = V _{IH}	I _{OL} = 100 μΑ	2.3	_	0	0.1	_	0.1		
				3.0	_	0	0.1	_	0.1		
				4.5	_	0	0.1	_	0.1		
			I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24		
			I _{OL} = 8 mA	2.3		0.1	0.3	_	0.3		
			I _{OL} = 16 mA	3.0		0.15	0.4		0.4		
			I _{OL} = 24 mA	3.0		0.22	0.55	_	0.55		
			I _{OL} = 32 mA	4.5		0.22	0.55	_	0.55		
Input leakage cu	irrent	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5			±1		±10	μA
Power off leakag	age I _{OFF} V _{IN or} V _{OUT} = 5.5V		0.0	_		1	_	±10	μA		
Quiescent suppl	y current	ICC	I_{CC} $V_{IN} = 5.5$ V or GND		5.5	_	_	2	_	20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C T			Ta = -40 to 85°C		Unit
	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.80 ± 0.15	2.0	4.5	9.6	2.0	9.8	- ns
	^t pLH tpHL		2.5 ± 0.2	0.8	3.2	5.3	0.8	5.7	
			$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.4	3.7	0.5	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.0	4.6	1.5	4.9	
			5.0 ± 0.5	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C _{IN}		0 to 5.5		4		_	_	pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3	_	19		_	_	рF
			5.5	_	27	_		_	

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

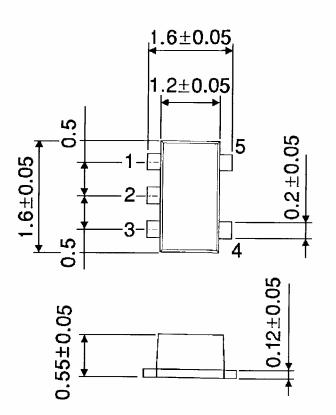
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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