

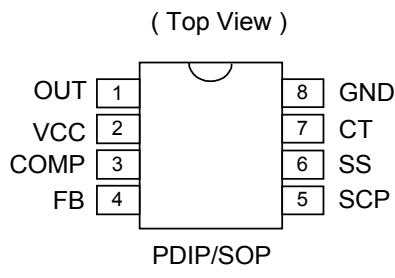
## ■ Features

- PWM Buck Control Circuitry
- Operating voltage can be up to 20V
- Under voltage Lockout (UVLO) Protection
- Short Circuit Protection (SCP)
- Soft-start circuit
- Variable Oscillator Frequency -- 300KHz Max
- 0.77V voltage reference Output
- 8-pin PDIP and SOP packages

## ■ Applications

- Backlight inverter
- LCD Monitor
- XDROM, XDSL Product
- DC/DC converters in computers, etc.

## ■ Pin Assignment



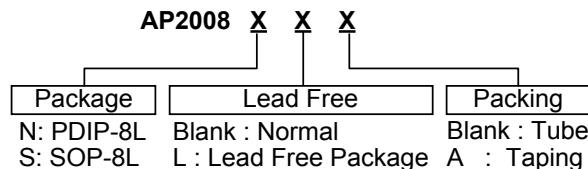
## ■ General Description

The AP2008 integrates Pulse-Width-Modulation (PWM) control circuit into a single chip, mainly designs for power-supply regulator. All the functions included an on-chip 0.77V reference output, an error amplifier, an adjusted oscillator, a soft-start, UVLO, SCP circuitry, and a push-pull output circuit. Switching frequency can be adjustable by trimming CT. During low VCC situation, the UVLO makes sure that the outputs are off until the internal circuit is operational normally.

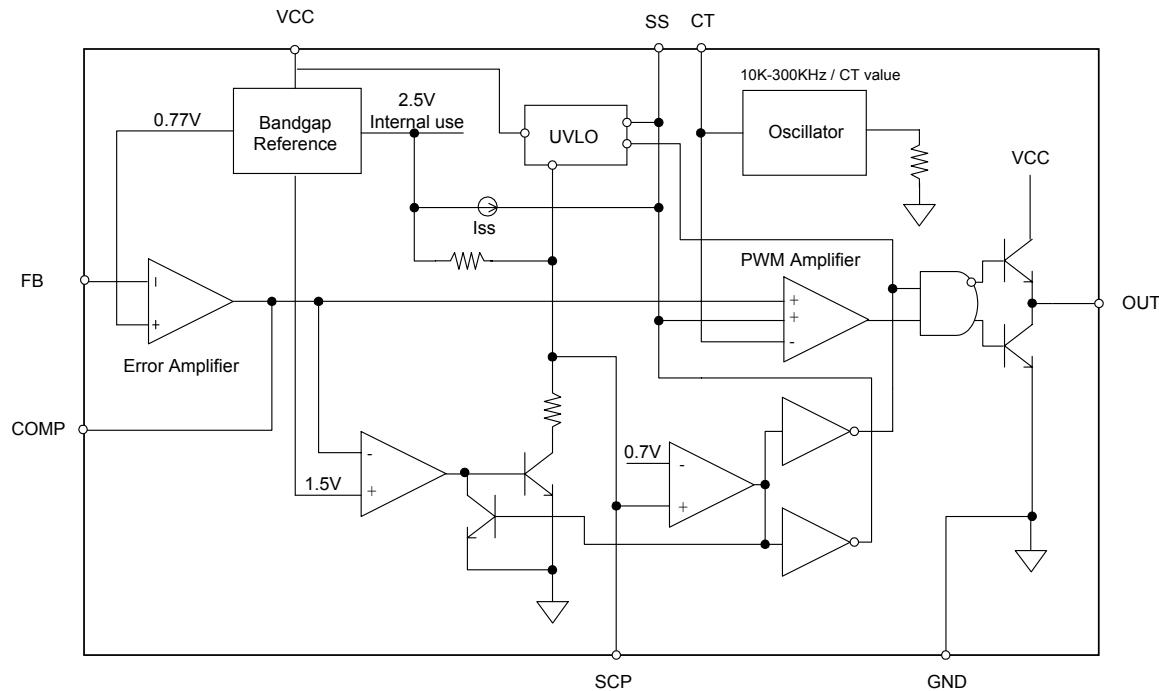
## ■ Pin Descriptions

Name	Description
CT	Timing Capacitor
FB	Voltage Feedback
SS	Soft-Start.
COMP	Feedback Loop Compensation
OUT	PWM Output
GND	Ground
VCC	Supply Voltage
SCP	Short Circuit Protection

## ■ Ordering Information



## ■ Block Diagram



## ■ Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
$V_{CC}$	Supply voltage	22	V
$V_I$	Amplifier input voltage	20	V
$V_O$	Collector output voltage	$V_{CC}-1.0V$	V
$I_{SOURCE}$	Source current	200	mA
$I_{SINK}$	Sink current	200	mA
$T_{OP}$	Operating temperature range	-20 to +85	°C
$T_{ST}$	Storage temperature range	-65 to +150	°C
$T_{LEAD}$	Lead temperature 1.6 mm(1/16 inch) from case for 10 seconds	260	°C

## ■ Recommended Operating Conditions

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply voltage	3.6	20	V
V <sub>I</sub>	Amplifier input voltage	1.05	1.45	V
V <sub>O</sub>	Collector output voltage		V <sub>CC</sub> -1.5	V
I <sub>FB</sub>	Current into feedback terminal		45	µA
R <sub>F</sub>	Feedback resistor	100		kΩ
C <sub>T</sub>	Timing capacitor	100	6800	pF
F <sub>OSC</sub>	Oscillator frequency	10	300	KHz
T <sub>OP</sub>	Operating free-air temperature	-20	85	°C

## ■ Electrical Characteristics (T<sub>A</sub>=25°C, V<sub>CC</sub>=6V, f=200 KHz)

**Reference (REF)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V <sub>REF</sub>	Comp connect to FB		0.755	0.770	0.785	V
	Output voltage change with temperature	T <sub>A</sub> = -20°C ~ 25°C		-0.1	±1	%
		T <sub>A</sub> = 25°C ~ 85°C		-0.2	±1	%

**Under voltage lockout (UVLO)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V <sub>UT</sub>	Upper threshold voltage (V <sub>CC</sub> )			2.9		V
V <sub>LWT</sub>	Lower threshold voltage (V <sub>CC</sub> )	I <sub>O(REF)</sub> = 0.1mA T <sub>A</sub> = 25°C		2.4		V
V <sub>HT</sub>	Hysteresis (V <sub>CC</sub> )			500		mV

**Short-circuit protection (SCP) control**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V <sub>IT</sub>	Input threshold voltage	T <sub>A</sub> = 25°C	0.60	0.67	0.75	V
V <sub>STB</sub>	Standby voltage	No pull up	100	130	160	mV
V <sub>LT</sub>	Latched input voltage	No pull up		50	100	mV
I <sub>SCP</sub>	Input (source) current	V <sub>I</sub> = 0.7V, T <sub>A</sub> = 25°C	-10	-15	-20	µA
V <sub>CT</sub>	Comparator threshold voltage (COMP)			1.5		V

**Oscillator (OSC)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
F <sub>OSC</sub>	Frequency	C <sub>T</sub> = 270 pF		200		KHz
ΔF <sub>OSC</sub>	Standard deviation of frequency	C <sub>T</sub> = 270 pF		10		%
	Frequency change with voltage	V <sub>CC</sub> =3.6V ~ 20V		1		

**PWM Buck Controller**
**■ Electrical Characteristics (Continued)** ( $T_A=25^\circ\text{C}$ ,  $V_{CC}=6\text{V}$ ,  $f=200\text{ KHz}$ )

**Error-amplifier**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$V_{IO}$	Input offset voltage	$V_O \text{ (FB)} = 0.77\text{V}$			$\pm 6$	mV
$I_{IO}$	Input offset current	$V_O \text{ (FB)} = 0.77\text{V}$			$\pm 100$	nA
$I_{IB}$	Input bias current	$V_O \text{ (FB)} = 0.77\text{V}$		160	500	nA
$V_{CM}$	Common-mode input voltage range	$V_{CC} = 3.6\text{V} \sim 20\text{V}$	1.05		1.45	V
$AV$	Open-loop voltage amplification	$R_F = 200\text{ k}\Omega$	70	80		dB
$GBW$	Unity-gain bandwidth			1.5		MHz
$CMRR$	Common-mode rejection ratio		60	80		dB
$V_{OH}$	Max. output voltage			$V_{ref} - 0.1$		V
$V_{OL}$	Min. output voltage				1	V
$I_{OI}$	Output (sink) current (COMP)	$V_{ID} = -0.1\text{V}$ , $V_O = 0.77\text{V}$	0.5	1.6		mA
$I_{OO}$	Output (source) current (COMP)	$V_{ID} = 0.1\text{V}$ , $V_O = 0.77\text{V}$	-45	-70		$\mu\text{A}$

**Output section**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$I_{LEAK}$	Leakage current	$V_O = 20\text{V}$			10	$\mu\text{A}$
$I_{DRV}$	Sink current	$V_{IN} = 12\text{V}$		200		mA
	Source current	$V_{IN} = 12\text{V}$		200		mA
$V_{SAT}$	Output saturation voltage	$I_O = 10\text{ mA}$		1.0	1.5	V
$I_{SC}$	Short-circuit output current	$V_O = 6\text{V}$		120		mA

**PWM comparator**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$V_{T0}$	Input threshold voltage at $f = 10\text{ KHz}$ (COMP)	$CT = 6800\text{pF}$		0.6	0.7	V
		Maximum duty cycle	1.2	1.3		V

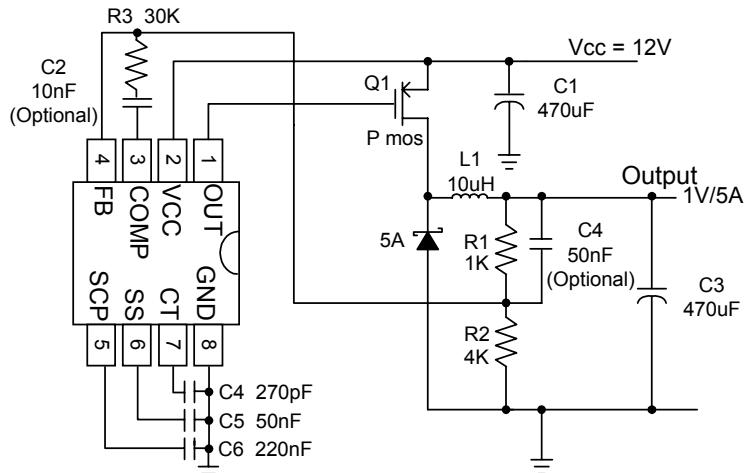
**Total device**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$I_{CCA}$	Average supply current	$C_T = 270\text{pF}$		6	10	mA

**Soft Start**

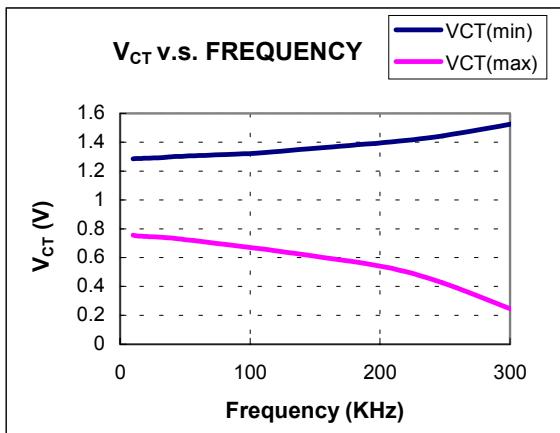
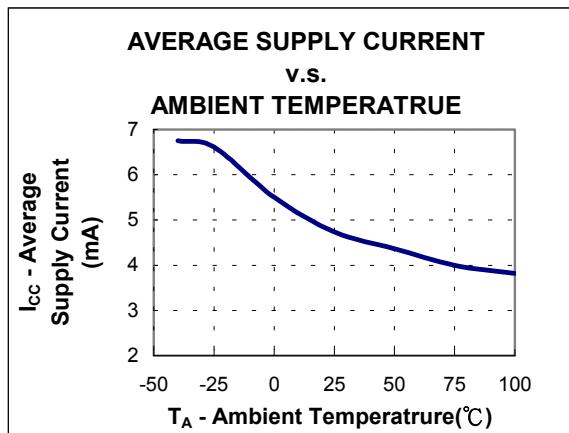
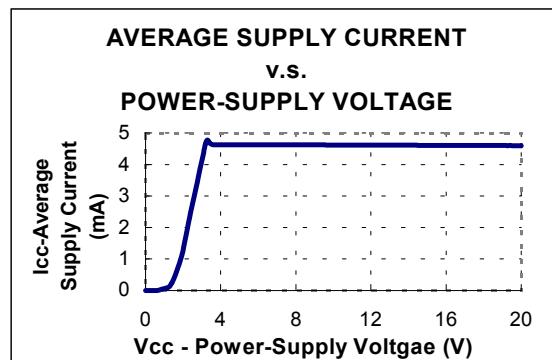
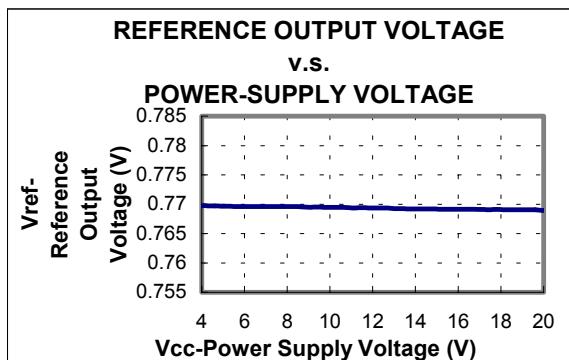
<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$V_{SS}$	Soft-start Voltage			2.3		V
$I_{SS}$	Constant Charge Current			20		$\mu\text{A}$

## ■ Typical Application Circuit

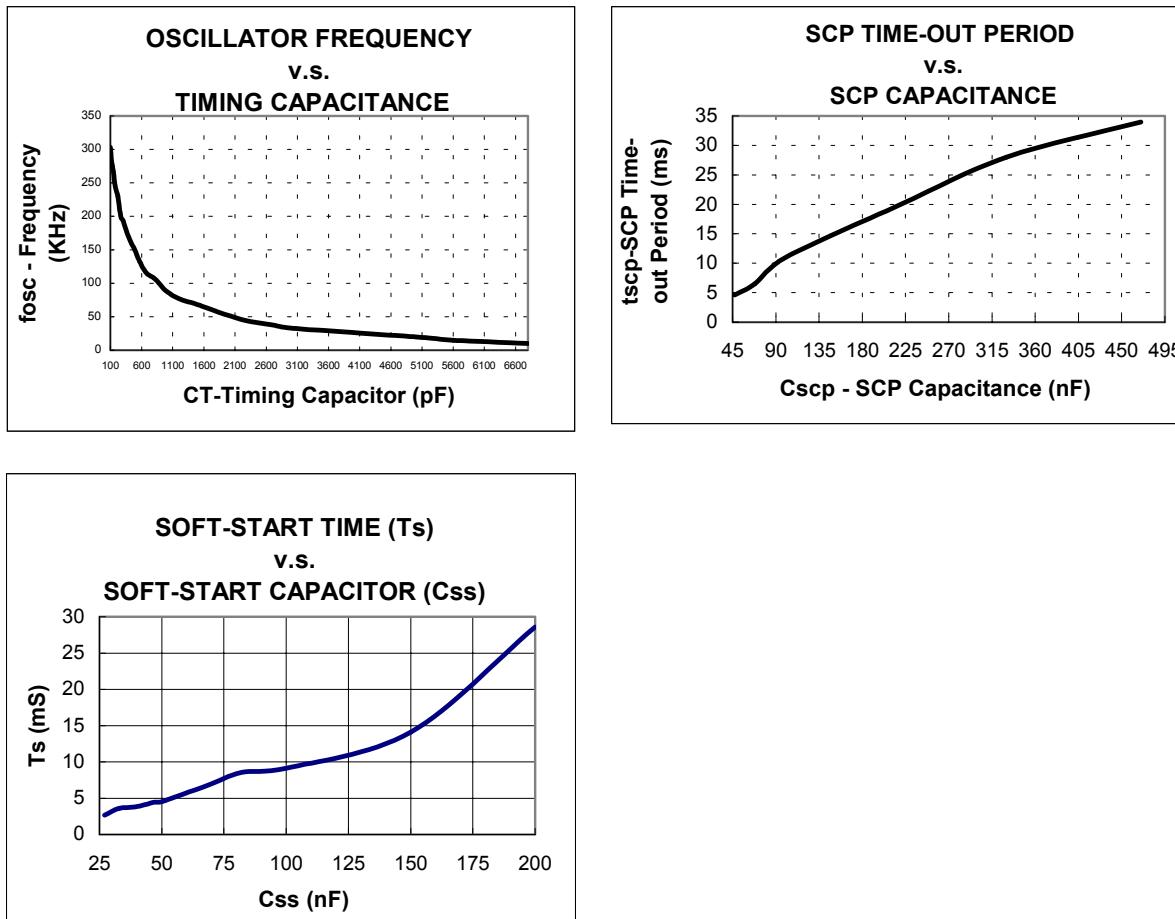


Step-Down DC/DC converter

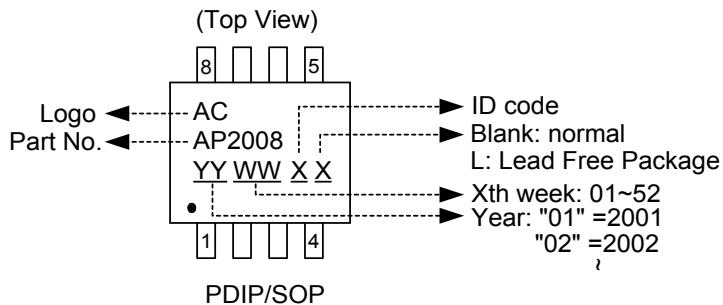
## ■ Typical Characteristics



## ■ Typical Characteristics (Continued)

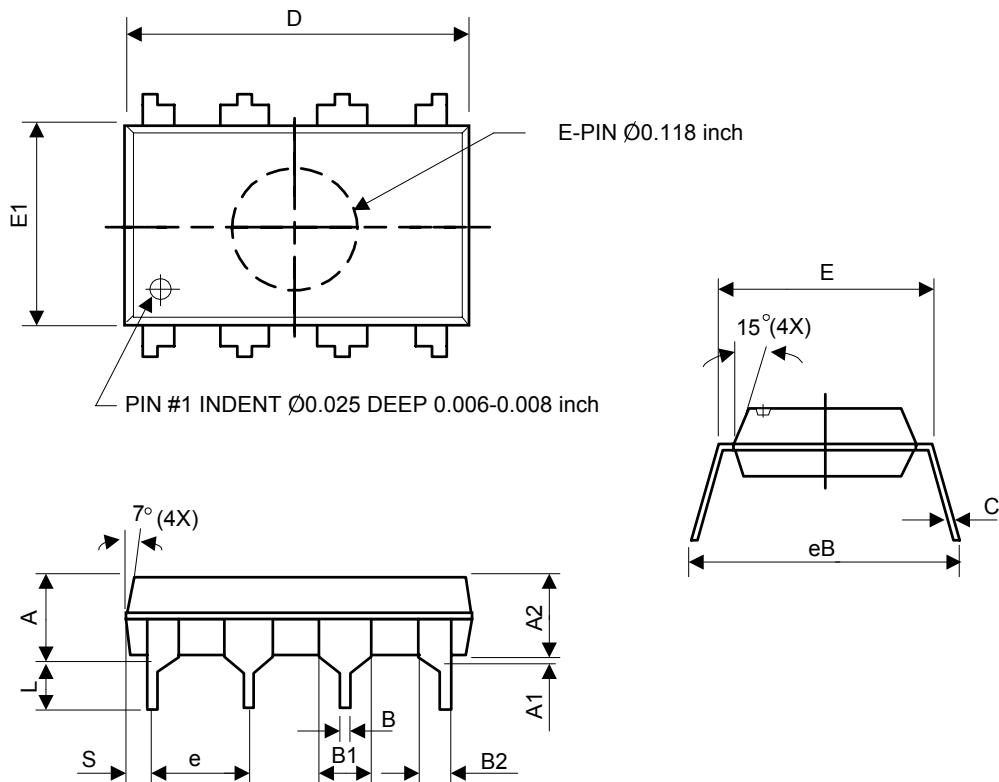


## ■ Marking Information



## ■ Package Information

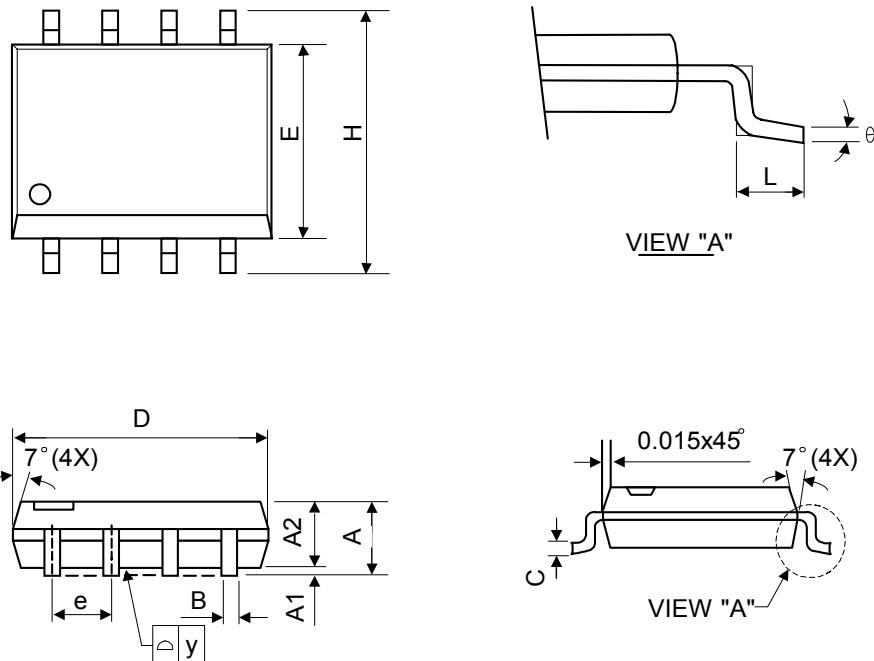
### (1) PDIP-8L (Plastic Dual-in-line Package )



Symbol	Dimensions in millimeters			Dimensions in inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	5.33	-	-	0.210
A1	0.38	-	-	0.015	-	-
A2	3.1	3.30	3.5	0.122	0.130	0.138
B	0.36	0.46	0.56	0.014	0.018	0.022
B1	1.4	1.52	1.65	0.055	0.060	0.065
B2	0.81	0.99	1.14	0.032	0.039	0.045
C	0.20	0.25	0.36	0.008	0.010	0.014
D	9.02	9.27	9.53	0.355	0.365	0.375
E	7.62	7.94	8.26	0.300	0.313	0.325
E1	6.15	6.35	6.55	0.242	0.250	0.258
e	-	2.54	-	-	0.100	-
L	2.92	3.3	3.81	0.115	0.130	0.150
eB	8.38	8.89	9.40	0.330	0.350	0.370
S	0.71	0.84	0.97	0.028	0.033	0.038

## ■ Package Information (Continued)

### (2) SOP- 8L(JEDEC Small Outline Package)



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.60	1.75	0.055	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
B	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.80	5.05	5.30	0.189	0.199	0.209
E	3.70	3.90	4.10	0.146	0.154	0.161
e	-	1.27	-	-	0.050	-
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
y	-	-	0.10	-	-	0.004
theta	0°	-	8°	0°	-	8°