

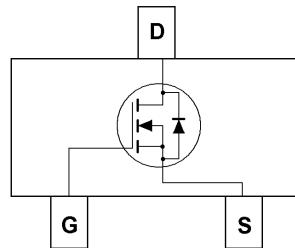
N-Channel Enhancement Mode MOSFET
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Feature

- 30V/5.8A, RDS(ON) = 35mΩ(MAX) @VGS = 10V.
RDS(ON) = 40mΩ(MAX) @VGS = 4.5V.
RDS(ON) = 55mΩ(MAX) @VGS = 2.5V.
- Super High dense cell design for extremely low RDS(ON).
- Reliable and Rugged.
- SC-59 for Surface Mount Package.



SC-59

**Applications**

- Power Management
Portable Equipment and Battery Powered Systems.

Absolute Maximum Ratings

TA=25°C Unless Otherwise noted

Parameter	Symbol	Limit	Units
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±12	V
Drain Current-Continuous	I _D	5.8	A

Electrical Characteristics

TA=25°C Unless Otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Off Characteristics						
Drain to Source Breakdown Voltage	BVDSS	V _{GS} =0V, ID=250μA	30	-	-	V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate Body Leakage Current, Forward	IGSSF	V _{GS} =12V, V _{DS} =0V	-	-	100	nA
Gate Body Leakage Current, Reverse	IGSSR	V _{GS} =-12V, V _{DS} =0V	-	-	-100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , ID=250μA	0.6	-	1.5	V
Static Drain-source On-Resistance	RDS(ON)	V _{GS} = 10V, ID = 5.8A	-	30	35	mΩ
		V _{GS} = 4.5V, ID = 5A	-	33	40	mΩ
		V _{GS} = 2.5V, ID = 4A	-	45	55	mΩ
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} = 0V, IS=1.25A			1.2	V

Dynamic		
Q_g	Total Gate Charge	
Q_{gs}	Gate-Source Charge	
Q_{gd}	Gate-Drain Charge	
t_{on}	Turn-on Time	
$t_{d(on)}$	Turn-on Delay time	
t_r	Turn-on Rise Time	
$T_{d(off)}$	Turn-off Delay Time	
t_f	Turn-off Fall Time	
t_{off}	Turn-off Time	
		V _{DS} =15V, V _{GS} =10V, I _D =2A
		V _{DD} =15V, I _D =2A, V _{GS} =10V, R _G =6Ω
		8.5
		1.1
		1.8
		40
		11
		17
		37
		20
		60
		nC
		ns

Typical Characteristics

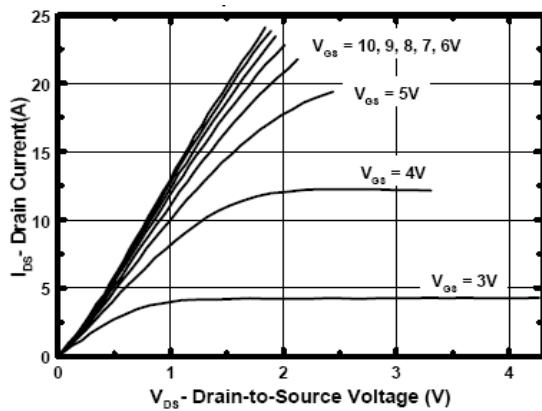


Figure 1. Output Characteristics

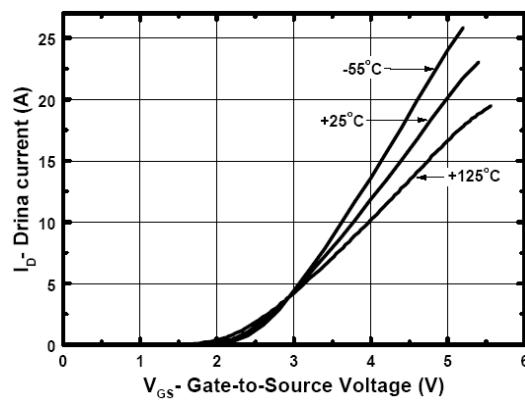


Figure 2. Transfer Characteristics

Typical Characteristics

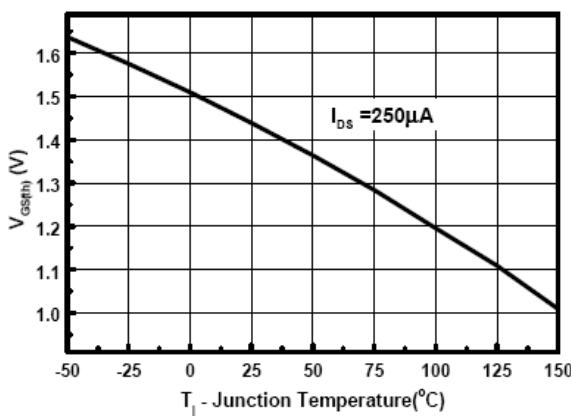


Figure 3. Gate Threshold Variation with Temperature

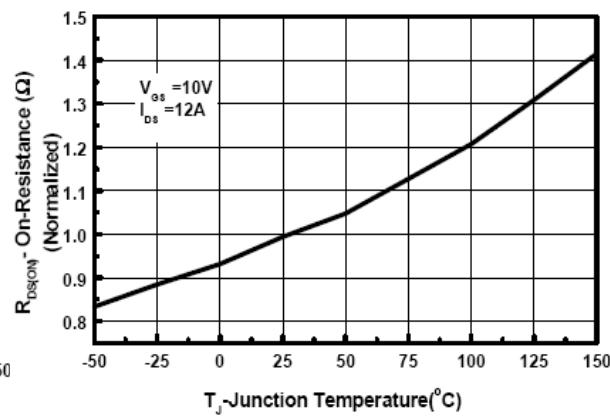


Figure 4. On-Resistance Variation with Temperature

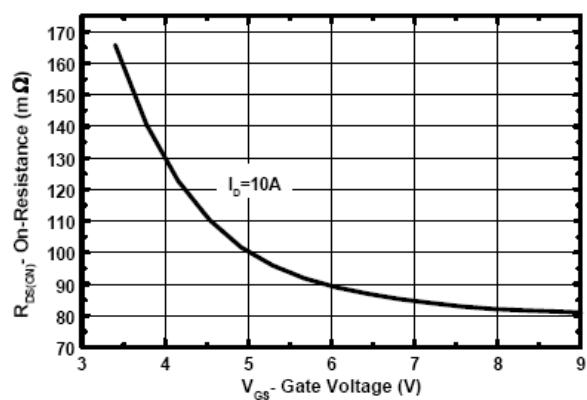


Figure 5. On-Resistance vs. Gate-to-Source Voltage

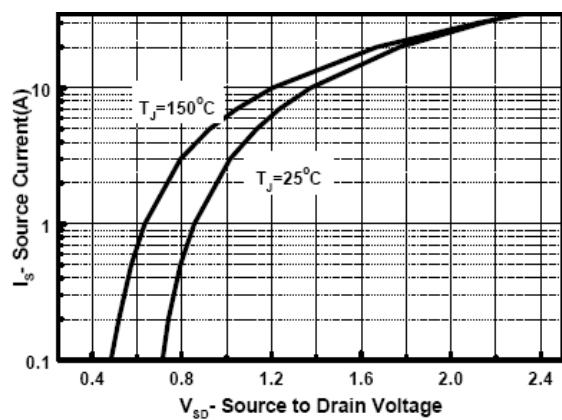
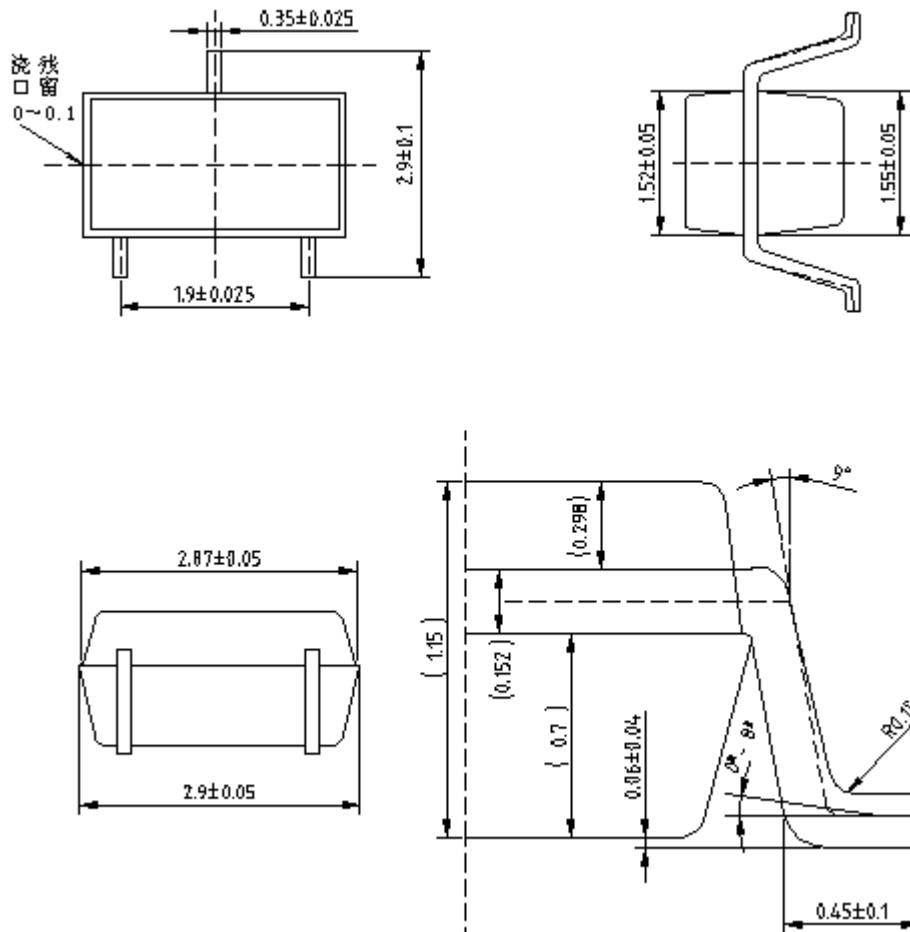


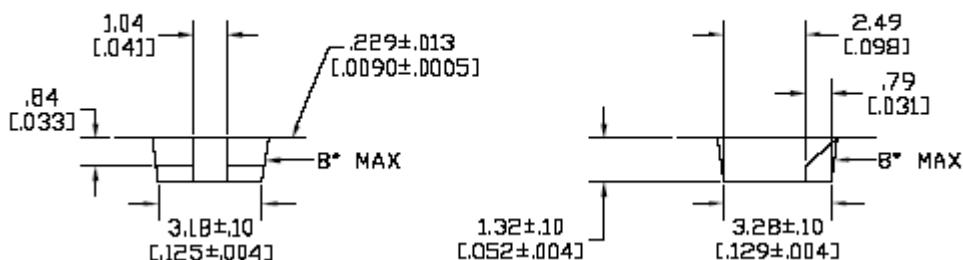
Figure 6. Source-Drain Diode Forward Voltage

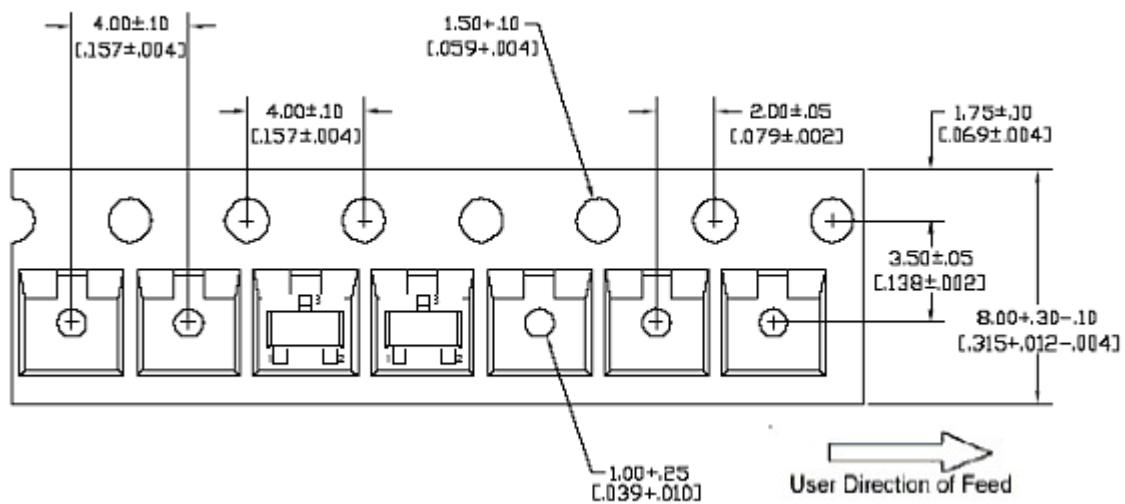
Package Outline Dimensions (UNIT: mm)

SC-59



SC-59 Carrier Tape





SC-59 Carrier Reel

