## Clock OSC

## SG7050CBN

Product name SG7050CBN f Product Number / Ordering code

133.330000MHz TJGA X1G0044910004xx

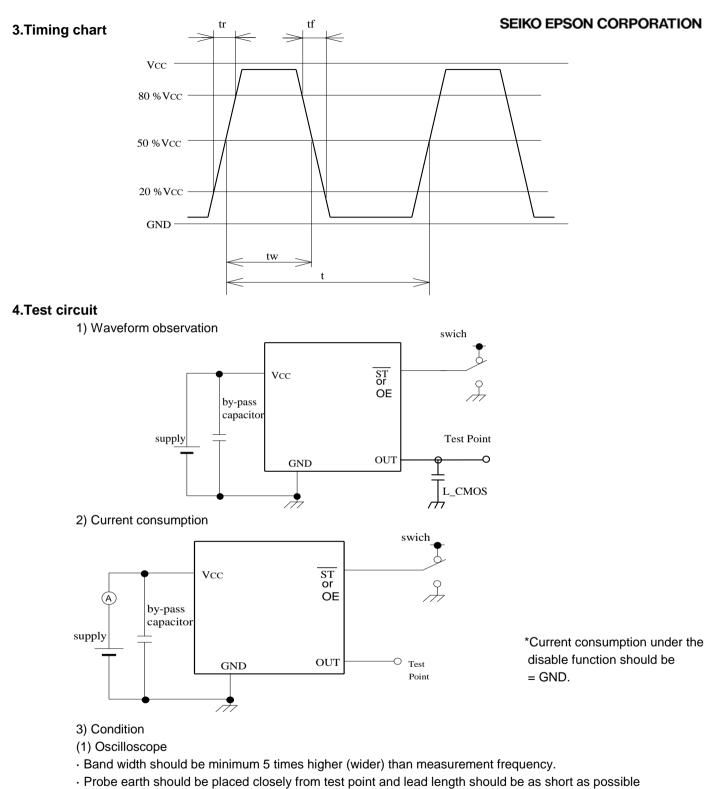
Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

Pb free / Complies with EU RoHS directive Reference weight Tvp 147 mg

Reference weight Typ. 147 mg						
1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	5	V	-
Storage temperature	T_stg	-40	-	+125	°C	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

2.Specifications(charac	teristics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO		133.3300		MHz	
Supply voltage	Vcc	1.6	-	3.63	V	-
Operating temperature	T_use	-40	-	+85	٥C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	lcc	-	-	11	mA	No load condition
Stand-by current	I_std	-	-	10.0	μA	ST = GND
Disable current	I_dis	-	-	-	mA	-
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V <sub>OH</sub>	Vcc-0.4	-	-		-
	V <sub>OL</sub>	-	-	0.4		-
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V <sub>IH</sub>	0.8Vcc	-	-		ST terminal
	V <sub>IL</sub>	-	-	0.2Vcc		ST terminal
Rise time	t <sub>r</sub>	-	-	3	ns	Vcc1.6V : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	3	ns	Vcc1.6V : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	5	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	-	0	-	ps	Deterministic Jitter Vcc=3.3V
	t <sub>RJ</sub>	-	2.1	-	ps	Random Jitter Vcc=3.3V
	t <sub>RMS</sub>	-	2.1	-	ps	δ(RMS of total distribution) Vcc=3.3V
	t <sub>p-p</sub>	-	17.8	-	ps	Peak to Peak Vcc=3.3V
	t <sub>acc</sub>	-	-	-	ps	
Phase jitter	t <sub>PJ</sub>	-	0.3	-	ps	Off set Frequency: 12kHz to 20MHz Vcc=3.3V
Phase noise	L(f)	-	-	-	dBc/Hz	-
		-	-57	-	dBc/Hz	Off set 10Hz Vcc=3.3V
		-	-86	-	dBc/Hz	Off set 100Hz Vcc=3.3V
		-	-115	-	dBc/Hz	Off set 1kHz Vcc=3.3V
		-	-128	-	dBc/Hz	Off set 10kHz Vcc=3.3V
		-	-137	-	dBc/Hz	Off set 100kHz Vcc=3.3V
		-	-146	-	dBc/Hz	Off set 1MHz Vcc=3.3V
Frequency aging	f_age	-5	-	5	x10 <sup>-6</sup>	@+25°C first year
		-	-	-		-



\* Recommendable to use miniature socket. (Don't use earth lead.)

(2) L\_CMOS also includes probe capacitance.

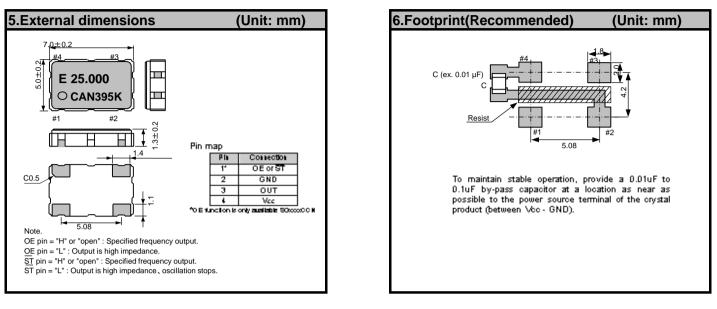
- (3) By-pass capacitor (0.01  $\mu$ F to 0.1  $\mu$ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.

(5) Power supply

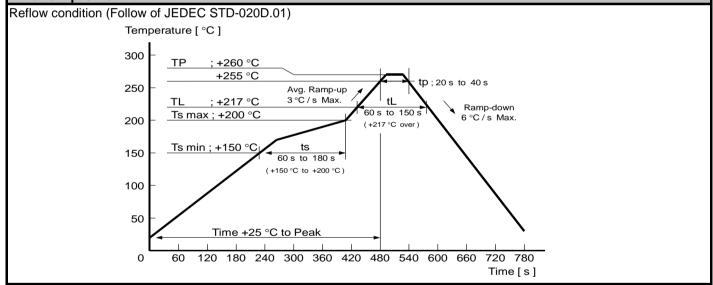
 $\cdot$  Start up time (0 %VCC to 90 %VCC) of power source should be more than 150  $\mu s.$ 

 $\cdot$  Impedance of power supply should be as lowest as possible.

### SEIKO EPSON CORPORATION



### 7.Reflow profile

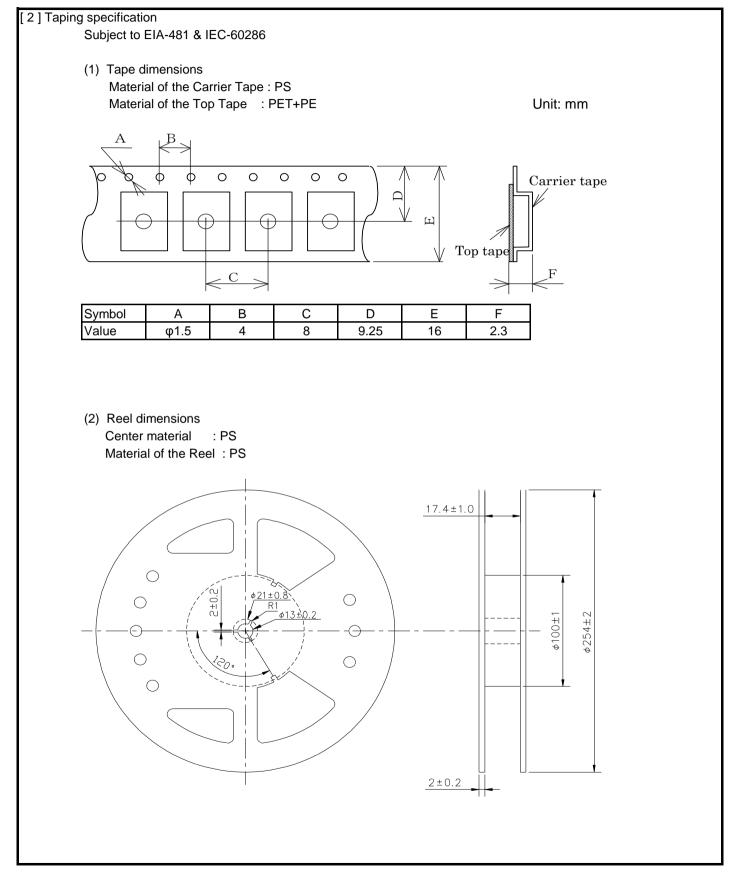


#### 8.Packing information

[1	]Product number last 2	digits	code(xx) description
	X1G0044910	004 yy	•

The recommended code is "00"

Code	Condition	Code	Condition
01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
11	Any Q'ty / Reel	00	1000pcs / Reel
12	250pcs / Reel		



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