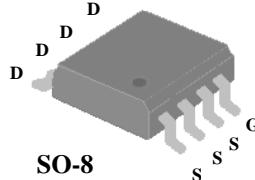
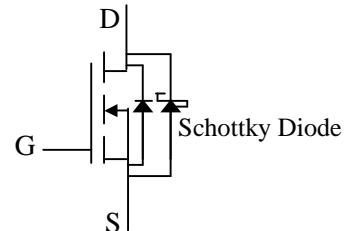




- ▼ Simple Drive Requirement
- ▼ Good Recovery Time
- ▼ Fast Switching Performance
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	30V
$R_{DS(ON)}$	9mΩ
I_D	13A



Description

AP4813 series are from Advanced Power innovative design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The SO-8 package is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for voltage conversion or switch applications.

Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}^3$	13	A
$I_D @ T_A=70^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}^3$	10.6	A
I_{DM}	Pulsed Drain Current ¹	50	A
V_{KA}	Schottky Reverse Voltage	30	V
$I_F @ T_A=25^\circ\text{C}$	Forward Current	1	A
I_{FM}	Pulsed Diode Forward Current	25	A
$P_D @ T_A=25^\circ\text{C}$	Max Power Dissipation (MOSFET)	2.5	W
	Max Power Dissipation (Schottky)	2.0	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³ (MOSFET)	50	°C/W
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³ (Schottky)	60	°C/W



Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=12\text{A}$	-	-	9	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	-	-	15	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	-	3	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=8\text{A}$	-	20	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	100	μA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	mA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge	$I_{\text{D}}=8\text{A}$	-	11.5	18	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=15\text{V}$	-	2.5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=4.5\text{V}$	-	7	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}$	-	9	-	ns
t_r	Rise Time	$I_{\text{D}}=1\text{A}$	-	7	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{\text{GS}}=10\text{V}$	-	23	-	ns
t_f	Fall Time	$R_D=15\Omega$	-	8	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	730	1170	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	205	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	150	-	pF
R_g	Gate Resistance	$f=1.0\text{MHz}$	-	1.5	-	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Diode+Schottky Forward On Voltage ²	$I_{\text{S}}=1.0\text{A}, V_{\text{GS}}=0\text{V}$	-	0.48	0.5	V
t_{rr}	Body Diode+Schottky Reverse Recovery Time	$I_{\text{S}}=8\text{A}, V_{\text{GS}}=0\text{V},$	-	20	-	ns
Q_{rr}	Body Diode+Schottky Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$	-	9	-	nC

Notes:

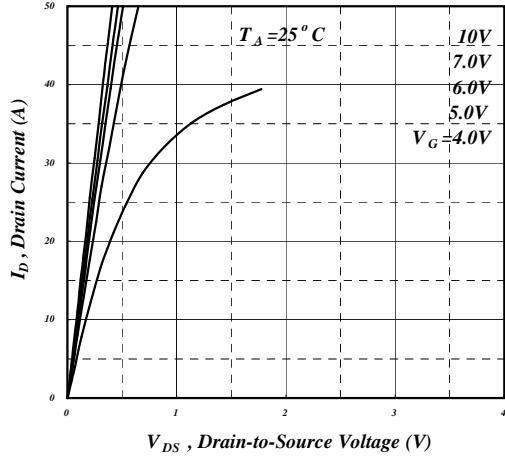
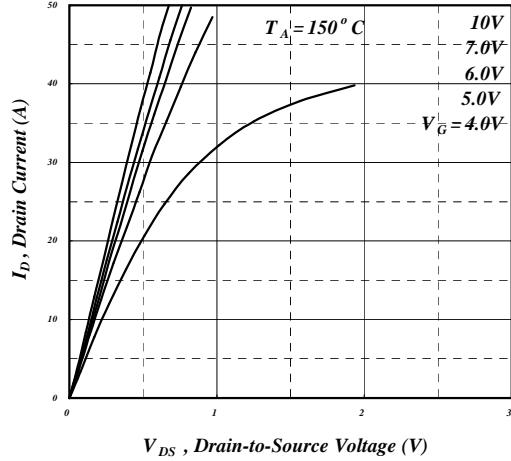
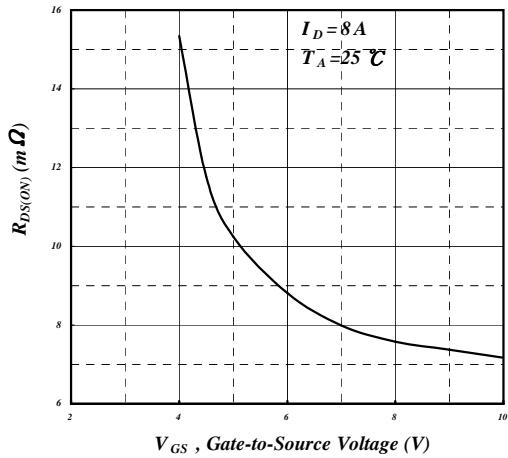
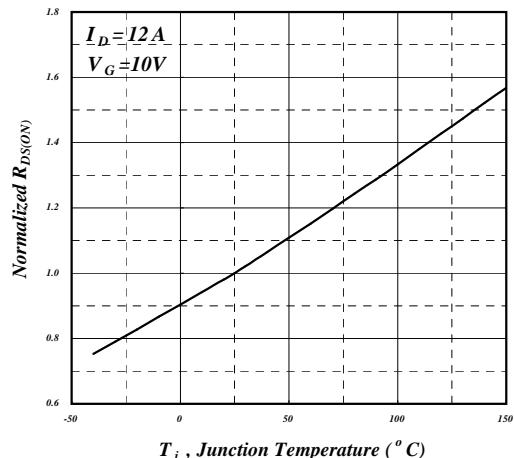
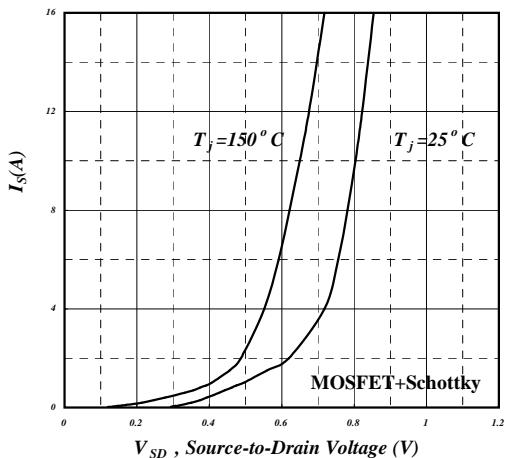
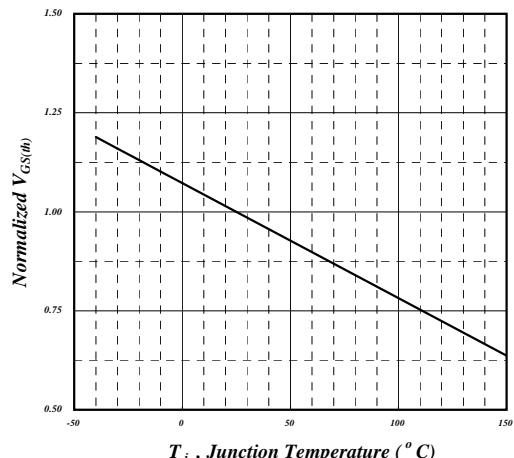
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board, t \leq 10 sec.

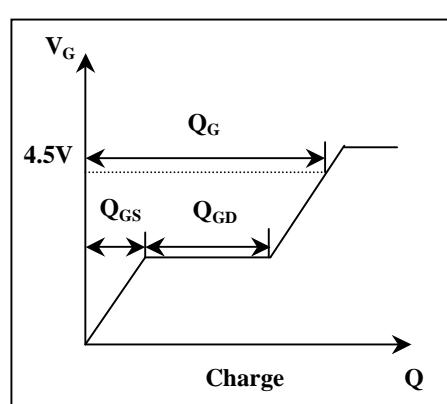
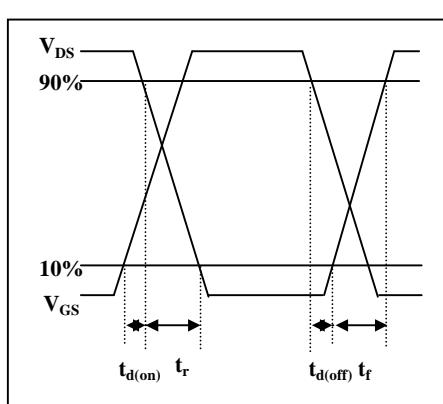
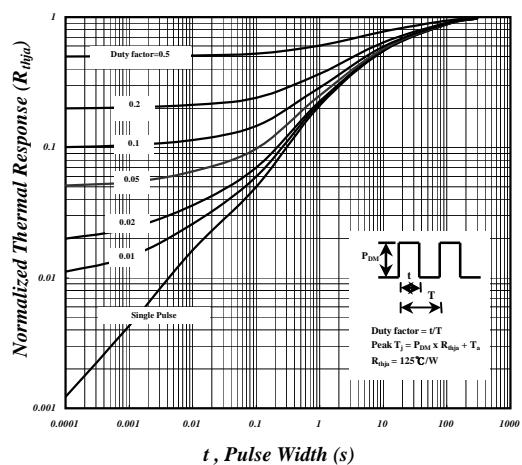
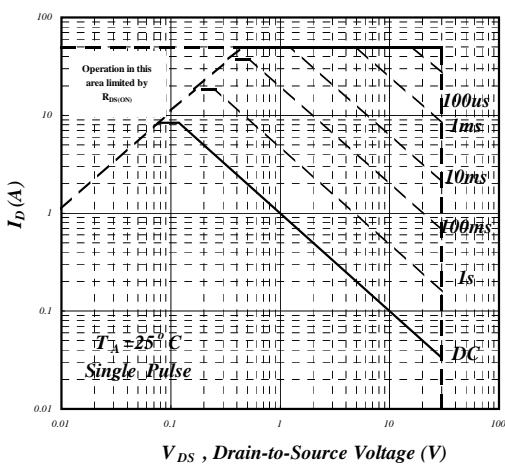
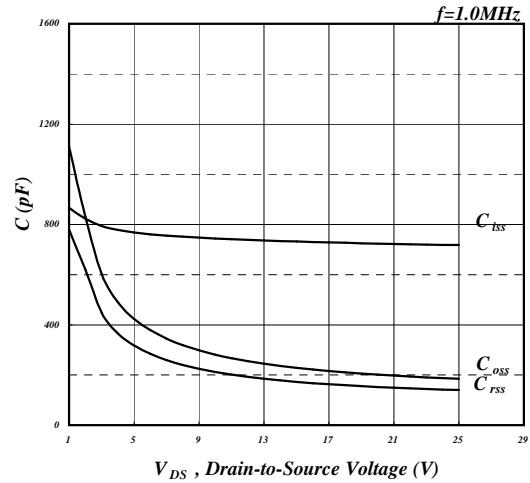
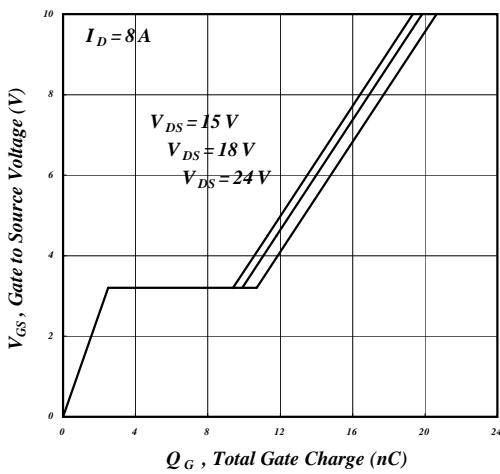
THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

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Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics

Fig 3. On-Resistance v.s. Gate Voltage

Fig 4. Normalized On-Resistance v.s. Junction Temperature

Fig 5. Forward Characteristic of Reverse Diode

Fig 6. Gate Threshold Voltage v.s. Junction Temperature





MARKING INFORMATION

