

# Motorola Semiconductor Technical Data

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## *Addendum to* **MC68HC908GR8**

This addendum provides changes to the MC68HC908GR8 Technical Data Book Rev 4 (Motorola document order number MC68HC908GR8/D).

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### Incorrect Register Addresses

#### **Incorrect addresses:**

Address of Interrupt Status Register 1 (INT1) should be \$FE04, not \$FE09

Address of Interrupt Status Register 2 (INT2) should be \$FE05, not \$FE0A

Address of Interrupt Status Register 3 (INT3) should be \$FE06, not \$FE0B

#### **Locations:**


In 4th, 5th and 6th bullets of page 36, Memory Map section.

Near bottom of memory map on page 37, Memory Map section.

In bottom two registers on page 45 and top register on page 46, Memory Map section.

In last three registers on page 274, SIM section.



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#### How to reach us:

**USA/EUROPE:** Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 1-303-675-2140

**HOME PAGE:** <http://motorola.com/sps/>

**JAPAN:** Motorola Japan Ltd.; SPS, Technical Information Center, 3-20-1, Minami-Azabu, Minato-ku, Tokyo 106-8573 Japan.  
81-3-3440-3569

**ASIA/PACIFIC:** Motorola Semiconductors H.K. Ltd.; Silicon Harbour Centre, 2 Dai King Street, Tai Po Industrial Estate,  
Tai Po, N.T., Hong Kong. 852-266668334

**CUSTOMER FOCUS CENTER:** 1-800-521-6274



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