

100V N-Channel MOSFET

Voltage

Current 13 A

Features

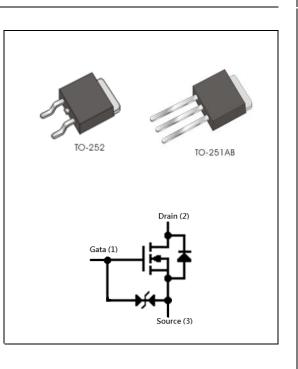
- R_{DS(ON)}, V_{GS}@10V,I_D@6.5A<115mΩ
- High power and current handing capability

100 V

- Low Gate Charge
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: TO-252, TO-251AB Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0104 ounces, 0.297 grams(TO-252)
- Approx. Weight: 0.0104 ounces, 0.297 grams(TO-251AB)
- Marking: D13N10(TO-252), U13N10(TO-251AB)



Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	13	А
Pulsed Drain Current		I _{DM}	52	А
Single Pulse Avalanche Energy (Note 1)		E _{AS}	30	mJ
Power Dissipation	T _C =25°C	PD	34.7	W
	Derate above 25°C		0.28	W/°C
Operating Junction andStorage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance				
- Junction to Case		$R_{ extsf{ heta}JC}$	3.6	°C/W
- Junction to Ambient		$R_{ extsf{ heta}JA}$	110	

• Limited only By Maximum Junction Temperature



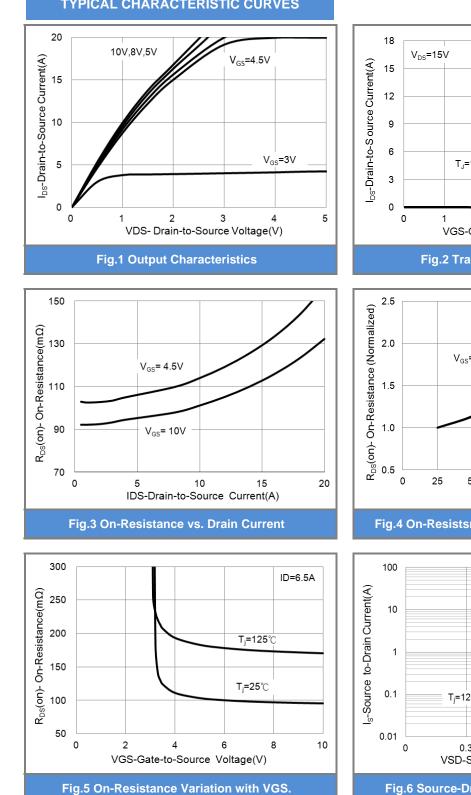
Electrical Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		1	1	1	1	1
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, I _D =250uA	1.5	2	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =10V,I _D =6.5A	-	95	115	mΩ
		V _{GS} =4.5V,I _D =2A	-	105	140	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =80V,V _{GS} =0V	-	0.02	1.0	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 16V,V _{DS} =0V	-	<u>+</u> 4	<u>+</u> 10	uA
Diode Forward Voltage	V_{SD}	I _S =3A,V _{GS} =0V	-	0.8	1.1	V
Dynamic						
Total Gate Charge	Qg		-	20.4	-	
Gate-Source Charge	Q_{gs}	Q_{gs} $V_{DS}=50V, I_D=10A,$		3.2	-	nC
Gate-Drain Charge	Q_gd	V _{GS} =10V ^(Note 2,3)	-	4.3	-	
Input Capacitance Ciss			-	906	-	
Output Capacitance	Coss	V _{DS} =30V, V _{GS} =0V,	-	63	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	33	-	
Switching						
Turn-On Delay Time	td _(on)	V _{DS} =30V,RL=3Ω,	-	5	-	ns
Turn-On Rise Time	t _r	V _{GEN} =10V, R _G =6Ω _(Note 2,3)	-	25	-	
Turn-Off Delay Time	td _(off)		-	26	-	
Turn-Off Fall Time	t _f		-	7	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	n Continuous Drain-Source					
Diode Forward Current	I _S		-	-	3	A
Reverse Recovery Time	trr	V _{GS} =0V, I _S =3.5A	-	44	-	ns
Reverse Recovery Charge	Qrr	dI _F / dt=100A/us ^(Note 2)	-	80	-	nC

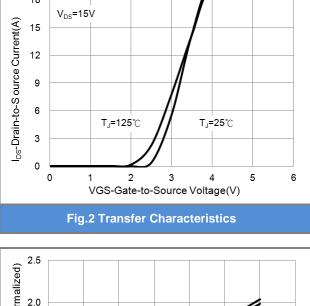
NOTES :

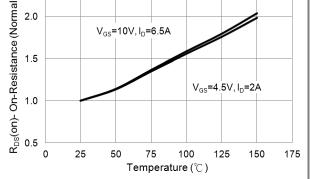
- 1. L=0.3mH, I_{AS}=10A, V_{DD}=25V, V_{GS}=10V, R_G=25ohm, Starting T_J=25^{o}C
- 2. Pulse width \leq 300 us, Duty cycle \leq 2%
- 3. Essentially independent of operating temperature typical characteristics.



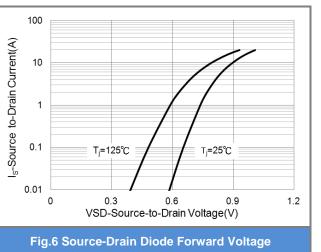


TYPICAL CHARACTERISTIC CURVES











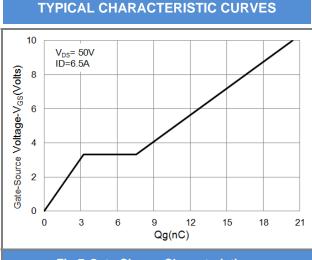


Fig.7 Gate-Charge Characteristics

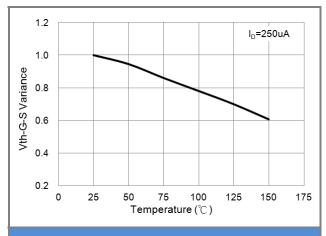
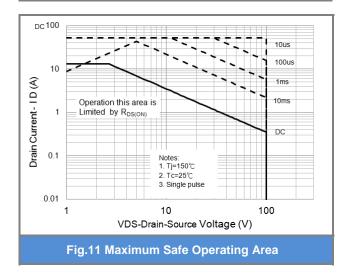
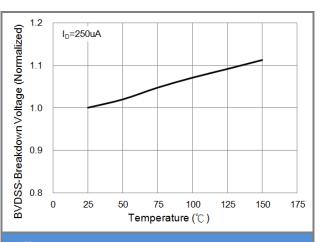


Fig.9 Threshold Voltage Variation with Temperature







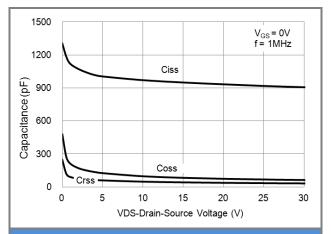
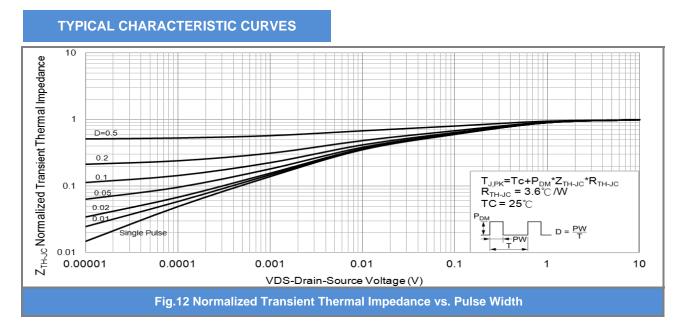


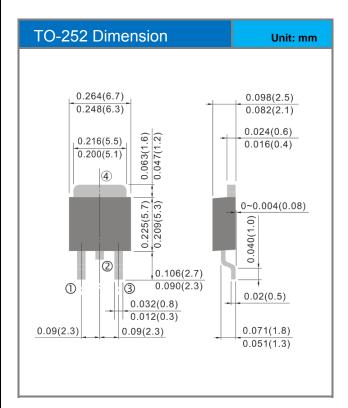
Fig.10 Capacitance vs. Drain-Source Voltage

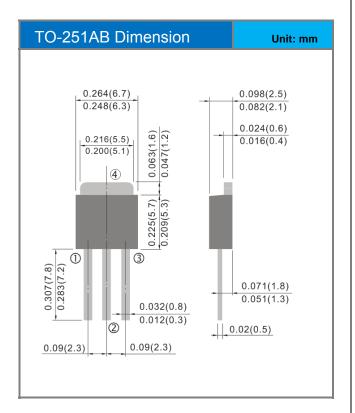






Packaging Information



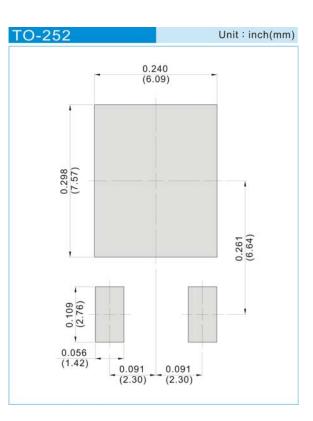




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD13N10_L2_00001	TO-252	13" tape & reel	D13N10	Halogen free
PJU13N10_T0_00001	TO-251AB	Tube packing	U13N10	Halogen free

MOUNTING PAD LAYOUT





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