Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIV)

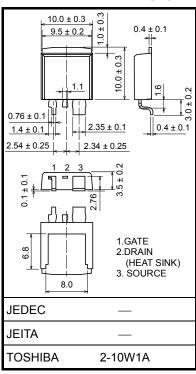
TK80F08K3

Swiching Regulator

- Low drain-source ON-resistance: R_{DS} (ON) = 3.4 m Ω (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 75 \text{ V)}$
- Enhancement-model: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

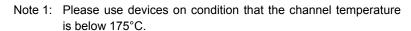
Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	75	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	75	V	
Gate-source voltage			V _{GSS}	±20	٧	
Drain current	DC	(Note 1)	I _D	80	Α	
	Pulse	(Note 1)	I _{DP}	320		
Drain power dissipation (Tc = 25°C)			PD	300	W	
Single pulse avalanche energy (Note 2)			E _{AS}	250	mJ	
Avalanche current			I _{AR}	80	Α	
Repetitive avalanche energy (Note 3)			E _{AR}	30	mJ	
Channel temperature (Note 4)			T _{ch}	175	°C	
Storage temperature range (Note 4)			T _{stg}	-55 to 175	°C	



Weight: 1.07 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.5	°C/W



Note 2: $V_{DD} = 25$ V, $T_{ch} = 25$ °C, L = 58 μH , $R_G = 1$ Ω , $I_{AR} = 80$ A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

Note 4: 175°C refers to AEC-Q101.

Note 5: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic sensitive device. Please handle with caution.

Start of commercial production 2012-06

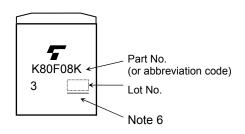
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off curre	Orain cut-off current		V _{DS} = 75 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	75	_	_	V
		V (BR) DSX	$I_D = 10$ mA, $V_{GS} = -20$ V	50	_	_	
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 40 A	_	3.4	4.3	mΩ
Input capacitance		C _{iss}		_	8200	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	770	_	
Output capacitance		Coss		_	1140	_	
Switching time	Rise time	t _r	V_{GS} $V_{DD} \approx 30 \text{ V}$	_	30	_	- ns
	Turn-on time	t _{on}		_	55	_	
	Fall time	tf		_	33	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	150	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	175	_	nC
Gate-source charge1		Q _{gs1}	$V_{DD} \approx 60 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 80 \text{ A}$	_	40	_	
Gate-drain ("miller") charge		Q _{gd}		_	65	_	
Gate switch charge		Q _{sw}		_	80	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	80	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	320	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 80 A, V _{GS} = 0 V	_	-0.9	-1.2	٧
Reverse recovery time	t _{rr}	$I_{DR} = 80 \text{ A}, V_{GS} = 0 \text{ V},$	_	60	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/μs	_	60	_	nC

Marking



Note 6: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



Moisture-Proof Packing

The TK150F04K3L is packed in a moisture-proof laminated aluminum bag.

Precautions for Transportation and Storage

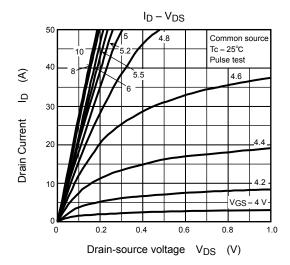
- (1) Avoid excessive vibration during transportation.
- (2) Do not toss or drop the packed devices to avoid ripping of the bag.
- (3) After opening the moisture-proof bag, the devices should be assembled within two weeks in an environment of 5°C to 30°C and RH70% or below. Perform reflow at most twice.
- (4) The moisture-proof bag may be stored unopened for up to 12 months at 5°C to 30°C and RH90% or below.
- (5) If, upon opening the bag, the moisture indicator card shows humidity of 30% or above (the color of the 30% dot has changed from blue to pink) or the expiration date has passed, the devices should be baked as follows: Baking conditions: 125°C for 48 hours.

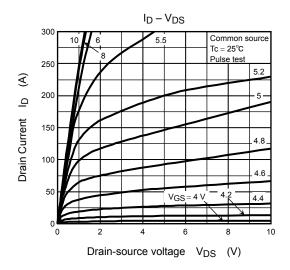
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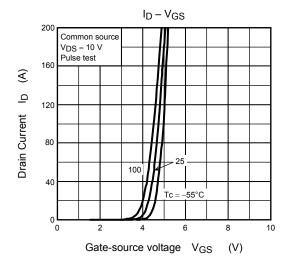
Since the tape materials are not heat-proof, devices should be placed on either heat-proof trays or aluminum magazines when baking.

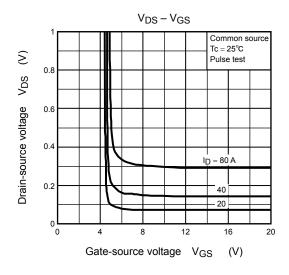


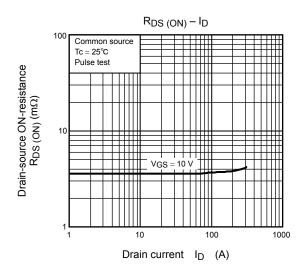
The humidity indicator shows an approximate ambient humidity at 25°C. If the ambient humidity is below 30%, the color of all the indicator dots is blue. If, upon opening the bag, the color of the 30% dot has changed from blue to pink, the devices should be baked before assembly.

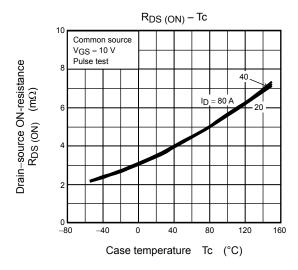


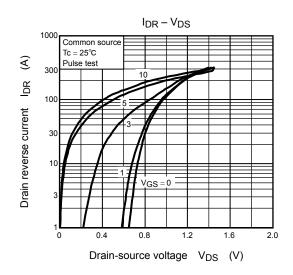


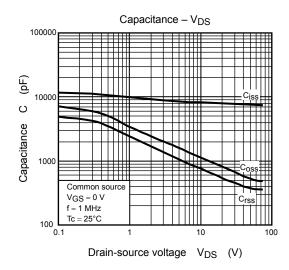


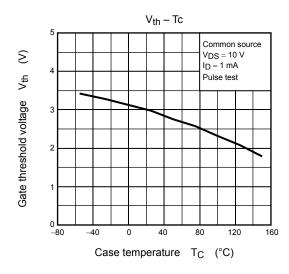


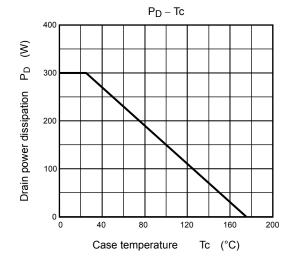


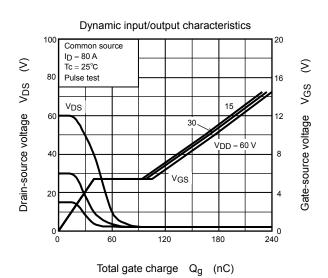


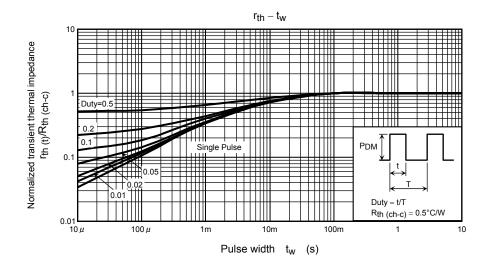


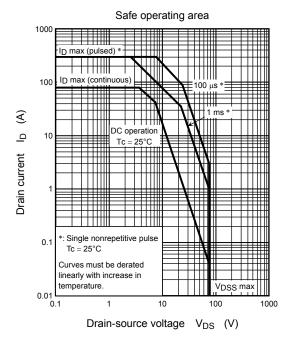


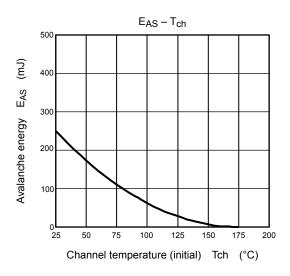


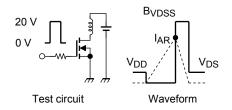












$$\begin{split} R_G &= 1~\Omega \\ V_{DD} &= 25~V,~L = 58~\mu H \end{split}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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