

# MGBR2U40

Preliminary

DIODE

# MOS GATED BARRIER RECTIFIER

## DESCRIPTION

The UTC **MGBR2U40** is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed etc.

The UTC MGBR2U40 suitable for supply applications.

#### FEATURES

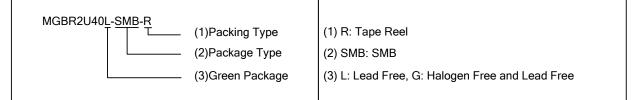
\* Ultra low forward voltage drop

\* High switching speed

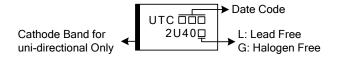
### SYMBOL

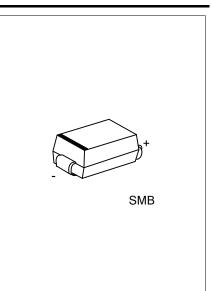
#### ORDERING INFORMATION

Ordering Number		Deekege	Pin Assignment		Decking
Lead Free	Halogen Free	Package	1	2	Packing
MGBR2U40L-SMB-R	MGBR2U40G-SMB-R	SMB	K	А	Tape Reel
Note: Pin Assignment: A: Anode K: Cathode					



#### MARKING





#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%

T OF Capacitance load, defate current by 2070.				
PARAMETER	SYMBOL	RATINGS	UNIT	
DC Blocking Voltage	V <sub>RM</sub>	40	V	
Working Peak Reverse Voltage	V <sub>RWM</sub>	40	V	
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	40	V	
Average Rectified Forward Current	I	2	٨	
(Rated VR-20Khz Square Wave) - 50% Duty Cycle	I <sub>O</sub>	Ζ	A	
Peak Forward Surge Current - 1/2 60hz	I <sub>FSM</sub>	50	А	
Peak Repetitive Reverse Surge Current (2uS-1Khz)	I <sub>RRM</sub>	2	А	
Maximum Rate of Voltage Change ( at Rated $V_R$ )	dv/dt	10000	V/µS	
Operating Junction Temperature	TJ	-65~+150	°C	
Storage Junction Temperature	T <sub>STG</sub>	-65~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θյΑ	90	°C/W

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V <sub>(BR)R</sub>	I <sub>R</sub> =0.5mA	40			V
Forward Voltage	VEM	I <sub>F</sub> =2A, T <sub>J</sub> =25°C			0.43	V
		I <sub>F</sub> =2A, T <sub>J</sub> =125°C			0.39	V
Reverse Current (Note 1)	RM	V <sub>R</sub> =40V, T <sub>J</sub> =25°C			500	μA
		V <sub>R</sub> =40V, T <sub>J</sub> =125°C			50	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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