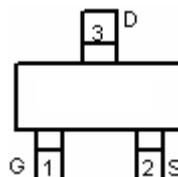
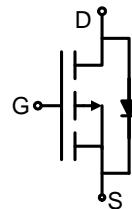


**General Features**

- $V_{DS} = -30V, I_D = -4.1A$
- $R_{DS(ON)} < 95m\Omega @ V_{GS}=-4.5V$
- $R_{DS(ON)} < 65m\Omega @ V_{GS}=-10V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

**Lead Free****Application**

- PWM applications
- Load switch
- Power management

**Marking and pin assignment****PIN Configuration****SOT-23 top view****Schematic diagram****Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
	MSP3407	SOT-23	Ø180mm	8 mm	3000 units

**Absolute Maximum Ratings ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-4.1	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-20	A
Maximum Power Dissipation	$P_D$	1.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

**Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	90	$^\circ C/W$
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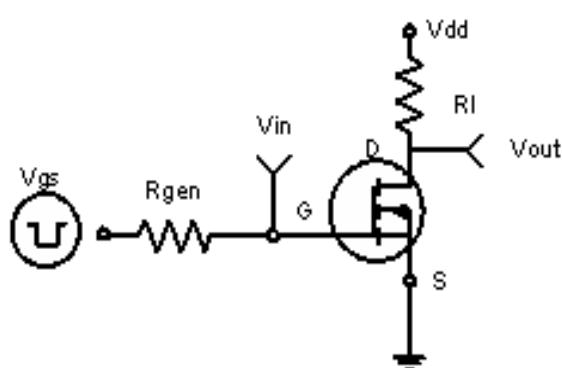
**Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-33	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-3	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-4.1\text{A}$	-	48	65	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4\text{A}$	-	60	95	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-4.1\text{A}$	5.5	-	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	650	-	PF
Output Capacitance	$C_{\text{oss}}$		-	105	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	65	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=-15\text{V}, R_{\text{L}}=3.6\Omega$ $V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=3\Omega$	-	8.5	-	nS
Turn-on Rise Time	$t_{\text{r}}$		-	4.5	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	26	-	nS
Turn-Off Fall Time	$t_{\text{f}}$		-	12.5	-	nS
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4\text{A}, V_{\text{GS}}=-10\text{V}$	-	12.5	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	2.8	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	2.7	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-4.1\text{A}$	-	-	-1.2	V

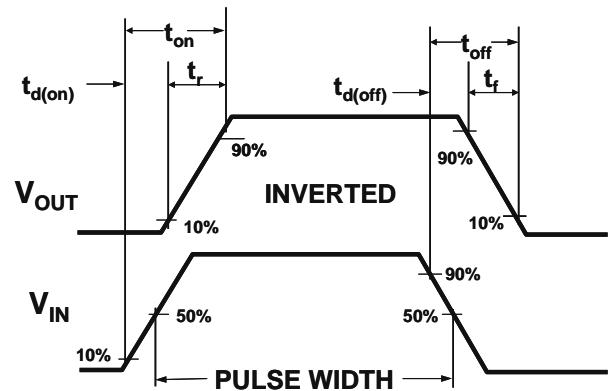
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

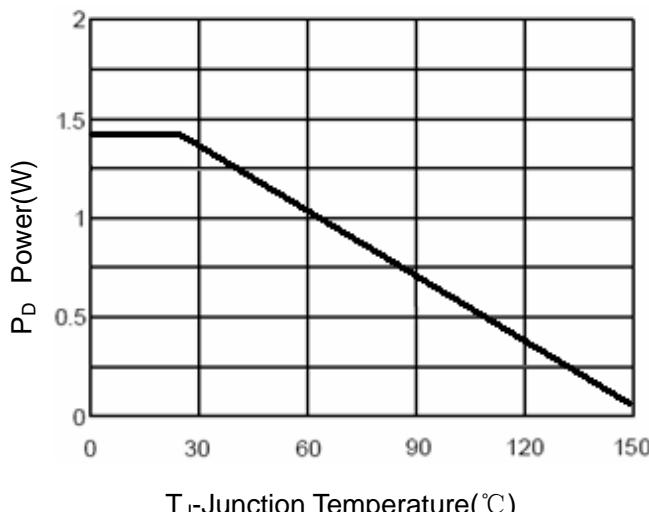
### Typical Electrical and Thermal Characteristics



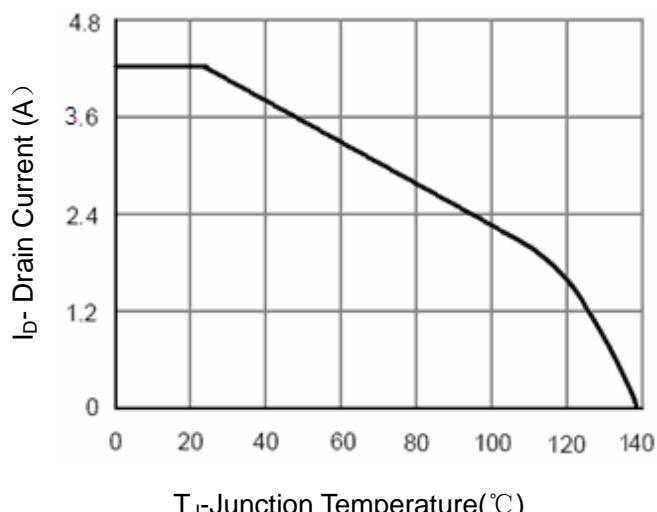
**Figure 1:Switching Test Circuit**



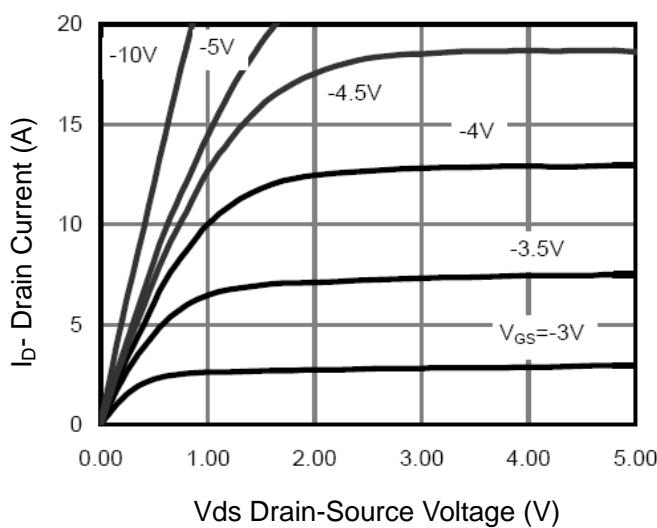
**Figure 2:Switching Waveforms**



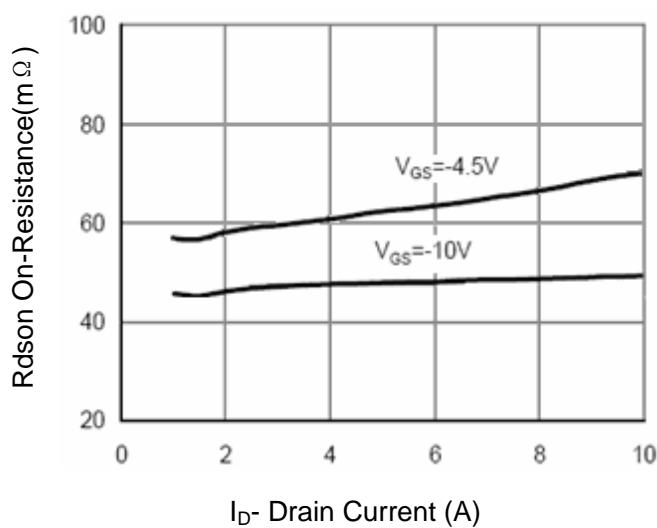
**Figure 3 Power Dissipation**



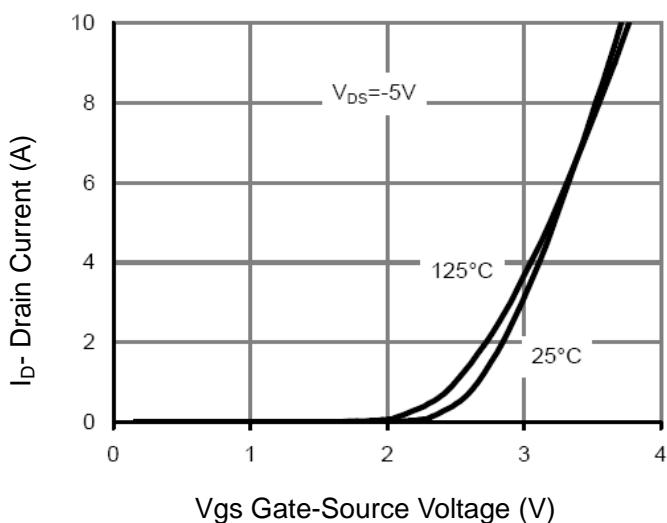
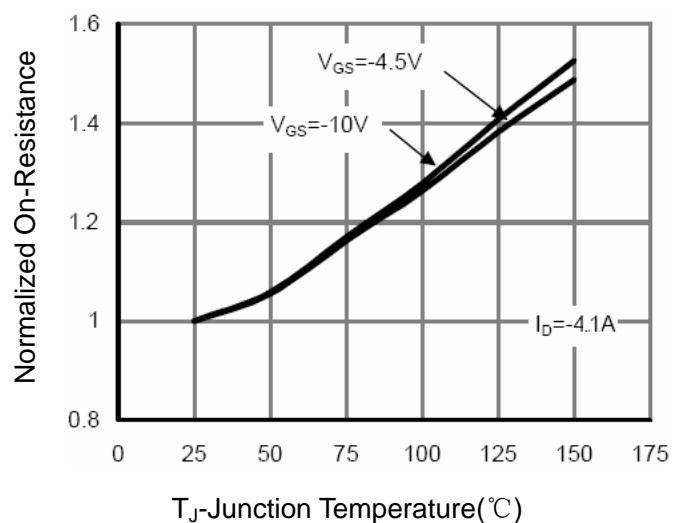
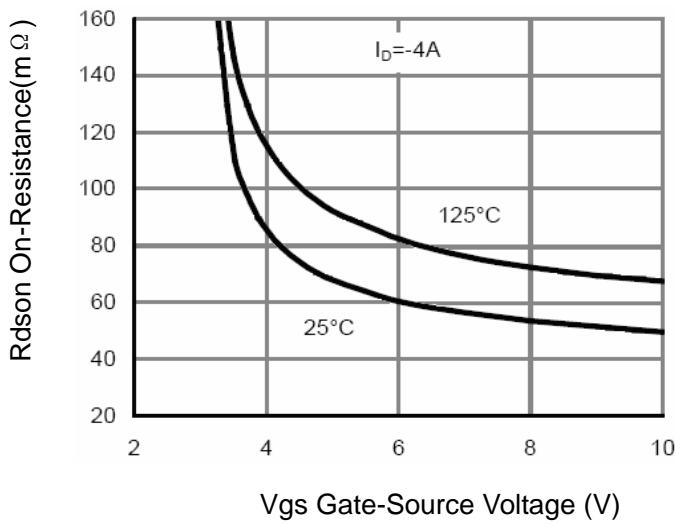
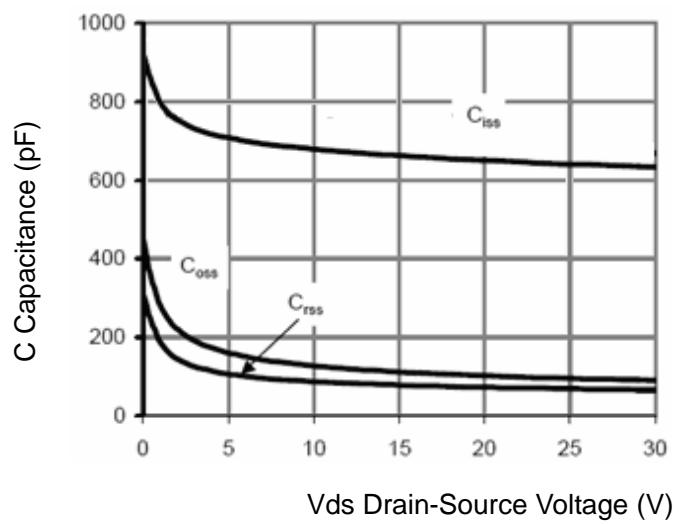
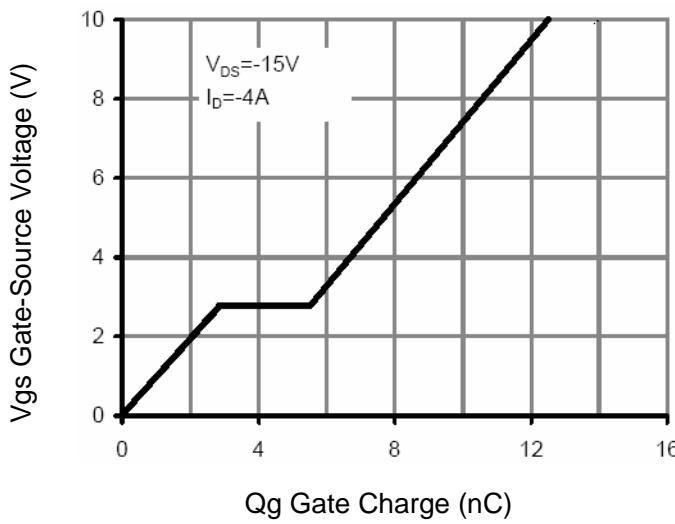
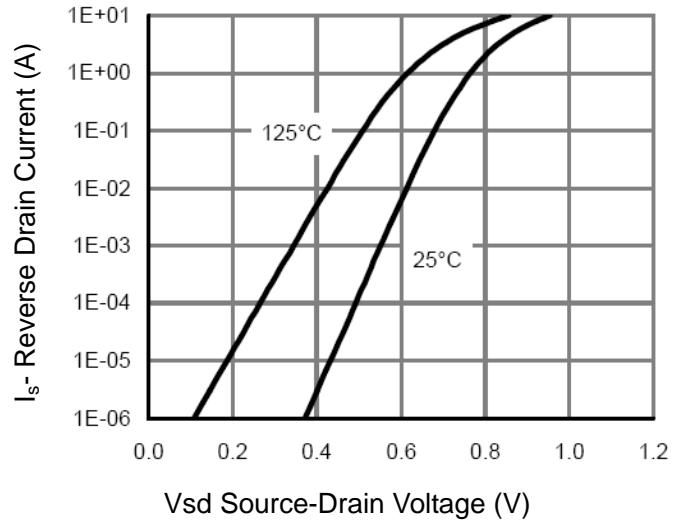
**Figure 4 Drain Current**

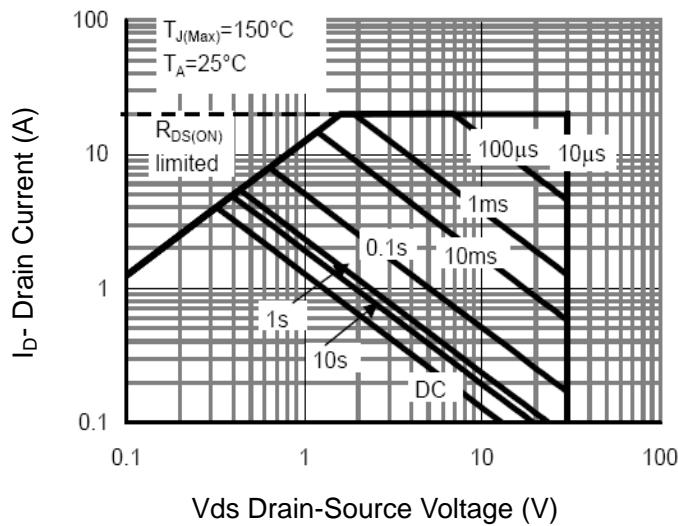
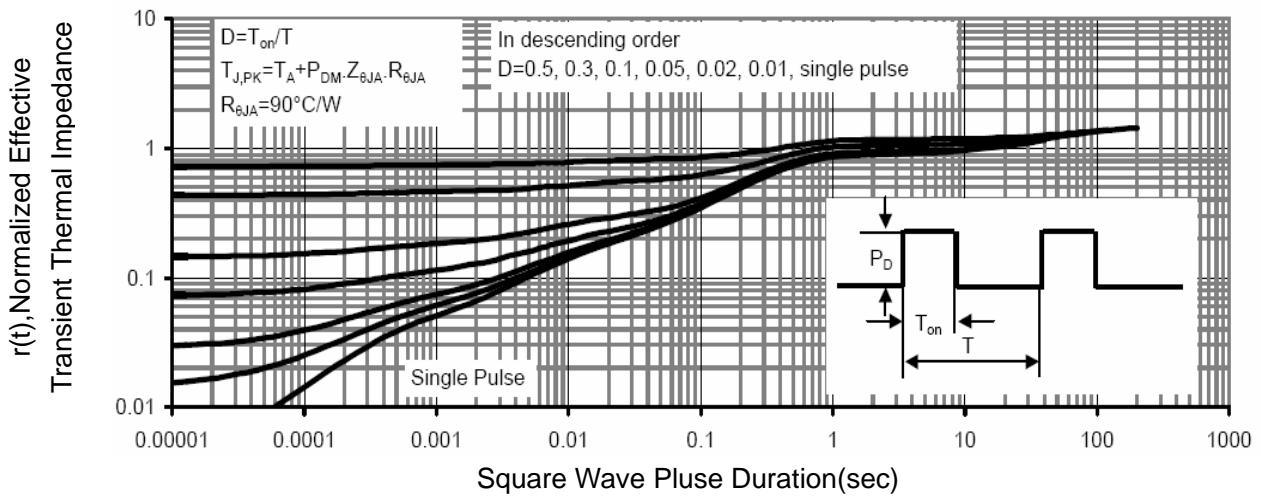


**Figure 5 Output Characteristics**

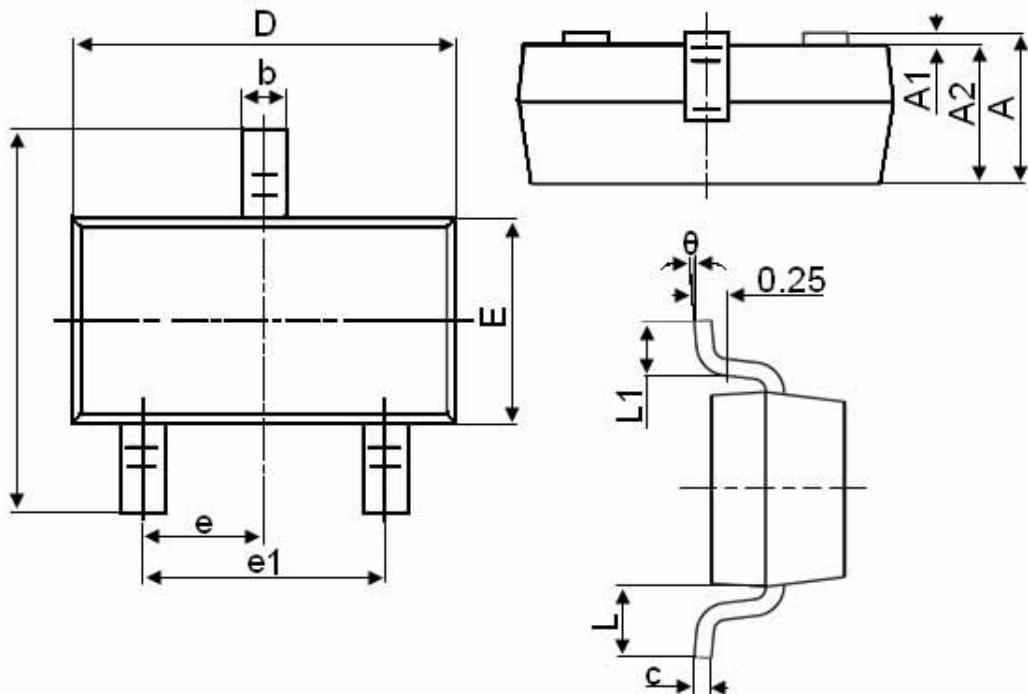


**Figure 6 Drain-Source On-Resistance**

**Figure 7 Transfer Characteristics****Figure 8 Drain-Source On-Resistance****Figure 9  $R_{DSON}$  vs  $V_{GS}$** **Figure 10 Capacitance vs  $V_{DS}$** **Figure 11 Gate Charge****Figure 12 Source- Drain Diode Forward**

**Figure 13 Safe Operation Area****Figure 14 Normalized Maximum Transient Thermal Impedance**

## SOT-23 Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

## Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.