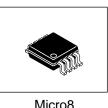
LA6595DM

Monolithic Linear IC BTL Drive Single-Phase Full-Wave Fan Motor Driver



Overview

The LA6595T is a single-phase bipolar fan motor driver that achieves quite operation, power savings, silent operation and high efficiency that suppresses reactive current through BTL output linear drive. It provides lock protection and rotation detection circuits on chip, and is optimal for applications that require high reliability and low noise, such as notebook personal computers, power supplies in consumer electronic equipment, car audio, and CPU cooling systems.



Features

- BTL output single-phase full-wave linear drive (gain resistor : 1 to $360k\Omega$, 51dB)
- Supports low-voltage drive and features a wide usable voltage range (2.2 to 14.0V)
- Low saturation output (high side + low side saturation voltage : V_{O} sat (total) = 1.2V (typical), I_{O} = 200mA)
- Built-in lock protection and automatic return circuits
- Built-in RD (Rotation Detection) output
- Thermal protection circuit
- Extra-small package (Micro8)

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC} max		15	V
Output current	IOUT max		0.5	А
Output voltage	V _{OUT} max		15	V
RD output pin output withstand voltage	V _{RD} max		15	V
RD output current	I _{RD} max		10	mA
Allowable power dissipation	Pd max	Mounted on a specified board*	400	mW
Operating temperature	Topr		-30 to +90	°C
Storage temperature	Tstg		-55 to +150	°C

* Specified board : 20.0mm \times 10.0mm \times 0.8mm, glass epoxy board.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

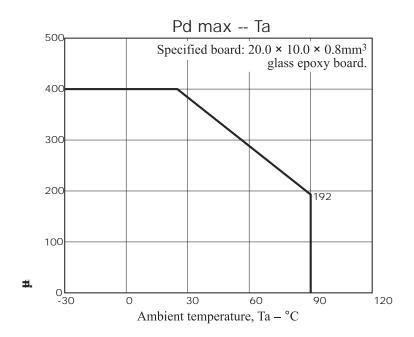
Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		2.2 to 14.0	V
Common-phase input voltage range of hall input	VICM		0 to V _{CC} -1.5	V

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

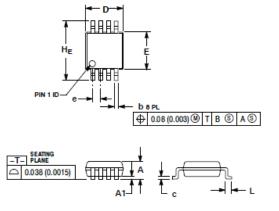
Electrical Characteristics	(P				
Parameter				Ratings		
Parameter	Symbol	Conditions	min	typ	max	Unit
Circuit current	cb1	Drive mode (CT = low)	3	6	9	mA
	I _{CC} 2	Lock protection mode (CT = high)	2.5	5	7.5	mA
Lock detection capacitor charge current	I _{CT} 1		0.9	1.2	1.5	FA
Capacitor discharge current	CT ^{I2}		0.10	0.18	0.25	FA
Capacitor charge/discharge curren ratio	ntR _{CT}	$R_{CD} = I_{CT} 1 / I_{CT} 2$	5	6.5	8	
CT charge voltage	₩ _T 1		1.3	1.5	1.7	V
CT discharge voltage	& _T 2		0.3	0.5	0.7	V
OUT output low saturation voltage	olV	I _O = 200mA		0.25	0.4	5 V
OUT output high saturation voltage	e oł¥	I _O = 200mA		0.95	1.2	V
Hall input sensitivity	VHN	Zero peak value (including offset and hysteresis)		7	15	mV
RD output pin low-level voltage	RŬ	I _{RD} = 5mA		0.15	0.3	V
RD output pin leakage current	RDL	V _{RD} = 15V		1	30	FA



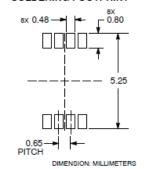
Package Dimensions

unit : mm (typ)

Micro8 CASE 846A-02 **ISSUE J**



RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER. 3. DIMENSIONA DOES NOT INCLUDE MOD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.009) FERSIDE. 4. DIMENSION B DOES NOT INCLUDE INT ERLEAD FLASH OR PROTRUSION INTRELAD FLASH OR PROTRUSIONS MILL NOT EXCEED 0.25 (0.010) PER SIDE. 5. 846A-01 OBSOLETE, NEW STANDARD 846A-02.

	MILLIMETERS			METERS INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α			1.10			0.043
A1	0.05	0.08	0.15	0.002	0.003	0.008
b	0.25	0.33	0.40	0.010	0.013	0.016
0	0.13	0.18	0.23	0.005	0.007	0.009
D	2.90	3.00	3.10	0.114	0.118	0.122
E	2.90	3.00	3.10	0.114	0.118	0.122
e	0.65 BSC				0.026 BSC	2
L	0.40	0.55	0.70	0.016	0.021	0.028
HE	4.75	4.90	5.05	0.187	0.193	0.199

GENERIC

MARKING DIAGRAM* XXXX AYW. 1 11111

XXXX = Specific Device Code A Y

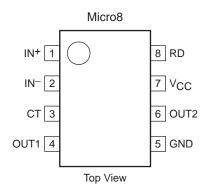
- = Assembly Location
- = Year W = Work Week

= Pb-Free Package
(Note: Microdot may be in either location)

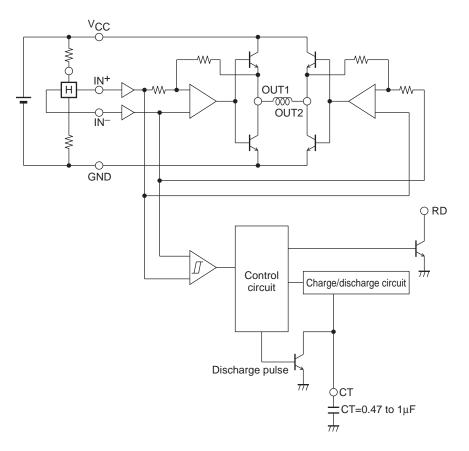
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

STYLE 1:	STYLE 2:	ST	YLE 3
PIN1.	SOURCE PIN 1.	SOURCE 1	PIN1. N-SOURCE
2	SOURCE 2	GATE 1	N-GATE
3.	SOURCE 3.	SOURCE 2	P-SOURCE
4.	GATE 4	GATE 2	P-GATE
5.	DRAIN 5.	DRAIN2	5. P-DRAIN
6.	DRAIN 6.	DRAIN2	P-DRAIN
7.	DRAIN 7.	DRAIN 1	N-DRAIN
8.	DRAN 8	DRAIN 1	8. N-DRAN

Pin Assignment



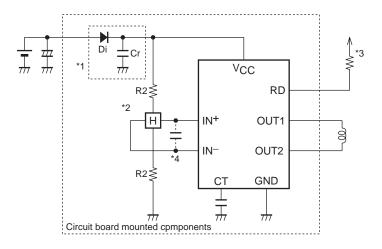
Block Diagram



Truth Table

IN ⁻	IN+	СТ	OUT1	OUT2	RD	Mode
High	Low	1	High	Low	Low	During antation
Low	High	Low	Low	High	Low	During rotation
_	-	High	Off	Off	Off	Lock protection

Application Circuit Example



- *1. If the diode Di (which protects the IC destruction by reverse connection) is used, it is necessary to insert the capacitor Cr and provide a regenerative current route. Similarly, if there is no nearby capacitor on the fan power supply line, Cr will also be necessary to improve reliability.
- *2. If the Hall sensor bias is taken from V_{CC}, a 1/2 V_{CC} bias, as shown in the figure, must be used. Linear drive is implemented by amplifying the Hall sensor output and applying voltage control to the coil. If the Hall effect sensor provides a strong output, the startup characteristics and efficiency will be good, then even quieter operation will be achieved by adjusting the Hall effect sensor.
- *3. This pin must be left open if unused.
- *4. If the line from the Hall sensor output to the Hall sensor input of IC are long, noise may enter the system from that line. If that becomes a problem, insert a capacitor as shown in the figure.

ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LA6595DMR2G	Micro8 (Pb-Free / Halogen Free)	4000 / Tape & Reel

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