



Size: 0.60 x 0.31 x 0.28 inches 15.3 x 8.0 x 7.1 mm

Weight: 0.08oz (2.2g)

### **FEATURES**

- 1 Watt Output Power
- Single & Dual Outputs
- High I/O Isolation: 3000VDC
- High Accuracy of Pin Planarity
- MTBF > 2,000,000 Hours
- -40°C to +85°C Operating Temperature
- SMT Package with Industry Standard Pin-out
- Qualified for Lead-free Reflow Solder Processes
   According to IPC/JEDEC J-STD-020D
- Tape & Reel Packaging Available
- Water-washable process available Efficiency up to 80%

# **DESCRIPTION**

The DCMSAU1 series of DC/DC converters provides 1 Watt of output power in an SMT package featuring high I/O isolation of 3000VDC. These converters operate over input voltage ranges of 4.5~5.5VDC, 10.8~13.2VDC, and 21.6~26.4VDC. This series also has single and dual output voltages of 5V, 12V, 15V, ±5V, ±12V, and ±15V. The DCMSAU1 series' impressive efficiencies enable these modules to deliver their fully rated output power from –40°C to +85°C without a heatsink or forced-air cooling. The very small footprint of these converters makes them an ideal solution for many applications where a voltage has to be isolated such as for noise reduction, ground loop elimination in digital interfaces, or where a higher I/O isolation is required. These converters are also fully qualified for the higher temperature profile used in lead-free reflow solder processes. These converters can also be supplied in tape & reel packaging for use in automated SMD production lines.

MODEL SELECTION TABLE													
SINGLE OUTPUT MODELS													
Model Number	Input Voltage	Output Voltage	Output Min	Current Max	Input No Load	Current Max Load	Load Regulation	Output Power	Efficiency	Maximum Capacitive Load			
DCMSAU505H-1	5 VDC (4.5 - 5.5 VDC)	5 VDC	4mA	200mA	30mA	281mA	10%	1W	71%	33µF			
DCMSAU512H-1		12 VDC	1.5mA	84mA		258mA	7%	1W	78%	4.7µF			
DCMSAU515H-1	(4.5 - 5.5 VDC)	15 VDC	1mA	67mA		258mA	7%	1W	78%	4.7µF			
DCMSAU1205H-1	12 VDC (10.8 - 13.2	5 VDC	4mA	200mA	12mA	117mA	8%	1W	71%	33µF			
DCMSAU1212H-1		12 VDC	1.5mA	84mA		106mA	5%	1W	79%	4.7µF			
DCMSAU1215H-1	VDC)	15 VDC	1mA	67mA		104mA	5%	1W	80%	4.7µF			
DCMSAU2405H-1	24 VDC (21.6 - 26.4	5 VDC	4mA	200mA	7mA	58mA	8%	1W	71%	33µF			
DCMSAU2412H-1		12 VDC	1.5mA	84mA		53mA	5%	1W	78%	4.7µF			
DCMSAU2415H-1	VDC)	15 VDC	1mA	67mA		43mA	5%	1W	79%	4.7µF			
DUAL OUTPUT MODELS													
Model Number	Input Voltage	Output Voltage	Output Min	Current Max	Input No Load	Current Max Load	Load Regulation	Output Power	Efficiency	Maximum Capacitive Load			
DCMSAU505DH-1	5 VDC	±5 VDC	±2mA	±100mA	30mA	277mA	10%	1W	72%	±10µF			
DCMSAU512DH-1		±12 VDC	±0.8mA	±42mA		255mA	7%	1W	78%	±2.2µF			
DCMSAU515DH-1	(4.5 - 5.5 VDC)	±15 VDC	±0.7mA	±34mA		258mA	7%	1W	79%	±2.2µF			
DCMSAU1205DH-1	12 VDC (10.8 - 13.2	±5 VDC	±2mA	±100mA	12mA	112mA	8%	1W	74%	±10µF			
DCMSAU1212DH-1		±12 VDC	±0.8mA	±42mA		105mA	5%	1W	80%	±2.2µF			
DCMSAU1215DH-1	VDC)	±15 VDC	±0.7mA	±34mA		104mA	5%	1W	81%	±2.2µF			
DCMSAU2405DH-1	24 VDC (21.6 - 26.4	±5 VDC	±2mA	±100mA	7mA	57mA	8%	1W	72%	±10µF			
DCMSAU2412DH-1		±12 VDC	±0.8mA	±42mA		53mA	5%	1W	79%	±2.2µF			
DCMSAU2415DH-1	`VDC)	±15 VDC	±0.7mA	±34mA		53mA	5%	1W	80%	±2.2µF			

### NOTES

- 1. The DCMSAU1 series requires a minimum output loading to maintain specified regulations. Operation under no-load conditions will not damage these devices; however they may not meet all listed specifications.
- 2. All DC/DC converters should be externally fused at the front end for protection.
- 3. Other input and output voltages may be available, please contact factory.
- \*Due to advances in technology, specifications are subject to change without notice.



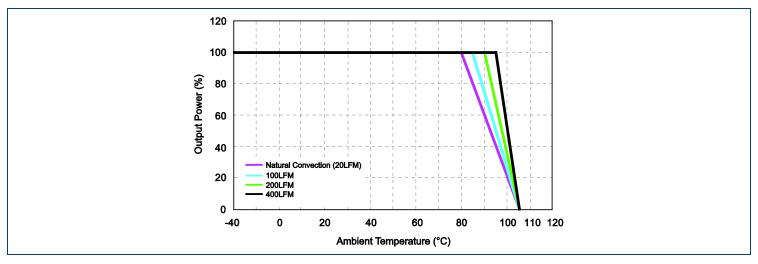
# SPECIFICATIONS: DCMSAU1 SERIES

All specifications are based on 25°C, Nominal Input Voltage, and Maximum Output Current unless otherwise noted. We reserve the right to change specifications based on technological advances.

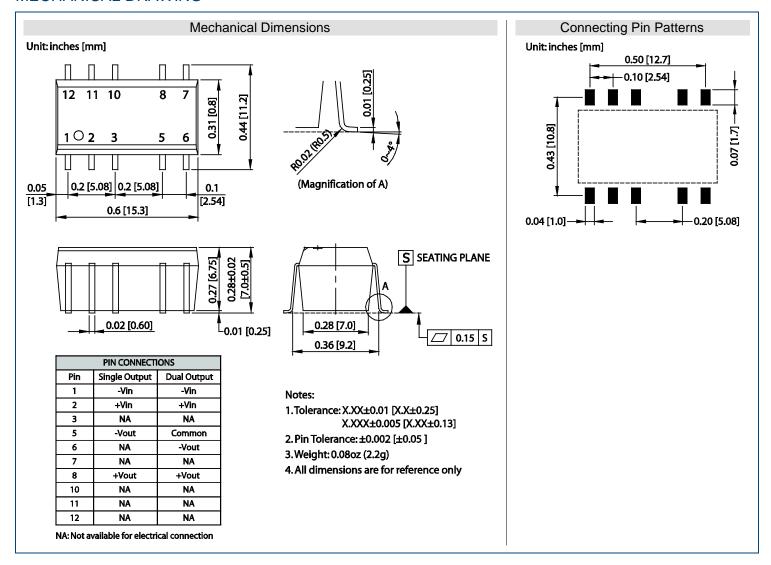
		Тур						
5VDC nominal input models	4.5	5	5.5					
•		12		VDC				
•	21.6	24	26.4					
·	-0.7		9					
•				VDC				
•								
	• • • • • • • • • • • • • • • • • • • •			Α				
		See '						
				mW				
	Internal capacitor							
5VDC nominal input models	50		•	<u> </u>				
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•								
24 VBG Hommar input modelo	10	701117 ( 310)	iv blovi type					
		S00.	Table					
				%				
		±1.5	±4.0					
Balanced loads		±0.1	±1.0	%				
For Vin change of 1%		±1.2	±1.5	%				
			_	,,,				
			1	W				
ttput Power  ttput Current								
See Note 1								
				mVp-p				
Over line, over load, and over temperature		70		mVp-p				
over mie, ever ieda, and ever temperature				mV rms				
		+0.01		%/°C				
		±0.01	±0.02	707 0				
Automatia Daggyany			0.5					
Automatic Recovery			0.5	S				
Nominal input voltage and full load								
		100	150	KHz				
				VDC				
	10			GΩ				
		60	100	pF				
		ı	I	_				
See derating curve	-40			°C				
				°C				
	-50			°C				
Non-condensing			95	% RH				
		Free air c	onvection					
1.5mm from case for 10 sec.			300	°C				
IPC/JEDEC J-STD-020D		Lev	rel 2					
MIL-HDBK-217F at 25°C, ground benign	2,000,000			hours				
ight				0.08oz (2.2g)				
	0.60 x 0.31 x 0.27 inch (15.3 x 8.0 x 6.75 mm)							
Flammability to UL 94V-0 rated	(,			/				
	For Vin change of 1% 20% load to 100% load  See Note 1  Over line, over load, and over temperature  Automatic Recovery  Nominal input voltage and full load  60 seconds 500VDC 100KHz, 1V NS See derating curve  Non-condensing  1.5mm from case for 10 sec. IPC/JEDEC J-STD-020D  MIL-HDBK-217F at 25°C, ground benign	12VDC nominal input models 24 VDC nominal input models 24 VDC nominal input models 25 VDC nominal input models 26 VDC nominal input models 27 VDC nominal input models 28 VDC nominal input models 29 VDC nominal input models 20 VDC nominal input models 20 VDC nominal input models 20 VDC nominal input models 21 VDC nominal input models 22 VDC nominal input models 26 VDC nominal input models 27 VDC nominal input models 28 VDC nominal input models 29 VDC nominal input models 20 VDC nominal input woltage and full load 20 VDC nominal input woltag	12VDC nominal input models   10.8   12   24 VDC nominal input models   21.6   24   5 VDC nominal input models   -0.7   12VDC nominal input models   500mA slot   200mA slot   200	12VDC nominal input models   10.8   12   13.2   24 VDC nominal input models   -0.7   9   12VDC nominal input models   -0.7   9   12VDC nominal input models   -0.7   30   0.3   24 VDC nominal input models   -0.7   30   0.3   24 VDC nominal input models   -0.7   30   30   25   25   25   25   25   25   25   2				



# **DERATING CURVE-**



# MECHANICAL DRAWING

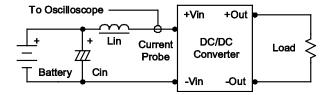




## **DESIGN CONSIDERATIONS**

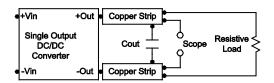
#### Input Reflected-Ripple Current Test Setup

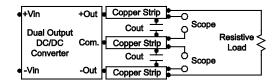
Input reflected-ripple current is measured with an inductor Lin (4.7 $\mu$ H) and Cin (220 $\mu$ F, ESR < 1.0 $\Omega$  at 100 KHz) to simulate source impedance. Capacitor Cin offsets possible battery impedance. Current ripple is measured at the input terminals of the module. Measurement bandwidth is 0-500 KHz.



### Peak-to-Peak Output Noise Measurement Test

Use a 0.33µF ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20MHz. Position the load between 50mm and 75mm from the DC/DC converter.





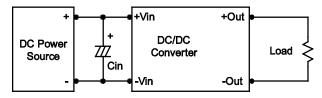
#### TEST SETUP

#### Maximum Capacitive Load

The DCMSAU1 series has a limitation of maximum connected capacitance on the output. The power module may be operated in current limiting mode during start-up, affecting the ramp-up and the start-up time. The maximum capacitance can be found in the Model Selection Table.

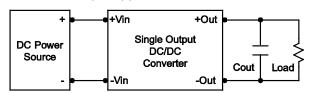
#### Input Source Impedance

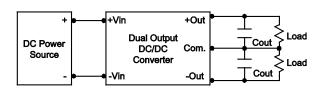
The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance (ESR <  $1.0\Omega$  at 100 KHz) capacitor of  $2.2\mu$ F for 5VDC input models,  $1.0\mu$ F for 12VDC input models, and  $0.47\mu$ F for 24VDC input models.



## **Output Ripple Reduction**

A good quality low ESR capacitor placed as close as possible across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use 1.5µF capacitors at the output.



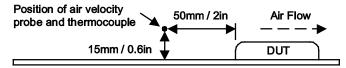




### TEST SETUP -

#### Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module, and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 90°C. The derating curves are determined from measurements obtained in a test setup.



## COMPANY INFORMATION -

Wall Industries, Inc. has created custom and modified units for over 50 years. Our in-house research and development engineers will provide a solution that exceeds your performance requirements on-time and on budget. Our ISO9001-2008 certification is just one example of our commitment to producing a high quality, well-documented product for our customers.

Our past projects demonstrate our commitment to you, our customer. Wall Industries, Inc. has a reputation for working closely with its customers to ensure each solution meets or exceeds form, fit and function requirements. We will continue to provide ongoing support for your project above and beyond the design and production phases. Give us a call today to discuss your future projects.

Contact Wall Industries for further information:

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