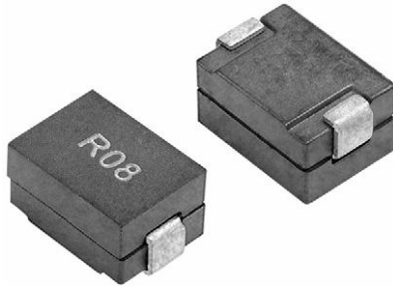


Low Profile, High Current Inductors



FEATURES

- Shielded construction
- Frequency range up to 2 MHz
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT

APPLICATIONS

- Notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- DC/DC converters in distributed power systems

STANDARD ELECTRICAL SPECIFICATIONS					
PART NUMBER	L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 1 V, 0 A (μH)	DCR TYP. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽³⁾	SATURATION CURRENT DC TYP. (A) ⁽⁴⁾
IFLR4027EZERR85NM01	0.085	0.38	0.42	50	70
IFLR4027EZERR10M01	0.100	0.38	0.42	50	70
IFLR4027EZERR12M01	0.120	0.38	0.42	50	52
IFLR4027EZERR15M01	0.155	0.38	0.42	50	40
IFLR4027EZERR22M01	0.220	0.38	0.42	50	33

Notes

- (1) All test data is referenced to 25 °C ambient.
- (2) Operating temperature range -40 °C to +125 °C.
- (3) DC current (A) that will cause an approximate ΔT of 40 °C.
- (4) DC current (A) that will cause L_0 to drop approximately 20 %.
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

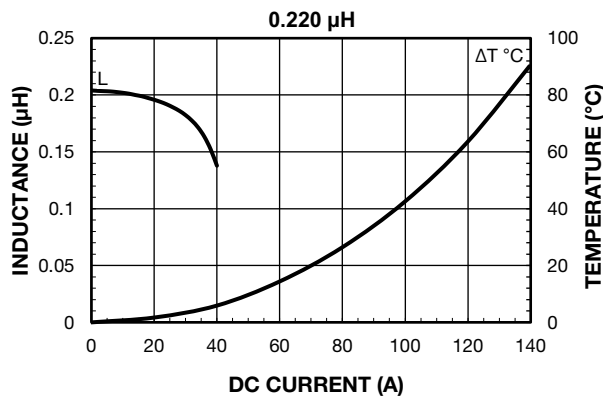
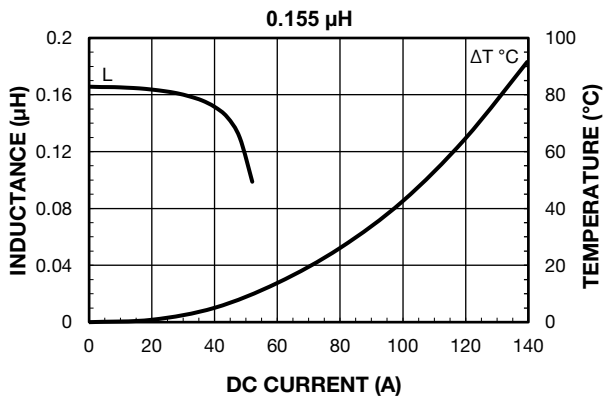
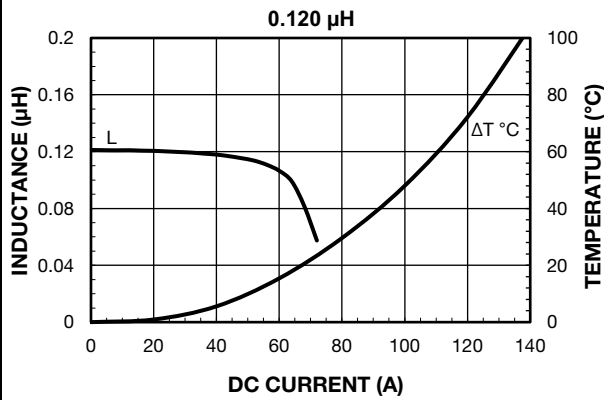
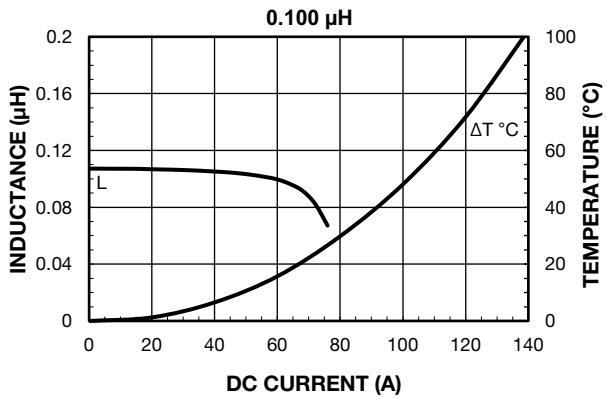
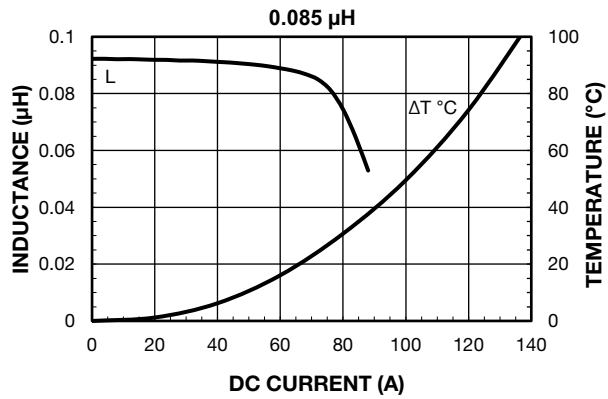
DIMENSIONS in inches [millimeters]			

DESCRIPTION				
IFLR-4027EZ-01	0.12 μH	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER																	
I	F	L	R	4	0	2	7	E	Z	E	R	R	1	2	M	0	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES				

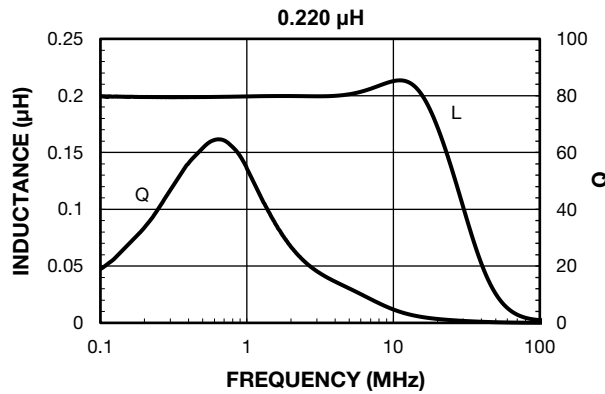
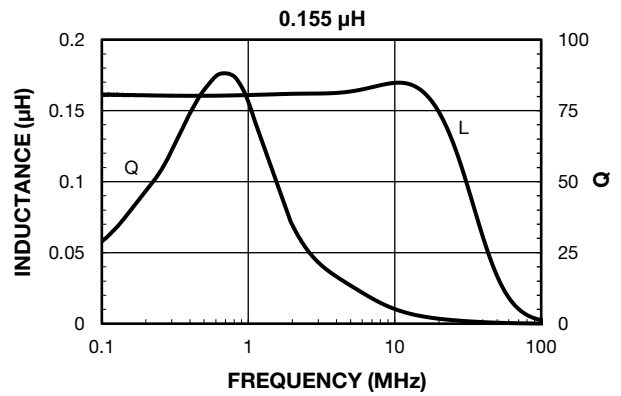
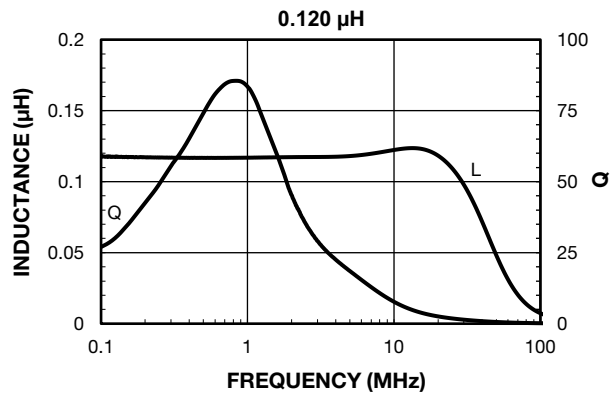
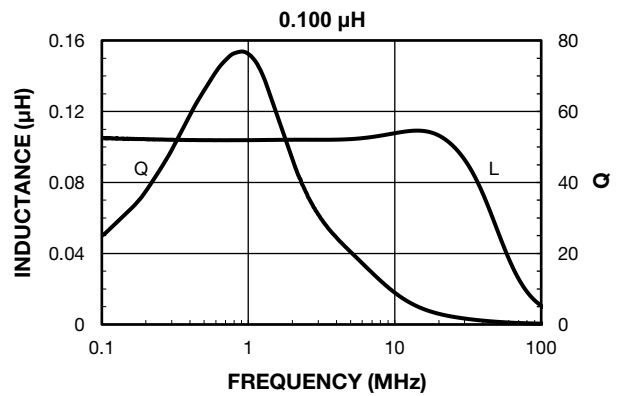
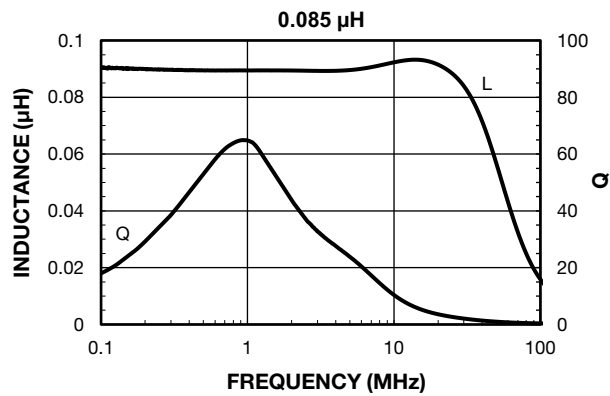


PERFORMANCE GRAPHS





PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





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