

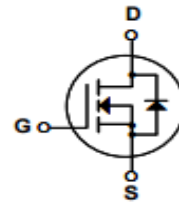
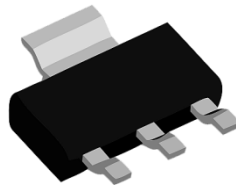
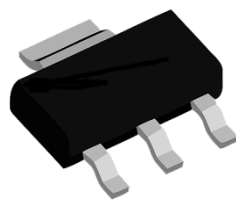
Description

This N-channel MOSFETS use advanced trench technology and design to provide excellent RDS(on) with low gate charge. It can be used in a wide variety of applications.

Features

BVDSS	RDS(ON)	ID
100V	0.7Ω	1.1A

- 1) Low gate charge.
- 2) Green device available.
- 3) Advanced high cell density trench technology for ultra RDS(ON)
- 4) Excellent package for good heat dissipation.



SOT-223

Absolute Maximum Ratings $T_c=25^{\circ}\text{C}$, unless otherwise noted

Symbol	Parameter	Ratings	Units
VDS	Drain-Source Voltage	100	V
VGS	Gate-Source Voltage	±20	V
ID	Continuous Drain Current-1	1.1	A
	Continuous Drain Current-T=100°C	0.88	
	Pulsed Drain Current ²	—	
EAS	Single Pulse Avalanche Energy ³	—	mJ
PD	Power Dissipation ⁴	1.79	W
TJ, TSTG	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Ratings	Units
RθJC	Thermal Resistance, Junction to Case ¹	—	°C/W
RθJA	Thermal Resistance, Junction to Ambient ¹	—	

Package Marking and Ordering Information

Part NO.	Marking	Package
KSMT296N	KSMT296N	SOT-223

Electrical Characteristics $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	—	—	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=32V$	—	—	0.1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=\pm 20V, V_{GS}=0A$	—	—	50	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{DS}=V_{DS}, I_D=250\mu A$	—	—	—	V
$R_{DS(on)}$	Drain-Source On Resistance ²	$V_{DS}=10V, I_D=6A$	—	0.62	1	Ω
		$V_{DS}=2.5V, I_D=5A$	—	—	—	
G_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=12A$	—	—	—	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V,$ $f=1MHz$	—	291	364	pF
C_{oss}	Output Capacitance		—	53	66	
C_{rss}	Reverse Transfer Capacitance		—	29	36	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=20V,$ $V_{GS}=10V, R_{GEN}=3.3\Omega$	—	5.2	7.8	ns
t_r	Rise Time		—	7.9	11.8	ns
$t_{d(off)}$	Turn-Off Delay Time		—	37.4	56.1	ns
t_f	Fall Time		—	21.4	32.1	ns
Q_g	Total Gate Charge	$V_{GS}=4.5V, V_{DS}=20V,$ $I_D=6A$	—	0.7	0.9	nC
Q_{gs}	Gate-Source Charge		—	5	7.5	nC
Q_{gd}	Gate-Drain "Miller" Charge		—	13.8	17.2	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A$	—	2.7	—	V
t_{rr}	Reverse Recovery Time	$I_F=7A, di/dt=100A/\mu S$	—	44.3	55.4	ns
Q_{rr}	Reverse Recovery Charge		—	71.9	89.8	nC

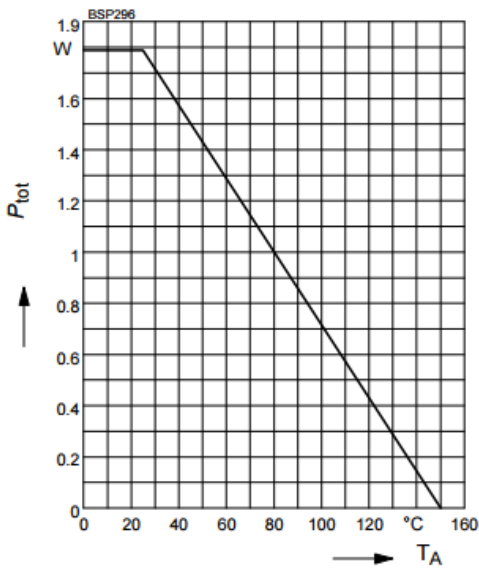
Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board 2OZ copper.
2. The data tested by pulse width≤300us,duty cycle≤2%
3. The EAS data shows Max.rating.The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, i_{AS}=17.8A$
4. The power dissipation is limited by 150°C junction temperature.

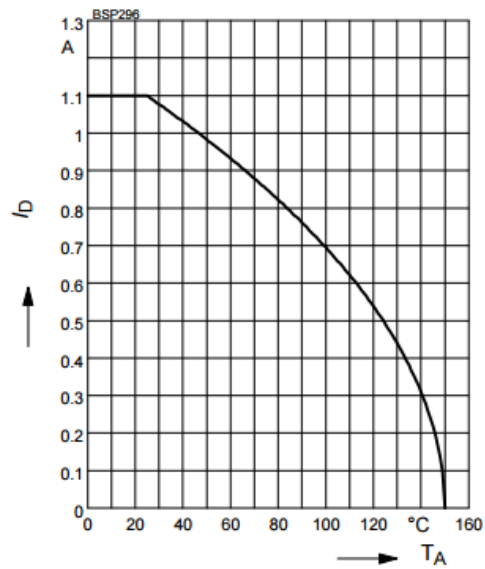
Typical Characteristics $T_J=25^{\circ}C$ unless otherwise noted

1 Power dissipation

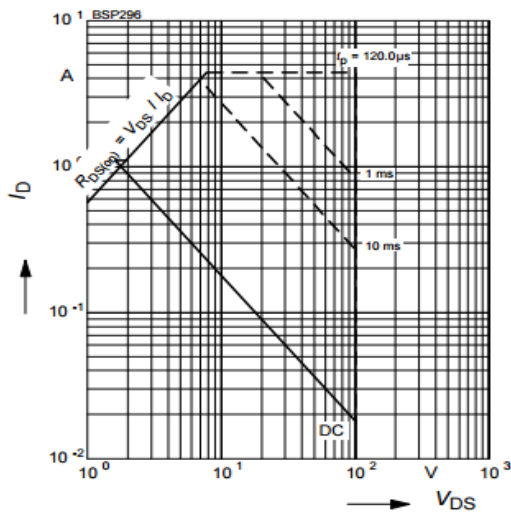
$$P_{tot} = f(T_A)$$


2 Drain current

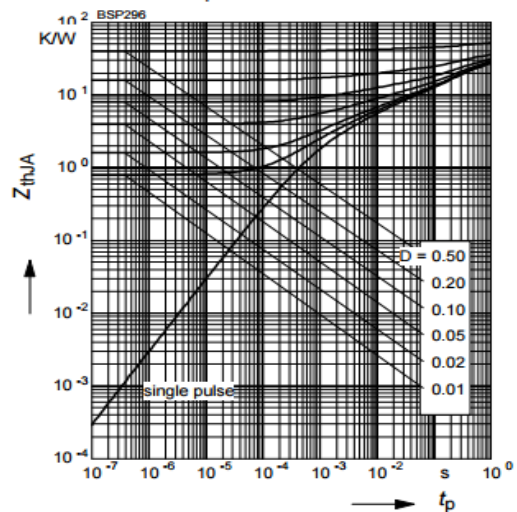
$$I_D = f(T_A)$$

 parameter: $V_{GS} \geq 10V$

3 Safe operating area

$$I_D = f(V_{DS})$$

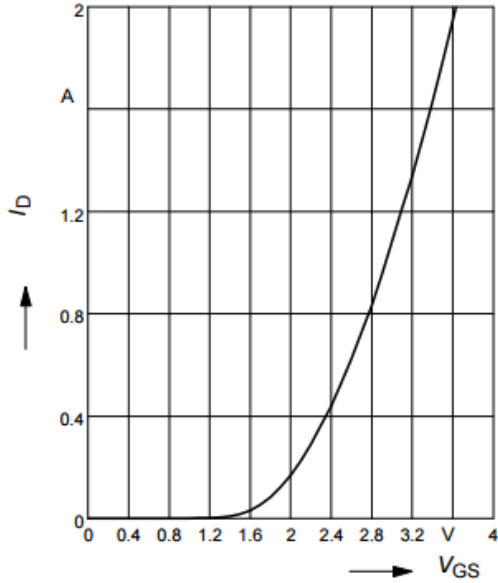
 parameter : $D = 0, T_A = 25^{\circ}C$

4 Transient thermal impedance

$$Z_{thJA} = f(t_p)$$

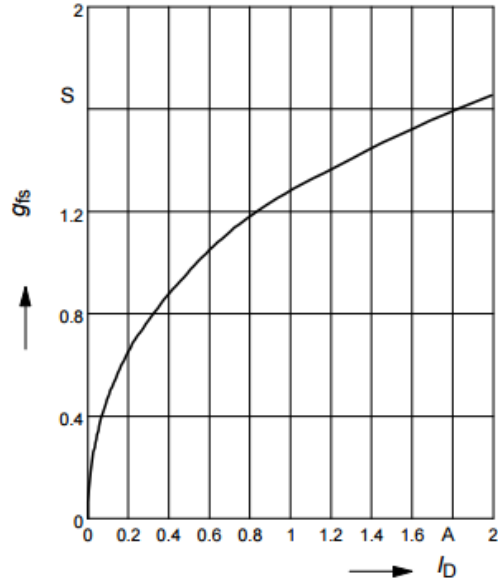
 parameter : $D = t_p/T$


7 Typ. transfer characteristics

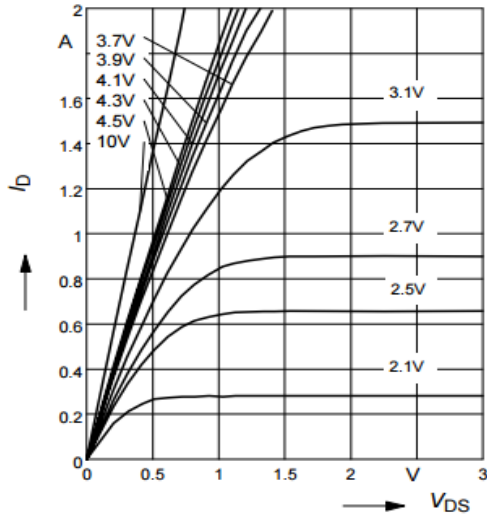
$I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$
parameter: $T_j = 25^\circ\text{C}$


8 Typ. forward transconductance

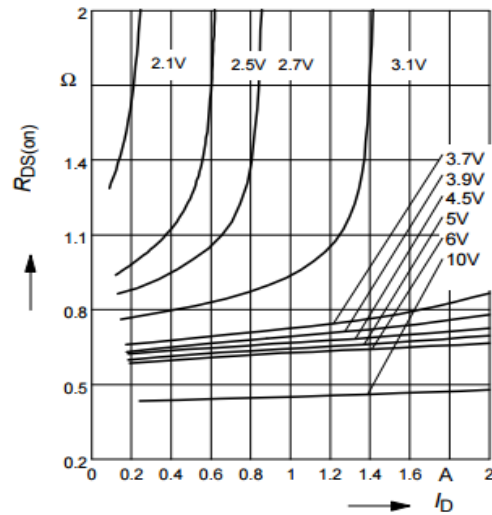
$g_{fs} = f(I_D)$
parameter: $T_j = 25^\circ\text{C}$

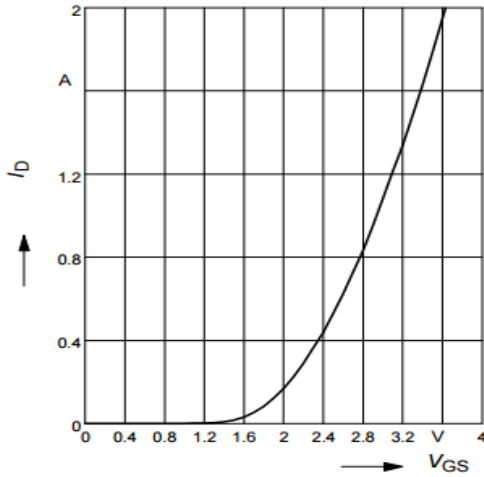
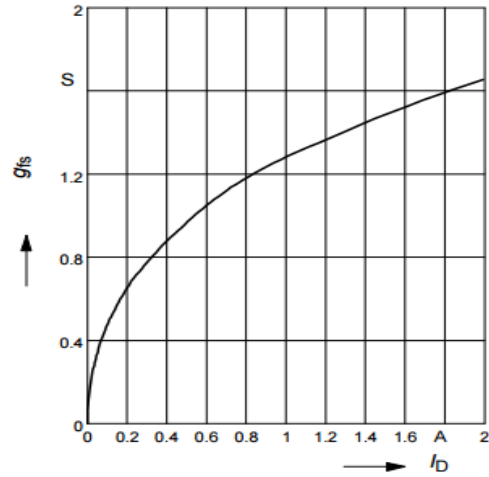
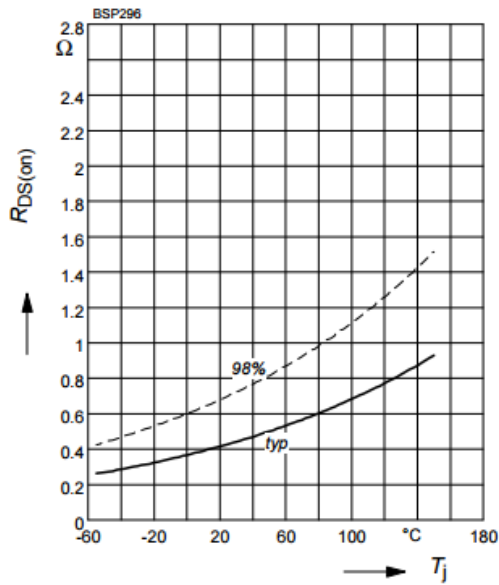

5 Typ. output characteristic

$I_D = f(V_{DS})$
parameter: $T_j = 25^\circ\text{C}$, V_{GS}


6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D)$
parameter: $T_j = 25^\circ\text{C}$, V_{GS}



7 Typ. transfer characteristics
 $I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$
 parameter: $T_j = 25^\circ\text{C}$

8 Typ. forward transconductance
 $g_{fs} = f(I_D)$
 parameter: $T_j = 25^\circ\text{C}$

9 Drain-source on-state resistance
 $R_{DS(on)} = f(T_j)$
 parameter: $I_D = 1.1\text{ A}$, $V_{GS} = 10\text{ V}$

10 Typ. gate threshold voltage
 $V_{GS(th)} = f(T_j)$
 parameter: $V_{GS} = V_{DS}$; $I_D = 400\mu\text{A}$
