

isc Silicon NPN Power Transistor

2SD928

DESCRIPTION

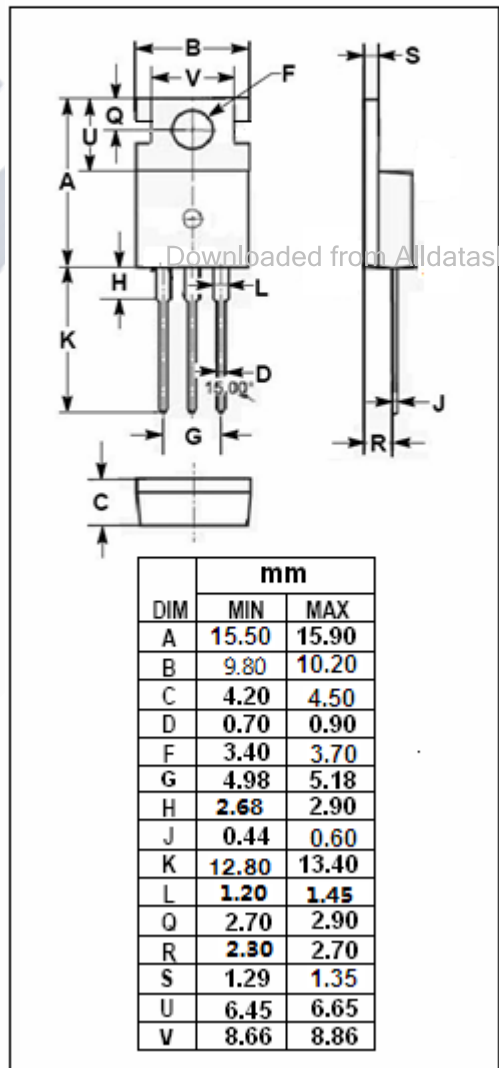
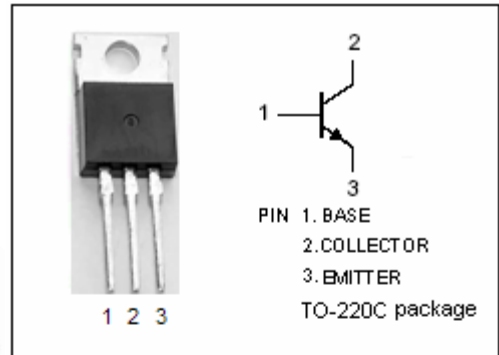
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V(\text{Min})$
- Low Collector-Emitter Saturation Voltage-
: $V_{CE(sat)} = 1.0V(\text{Max}) @ I_C = 3.0A$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for use in audio frequency power amplifier applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	80	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	4	A
I_{CM}	Collector Current-Peak	6	A
I_B	Base Current-Continuous	0.5	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	30	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD928****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=10\text{mA}; I_B=0$	80			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1\text{mA}; I_C=0$	7			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=3.0\text{A}; I_B=0.3\text{A}$			1.0	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C=0.5\text{A}; V_{CE}=5\text{V}$			1.0	V
I_{CBO}	Collector Cutoff Current	$V_{CB}=80\text{V}; I_E=0$			100	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB}=8\text{V}; I_C=0$			100	μA
h_{FE}	DC Current Gain	$I_C=0.5\text{A}; V_{CE}=5\text{V}$	60		300	
f_T	Current-Gain—Bandwidth Product	$I_C=0.5\text{A}; V_{CE}=5\text{V}; f_{test}=1.0\text{MHz}$	3.0			MHz