Technical Data Data Sheet N1512, Rev. - **Green Products**

SD88205 Unidirectional Discrete TVS

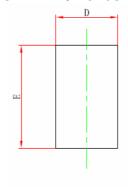
Applications:

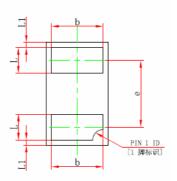
- Mobile phones
- Smart phones
- PDAs
- Portable navigation devices
- Digital cameras
- Portable medical devices

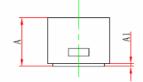
Features:

- ESD, IEC61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC61000-4-4, 40A(5/50ns)
- Lightning, IEC61000-4-5, 7A (8/20 μ s)
- Low leakage current of 100nA (MAX) at 5V
- Fits solder footprint of industry standard 0402(1005) devices
- This is a Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Mechanical Dimensions: In mm/Inches







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	0.400	0.500	0.016	0.020	
A1	0.000	0.050	0.000	0.002	
D	0.550	0.650	0.022	0.026	
E	0.950	1.050	0.037	0.041	
b	0.400	0.600	0.016	0.024	
е	0.650TYP.		0.026TYP.		
L	0.150	0.350	0.006	0.014	
L1	0.050REF.		0.002REF.		

DFN1.0*0.6*0.5

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Marking Diagram:

N5

Where N5 is SD88205

N5 = Part Name

Cautions: Molding resin

Epoxy resin UL:94V-0

Ordering Information:

Device	Package	Shipping	
SD88205	DFN1.0*0.6*0.5(Pb-Free)	10000pcs / reel	

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification.

Maximum Ratings and Thermal Information $@T_A=25$ °C unless otherwise specified

Parameter	Symbol	Value	Unit	
Peak Pulse Current (tp=8/20 μ s)	I _{pp}	7	Α	
Maximum Lead Temperature (Soldering 20-40s)	TL	260	C	
Operating Storage Temperature Range	Тѕтс	-55 to 150	C	
Operating Junction Temperature	TJ	150	C	

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Electrical Characteristics (T_J = 25 °C)

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units
Forward Voltage Drop*	V_{F}	IF=10mA	-	0.8	1.2	V
Reverse Voltage Drop	V_R	IR=1mA	6.0	7.8	8.5	V
Reverse Standoff Voltage	V_{RWM}	IR=1µA	-	-	5.0	V
Reverse Leakage Current	ILEAK	VR=5V	-	-	100	nA
Clamp Voltage(Note 1)	Vc	lpp=6A tp=8/20μs	-	11.4	-	V
Clamp voltage(Note 1)		lpp=7A tp=8/20µs	-	12.0	-	V
Diode Capacitance(Note 1)	C_D	Reverse Bias=0V	-	30	-	pF

Note: 1 Parameter is guaranteed by design and/or device characterization.

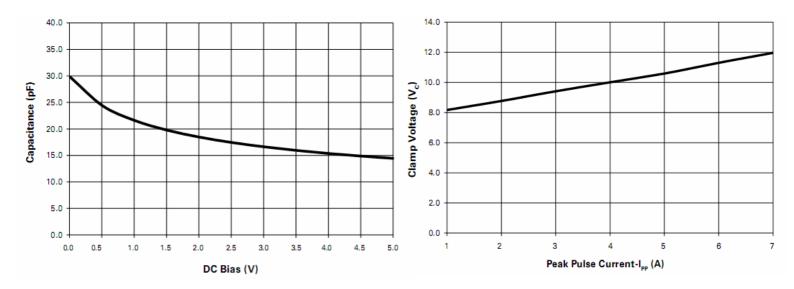


Fig.1-Capacitance vs. Reverse Bias

Fig.2-Clamping Voltage vs. IPP

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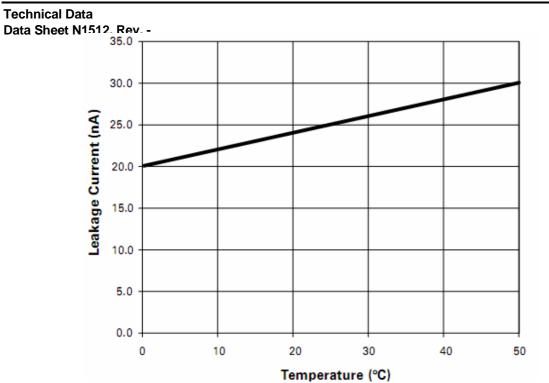


Fig.3-Leakage vs. Temperature

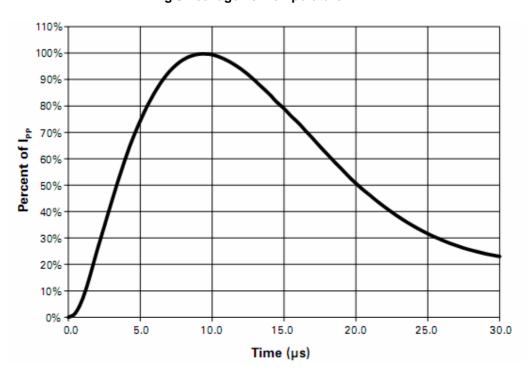


Fig.4-Pulse Waveform

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