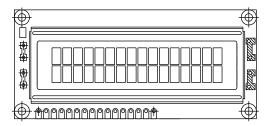
Vishay

COMPLIANT

16 x 2 Character LCD



FEATURES

• Type: character

• Display format: 16 x 2 characters

• Built-in controller: ST7066

• Duty cycle: 1/16

• 5 x 8 dots includes cursor

• +5 V power supply (also available for +3 V)

• LED can be driven by pin 1, pin 2, pin 15, pin 16, or A

and K

• N.V. optional for +3 V power supply

• Optional: smaller character size (2.95 mm x 4.35 mm)

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

MECHANICAL DATA							
ITEM	STANDARD VALUE	UNIT					
Module dimension	85.0 x 36.0						
Viewing area	66.0 x 16.0						
Dot size	0.55 x 0.65	mm					
Dot pitch	0.60 x 0.70	1111111					
Mounting hole	75.0 x 31.0						
Character size	2.95 x 5.55						

ABSOLUTE MAXIMUM RATINGS								
ITEM	SYMBOL	STAN	UNIT					
IIEWI	STIVIBUL	MIN.	TYP.	MAX.	UNII			
Power supply	V_{DD} to V_{SS}	-0.3	-	7.0	V			
Input voltage	VI	-0.3	-	V_{DD}	V			

Note

• $V_{SS} = 0 \text{ V}, V_{DD} = 5.0 \text{ V}$

ELECTRICAL CHARACTERISTICS									
ITEM	CYMPOL	COMPITION	ST						
	SYMBOL	CONDITION	MIN. TYP.		MAX.	UNIT			
Input voltage	V _{DD}	$V_{DD} = +5 \text{ V}$	4.7	5.0	5.3	V			
Supply current	I _{DD}	$V_{DD} = +5 \text{ V}$	-	1.2	1.5	mA			
Recommended LC driving voltage for normal temperature version module		-20 °C	=	=	5.2				
		0 °C	-	-	4.2				
	V_{DD} to V_{0}	25 °C	-	3.8	-	V			
		50 °C	3.5	-	-				
		70 °C	3.2	-	-	-			
LED forward voltage	V _F	25 °C	-	4.2	4.6	V			
LED forward current - array	,	05.90	-	100	-	A			
LED forward current - edge	I _F	25 °C	-	20	40	mA			
EL power supply current	I _{EL}	V _{EL} = 110 V _{AC} , 400 Hz	-	-	5.0	mA			

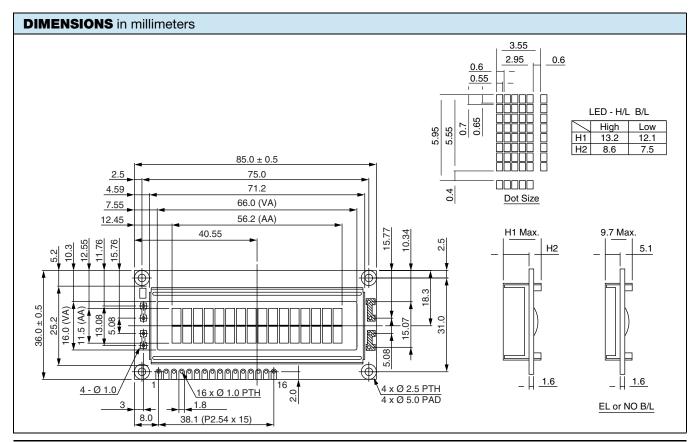
OPTIONS	3								
		PROCES	S COLOR		BACK	LIGHT			
TN	STN GRAY	STN YELLOW	STN BLUE	FSTN B&W	STN COLOR	NONE	LED	EL	CCFL
Х	Х	Х	Х	-	-	Х	Х	Х	-

For detailed information, please see the "Product Numbering System" document.



DISPLAY CHARACTER ADDRESS CODE																
Display position																
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DD RAM address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
DD RAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F

INTERFACE PIN FUNCTION							
PIN NO.	SYMBOL	FUNCTION					
1	V _{SS}	Ground					
2	V _{DD}	+3 V or +5 V					
3	V ₀	Contrast adjustment					
4	RS	H / L register select signal					
5	R/W	H / L read / write signal					
6	E	$ extsf{H} ightarrow extsf{L}$ enable signal					
7	DB0	H / L data bus line					
8	DB1	H / L data bus line					
9	DB2	H / L data bus line					
10	DB3	H / L data bus line					
11	DB4	H / L data bus line					
12	DB5	H / L data bus line					
13	DB6	H / L data bus line					
14	DB7	H / L data bus line					
15	A / V _{EE}	+4.2 V for LED ($R_A = 0 \Omega$) / negative voltage output					
16	K	Power supply for backlight (0 V)					





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