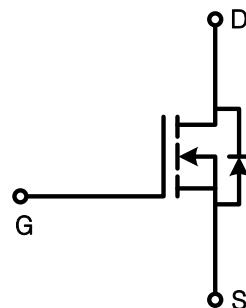
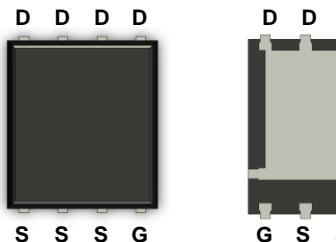


General Description

The MDU1531 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDU1531 is suitable device for DC/DC Converter and general purpose applications.

Features

- $V_{DS} = 30V$
- $I_D = 64A @ V_{GS} = 10V$
- $R_{DS(ON)}$
 $< 2.4 \text{ m}\Omega @ V_{GS} = 10V$
 $< 3.3 \text{ m}\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% R_g Tested



PDFN56

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	$T_c=25^\circ\text{C}$ (Silicon Limited)	I_D	163	A
	$T_c=25^\circ\text{C}$ (Package Limited)		64	
	$T_a=70^\circ\text{C}$ (Silicon Limited)		130	
	$T_a=25^\circ\text{C}$		42	
	$T_a=70^\circ\text{C}$		33	
Pulsed Drain Current		I_{DM}	256	
Power Dissipation	$T_c=25^\circ\text{C}$	P_D	83	W
	$T_c=70^\circ\text{C}$		53	
	$T_a=25^\circ\text{C}$		5.5	
	$T_a=70^\circ\text{C}$		3.5	
Single Pulse Avalanche Energy ⁽²⁾		E_{AS}	57	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾		$R_{\theta JA}$	22.7	°C/W
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	1.5	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDU1531RH	-55~150°C	PDFN56	Tape & Reel	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.8	2.7	
Drain Cut-Off Current	I_{DSS}	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 100	nA
Drain-Source ON Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 28\text{A}$	-	2.0	2.4	$\text{m}\Omega$
		$T_J = 125^\circ\text{C}$	-	2.6	3.1	
		$V_{GS} = 4.5\text{V}, I_D = 24\text{A}$	-	2.7	3.3	
Forward Transconductance	g_{fs}	$V_{DS} = 5\text{V}, I_D = 10\text{A}$	-	63	-	S
Dynamic Characteristics						
Total Gate Charge	$Q_{g(10\text{V})}$	$V_{DD} = 15\text{V}, I_D = 28\text{A}, V_{GS} = 10\text{V}$	47.6	63.6	79.6	nC
Total Gate Charge	$Q_{g(4.5\text{V})}$		21.9	29.3	36.7	
Gate-Source Charge	Q_{gs}		-	8.6	-	
Gate-Drain Charge	Q_{gd}		-	11.8	-	
Input Capacitance	C_{iss}	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	2827	3770	4712	pF
Reverse Transfer Capacitance	C_{rss}		130	174	217	
Output Capacitance	C_{oss}		980	1310	1639	
Turn-On Delay Time	$t_{d(on)}$		-	14.1	-	
Rise Time	t_r	$V_{GS} = 10\text{V}, V_{DD} = 15.0\text{V}, I_D = 28\text{A}, R_G = 3.0\Omega$	-	8.2	-	ns
Turn-Off Delay Time	$t_{d(off)}$		-	58.2	-	
Fall Time	t_f		-	11.7	-	
Gate Resistance	R_g	$f=1\text{ MHz}$	-	1.0	2.0	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 28\text{A}, V_{GS} = 0\text{V}$	-	0.8	1.1	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 28\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	49	73	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	62	93	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at $T_c=25^\circ\text{C}$ is silicon limited
2. E_{AS} is tested at starting $T_j = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $I_{AS} = 35\text{A}$, $V_{DD} = 27\text{V}$, $V_{GS} = 10\text{V}$

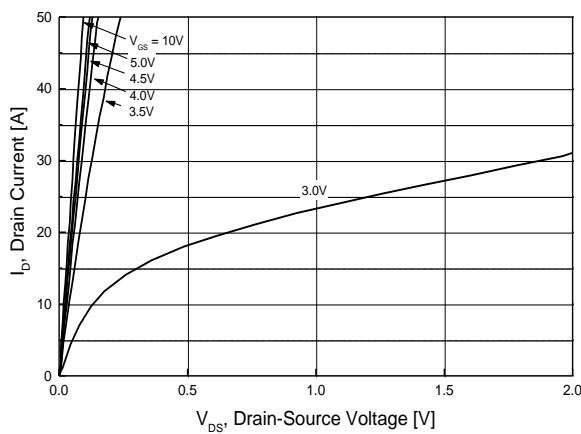


Fig.1 On-Region Characteristics

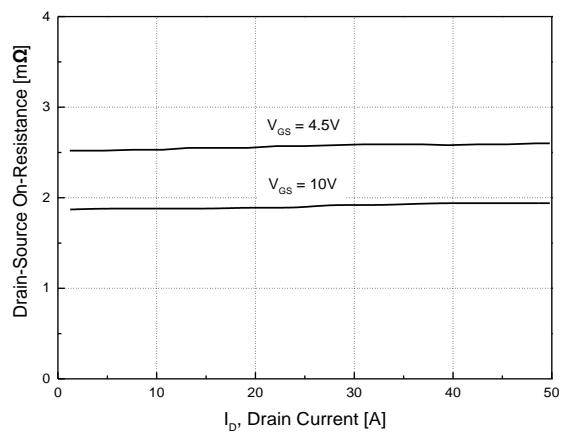


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

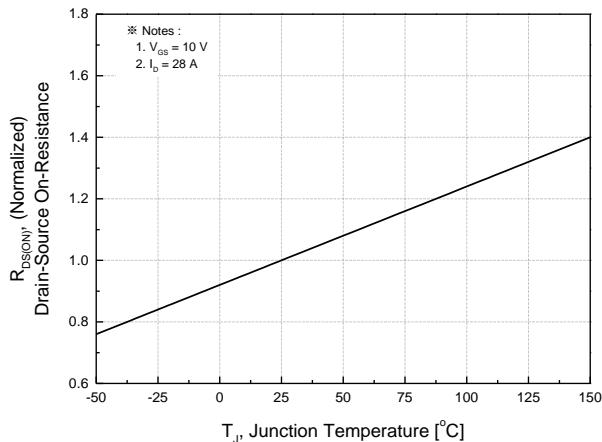


Fig.3 On-Resistance Variation with Temperature

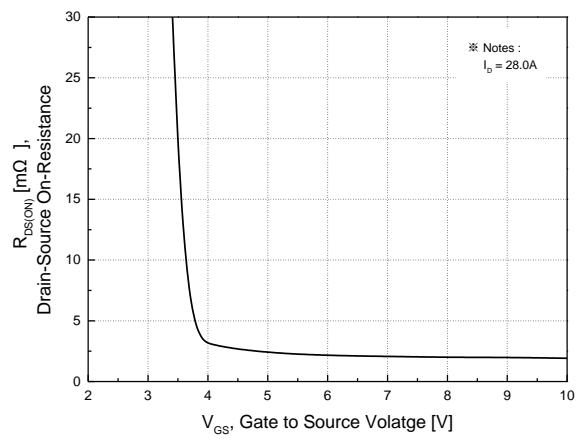


Fig.4 On-Resistance Variation with Gate to Source Voltage

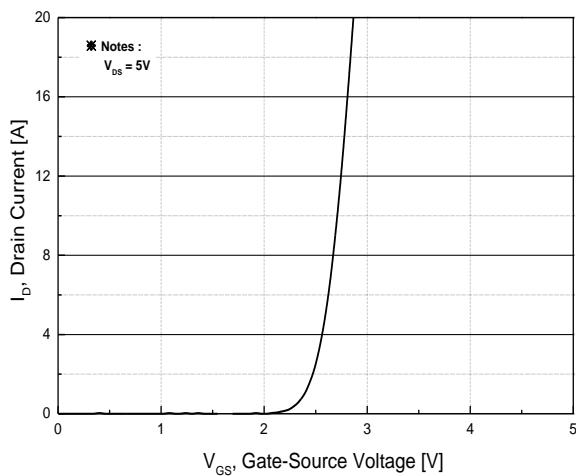


Fig.5 Transfer Characteristics

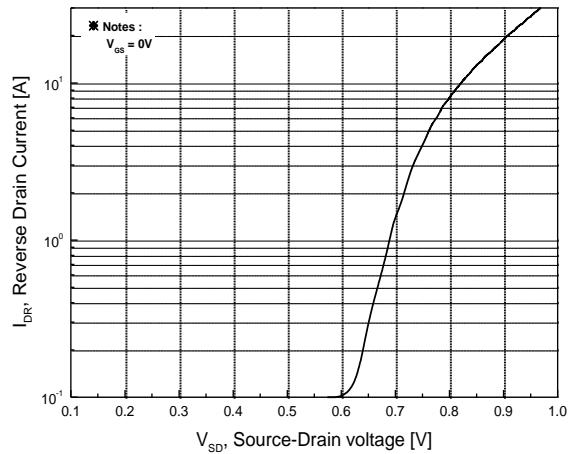


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

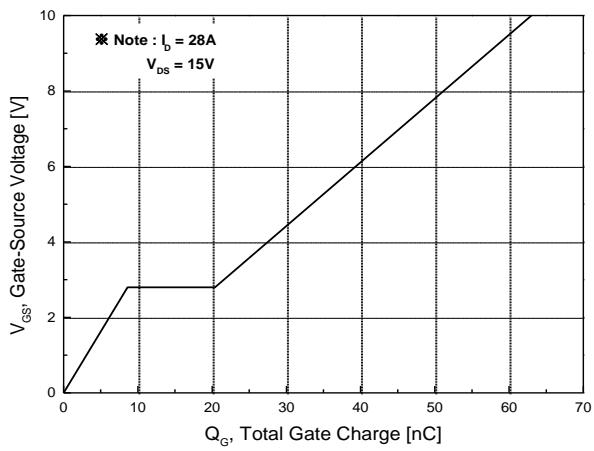


Fig.7 Gate Charge Characteristics

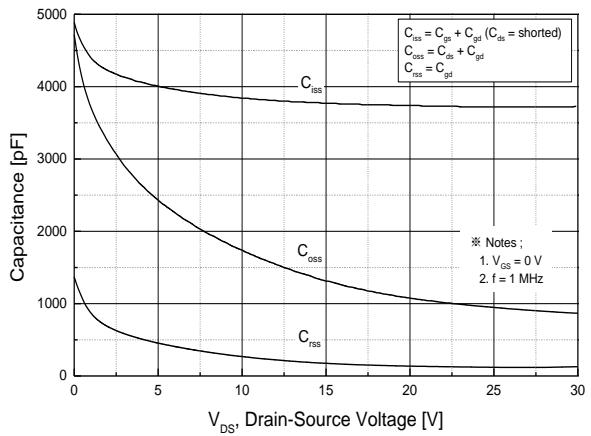


Fig.8 Capacitance Characteristics

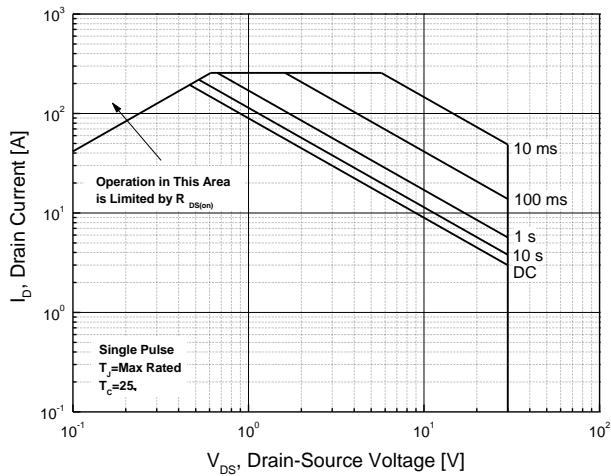


Fig.9 Maximum Safe Operating Area

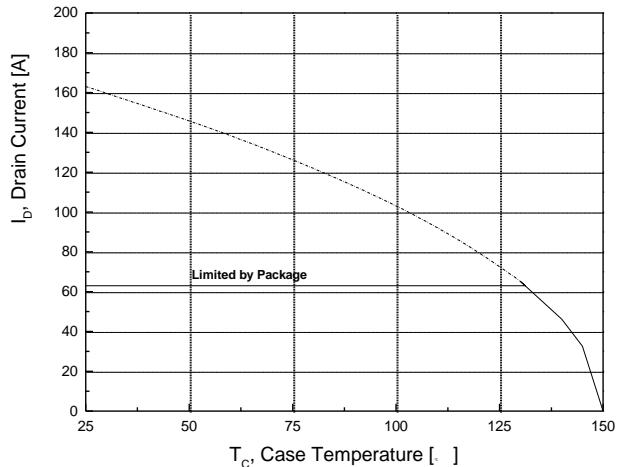


Fig.10 Maximum Drain Current vs. Case Temperature

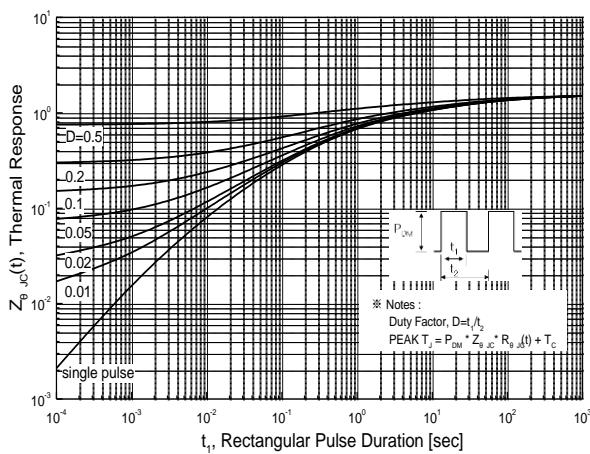
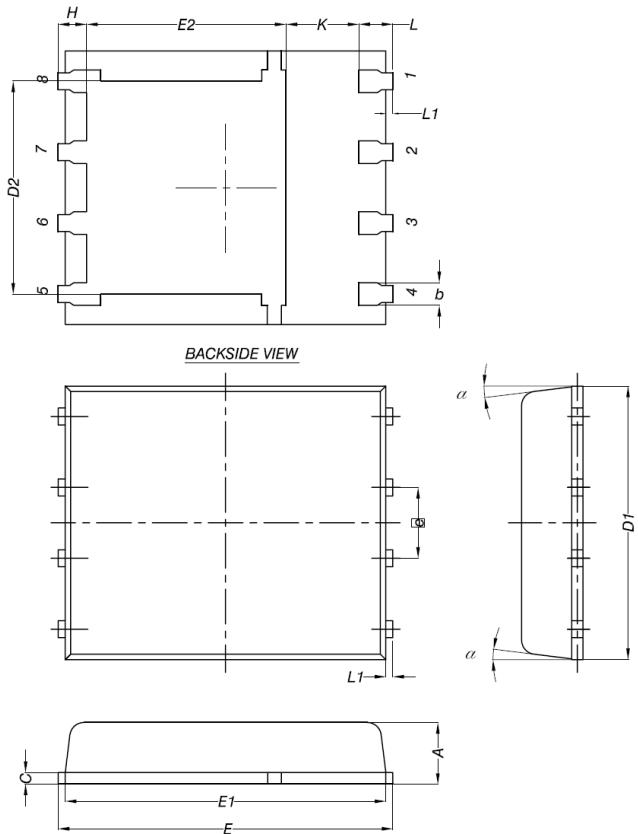


Fig.11 Transient Thermal Response Curve

Package Dimension

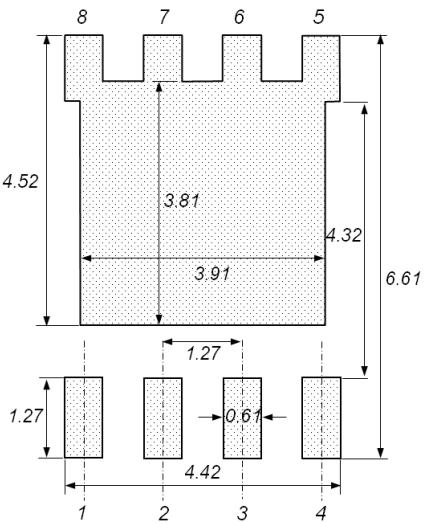
PDFN56 (5x6mm²)

Dimensions are in millimeters, unless otherwise specified



Dimension	MILLIMETERS	
	Min	Max
A	0.90	1.10
b	0.33	0.51
C	0.20	0.34
D1	4.50	5.10
D2	-	4.22
E	5.90	6.30
E1	5.50	6.10
E2	-	4.30
e	1.27BSC	
H	0.41	0.71
K	0.20	-
L	0.51	0.71
α	0°	12°

Land Pattern



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