

### **N-channel SiC power MOSFET**

$V_{DSS}$	650V
R <sub>DS(on)</sub> (Typ.)	$60 {\sf m}\Omega$
I <sub>D</sub>	39A
$P_D$	165W

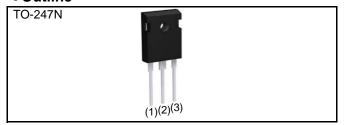
#### Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

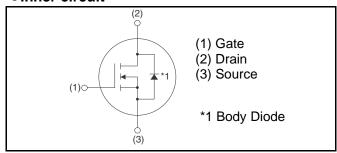
### Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

#### Outline



#### ●Inner circuit



Packaging specifications

	Packing	Tube			
	Reel size (mm)	-			
Typo	Tape width (mm)	-			
Type	Basic ordering unit (pcs)	30			
	Taping code	C11			
	Marking	SCT3060AL			

#### ◆Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{ m DSS}$	650	V	
Continuous drain current	$T_c = 25^{\circ}C$	I <sub>D</sub> *1	39	А
Continuous drain current	T <sub>c</sub> = 100°C	I <sub>D</sub> *1	27	А
Pulsed drain current		I <sub>D,pulse</sub> *2	97	А
Gate - Source voltage		$V_{GSS}$	-4 to 22	V
Gate-Source Surge Voltage		$V_{GSS\_surge}$	−4 to 22	V
Recommended Drive Voltage		$V_{GS\_op}$	0 / 18	V
Junction temperature		T <sub>j</sub>	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

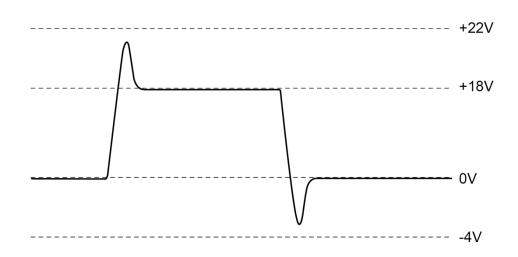
#### ●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	
Thermal resistance, junction - case	$R_{thJC}$	-	0.70	0.91	°C/W

### ●Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$ , $I_D = 1mA$	650	-	-	V
		$V_{DS} = 650V, V_{GS} = 0V$				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25$ °C	-	1	10	μΑ
drain barrone		T <sub>j</sub> = 150°C	-	2	-	
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I <sub>GSS</sub> _	$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V <sub>GS (th)</sub>	$V_{DS} = 10V, I_{D} = 6.67 \text{mA}$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 13A$				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *3	T <sub>j</sub> = 25°C	-	60	78	mΩ
		T <sub>j</sub> = 125°C	-	79.2	-	
Gate input resistance	$R_{G}$	f = 1MHz, open drain	-	12	-	Ω

#### ●Example of acceptable Vgs waveform



# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Cumbal	Conditions	Values			Linit
r arameter Symbol	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	<b>g</b> fs *3	$V_{DS} = 10V, I_D = 13A$	-	4.9	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	852	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 500V	-	55	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	24	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V$ to 300V	-	126	-	pF
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} = 300V, I_D = 13A$	-	19	-	
Rise time	t <sub>r</sub> *3	$V_{GS} = 18V/0V$	-	37	-	no
Turn - off delay time	t <sub>d(off)</sub> *3	$R_L = 23\Omega$	-	34	-	ns
Fall time	t <sub>f</sub> *3	$R_G = 0\Omega$	-	21	-	
Turn - on switching loss	E <sub>on</sub> *3	$V_{DD} = 300V, I_{D} = 13A$ $V_{GS} = 18V/0V$	-	70		
Turn - off switching loss	E <sub>off</sub> *3	R <sub>G</sub> = 0Ω L=500μH *E <sub>on</sub> includes diode reverse recovery	-	10	1	μJ

## •Gate Charge characteristics ( $T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*3}$	V <sub>DD</sub> = 300V	-	58	ı	
Gate - Source charge	$Q_{gs}^{*3}$	I <sub>D</sub> = 13A	-	15	-	nC
Gate - Drain charge	Q <sub>gd</sub> *3	V <sub>GS</sub> = 18V	-	23	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} = 300V, I_D = 13A$	-	9.6	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

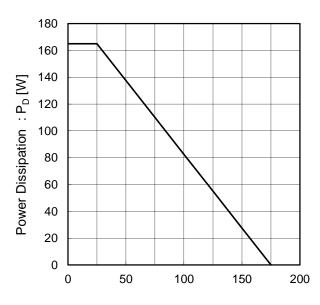
<sup>\*2</sup> PW  $\leq$  10  $\mu s,$  Duty cycle  $\leq$  1%

<sup>\*3</sup> Pulsed

## ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

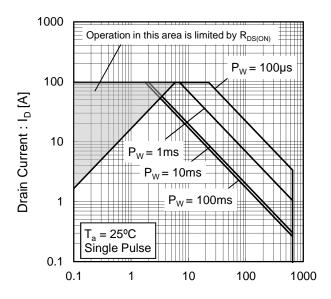
Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l <sub>S</sub> *1	-T <sub>c</sub> = 25°C	-	1	39	А
Inverse diode direct current, pulsed	I <sub>SM</sub> *2		-	-	97	А
Forward voltage	V <sub>SD</sub> *3	$V_{GS} = 0V, I_{S} = 13A$	-	3.2	-	V
Reverse recovery time	t <sub>rr</sub> *3	I <sub>F</sub> = 13A, V <sub>R</sub> = 300V di/dt = 1100A/μs	-	15	1	ns
Reverse recovery charge	Q <sub>rr</sub> *3		-	55		nC
Peak reverse recovery current	I <sub>rrm</sub> *3		-	8	-	Α

Fig.1 Power Dissipation Derating Curve



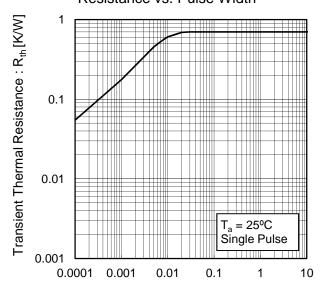
Junction Temperature : T<sub>i</sub> [°C]

Fig.2 Maximum Safe Operating Area



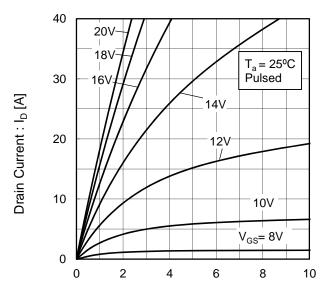
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



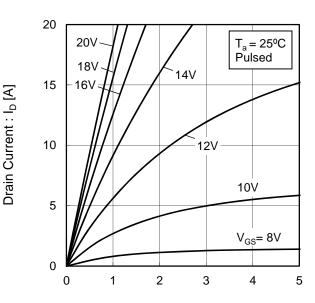
Pulse Width :  $P_W$  [s]

Fig.4 Typical Output Characteristics(I)

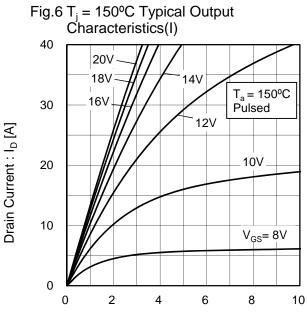


Drain - Source Voltage : V<sub>DS</sub> [V]

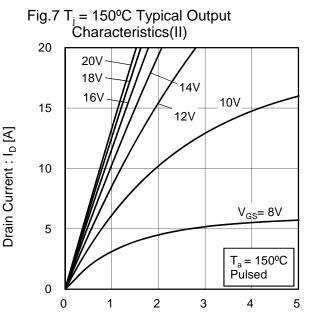
Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

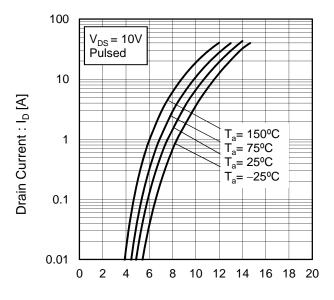


Drain - Source Voltage : V<sub>DS</sub> [V]



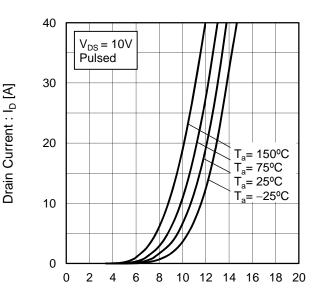
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.8 Typical Transfer Characteristics (I)



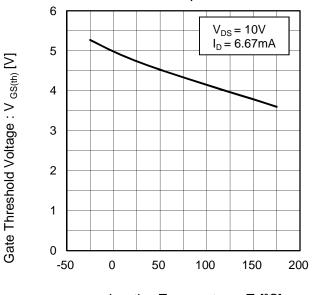
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.9 Typical Transfer Characteristics (II)



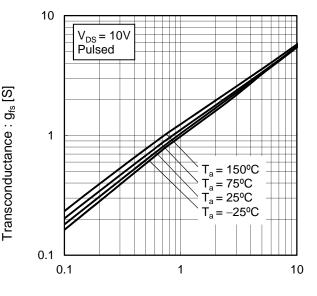
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature

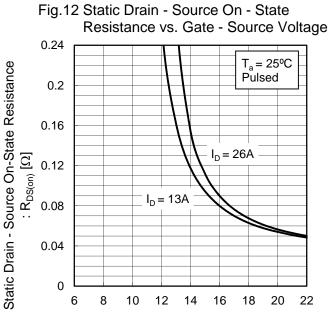


Junction Temperature : T<sub>i</sub> [°C]

Fig.11 Transconductance vs. Drain Current



Drain Current : I<sub>D</sub> [A]



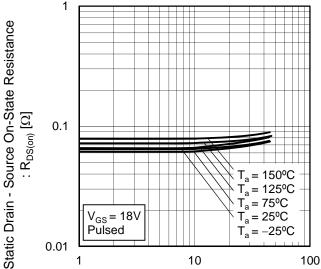
Resistance vs. Junction Temperature 0.24 V<sub>GS</sub> = 18V Pulsed Static Drain - Source On-State Resistance 0.2 0.16  $: R_{DS(on)} \left[ \Omega \right]$ 0.12  $I_{D} = 26A$ 0.08  $I_D = 13A$ 0.04 0 -50 0 50 100 150 200

Junction Temperature : T<sub>i</sub> [°C]

Fig.13 Static Drain - Source On - State

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current

Gate - Source Voltage : V<sub>GS</sub> [V]



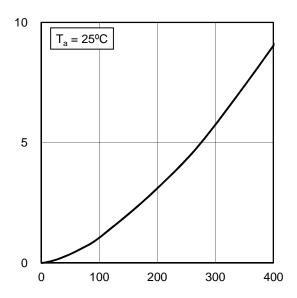
Drain Current: I<sub>D</sub> [A]

Fig.15 Typical Capacitance vs. Drain - Source Voltage 10000 1000 Capacitance: C [pF] Coss 100 10  $T_a = 25^{\circ}C$ f = 1MHz  $t_{GS} = 0V$ 1 10 0.1 100 1000

Drain - Source Voltage : V<sub>DS</sub> [V]

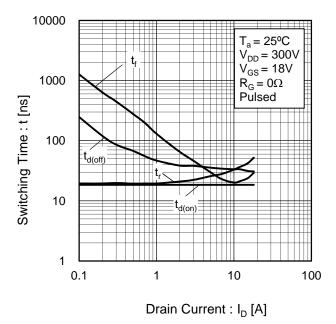
Fig.16 Coss Stored Energy

Coss Stored Energy : E<sub>OSS</sub> [μJ]



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.17 Switching Characteristics



20  $T_a = 25^{\circ}C$  $V_{DD} = 300V$   $I_{D} = 13A$ Pulsed 15

Fig.18 Dynamic Input Characteristics

10 5 0 20 40 0 60

Total Gate Charge : Q<sub>g</sub> [nC]

3ate - Source Voltage: V<sub>GS</sub> [V]

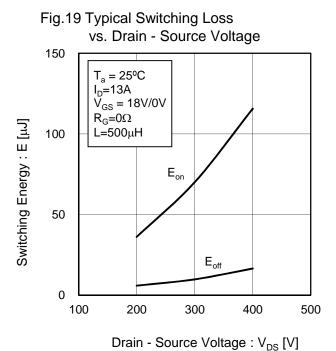


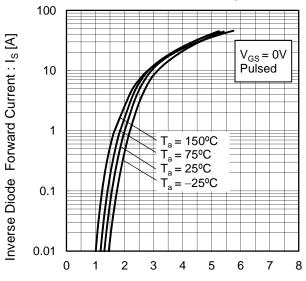
Fig.20 Typical Switching Loss vs. Drain Current 600  $T_a = 25^{\circ}C$  $V_{DD} = 300V$  $V_{GS} = 18V/0V$   $R_{G} = 0\Omega$ Switching Energy: E [µJ] L=500μH 400  $\mathsf{E}_{\mathsf{on}}$ 200  $\mathsf{E}_{\mathsf{off}}$ 0 0 20 40

Drain Current: I<sub>D</sub> [A]

Fig.21 Typical Switching Loss vs. External Gate Resistance 600  $T_a = 25^{\circ}C$ V<sub>DD</sub>=300V  $I_D = 13A$  $V_{GS} = 18V/0V$ L=500 $\mu$ H 400 Eon 200  $E_{off}$ 0 5 10 15 20 25 30

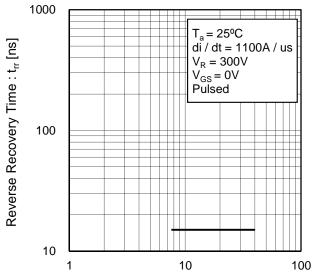
External Gate Resistance :  $R_G[\Omega]$ 

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

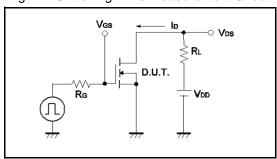


Fig.2-1 Gate Charge Measurement Circuit

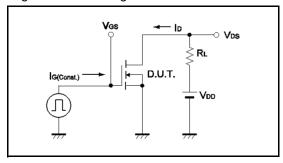


Fig.3-1 Switching Energy Measurement Circuit

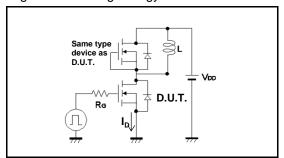


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

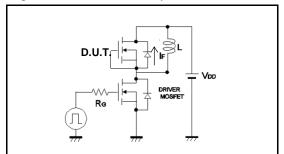


Fig.1-2 Switching Waveforms

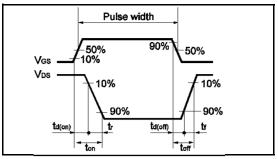


Fig.2-2 Gate Charge Waveform

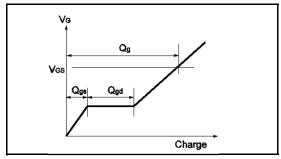
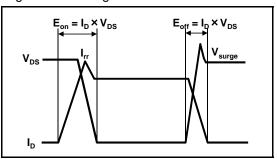
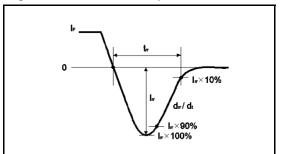


Fig.3-2 Switching Waveforms





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Package	TO-247N
Unit Quantity	450
Minimum Package Quantity	30
Packing Type	Tube
Constitution Materials List	inquiry
RoHS	Yes