Clock OSC

SG5032CBN

Product name SG5032CBN 106.250000MHz TJGA
Product Number / Ordering code X1G0044610011xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

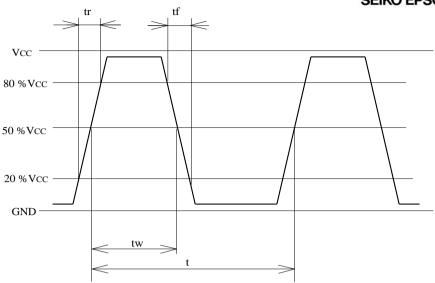
Pb free / Complies with EU RoHS directive

Reference weight Typ. 52 mg

1.Absolute maximum ratings								
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks		
Maximum supply voltage	Vcc-GND	-0.3	-	5	V	-		
Storage temperature	T_stg	-40	-	+125	٥C	Storage as single product		
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal		

2.Specifications(charac	teristics)						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks	
Output frequency	f0		106.2500		MHz		
Supply voltage	Vcc	1.6	-	3.6	V	-	
Operating temperature	T_use	-40	-	+85	۰C	-	
requency tolerance	f_tol	-50	-	50	x10 ⁻⁶	T_use	
Current consumption	Icc	-	-	11	mA	No load condition	
Stand-by current	I_std	-	-	10.0	μΑ	ST = GND	
Disable current	l_dis	-	-	-	mA	-	
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF	
Output voltage	V _{OH}	Vcc-0.4	-	-		-	
	V_{OL}	-	-	0.4		-	
Output load condition	L_CMOS	-	-	15	pF	CMOS Load	
nput voltage	V_{IH}	0.8Vcc	-	-		ST terminal	
	V_{IL}	-	-	0.2Vcc		ST terminal	
Rise time	t _r	-	-	3	ns	Vcc1.6V : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF	
Fall time	tf	-	-	3	ns	Vcc1.6V: 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF	
Start-up time	t_str	-	-	5	ms	t = 0 at 0.9Vcc	
litter	t _{DJ}	-	0	-	ps	Deterministic Jitter Vcc=3.3V	
	t _{RJ}	-	2.1	-	ps	Random Jitter Vcc=3.3V	
	t _{RMS}	-	2.1	-	ps	δ(RMS of total distribution) Vcc=3.3\	
	t _{p-p}	-	18.9	-	ps	Peak to Peak Vcc=3.3V	
	t _{acc}	-	-	-	ps	-	
Phase jitter	t _{PJ}	-	0.2	-	ps	Off set Frequency: 12kHz to 20MHz Vcc=3.3V	
Phase noise	L(f)	-	-	-	dBc/Hz	-	
	, ,	-	-66	-	dBc/Hz	Off set 10Hz Vcc=3.3V	
		-	-95	-	dBc/Hz	Off set 100Hz Vcc=3.3V	
		-	-120	-	dBc/Hz	Off set 1kHz Vcc=3.3V	
		-	-131	-	dBc/Hz	Off set 10kHz Vcc=3.3V	
		-	-141	-	dBc/Hz	Off set 100kHz Vcc=3.3V	
		-	-149	-	dBc/Hz	Off set 1MHz Vcc=3.3V	
requency aging	f_age	-5	-	5	x10 ⁻⁶	@+25°C first year	
		-	_	-		-	

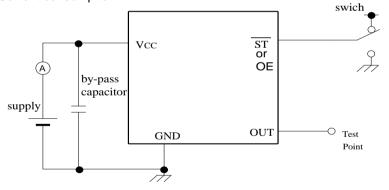
3.Timing chart



4.Test circuit

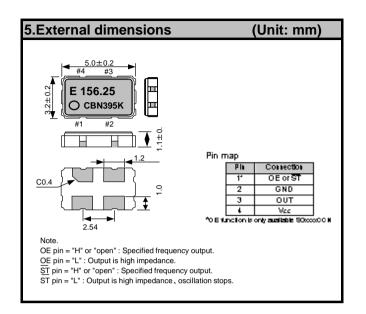
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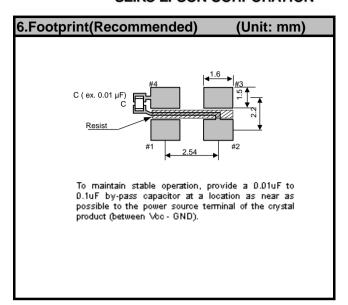
2) Current consumption

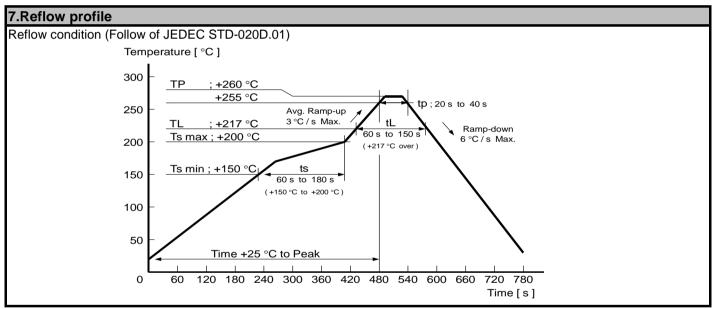


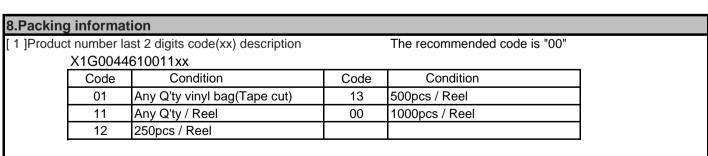
*Current consumption under the disable function should be = GND.

- 3) Condition
- (1) Oscilloscope
- · Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- * Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01 μ F to 0.1 μ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- · Start up time (0 %VCC to 90 %VCC) of power source should be more than 150 µs.
- · Impedance of power supply should be as lowest as possible.







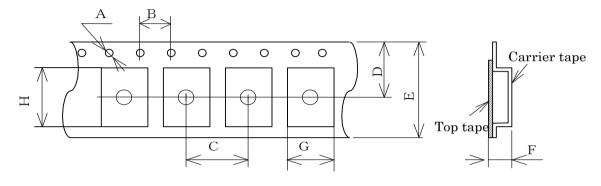


[2] Taping specification Subject to EIA-481 & IEC-60286

(1) Tape dimensions

Material of the Carrier Tape : PS Material of the Top Tape : PET+PE

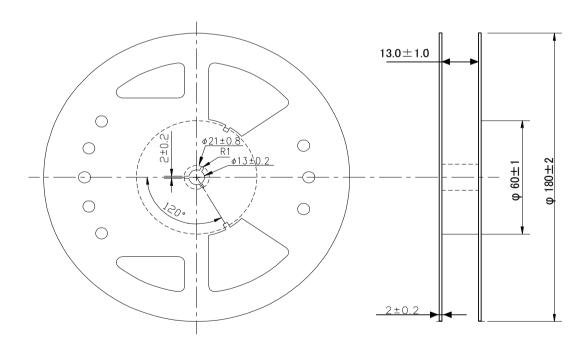
Unit: mm



Symbol	Α	В	С	D	Е	F	G	Н
Value	φ1.5	4.0±0.1	8.0±0.1	7.25±0.2	12.0±0.2	1.40±0.1	3.5±0.1	5.4±0.1
	+0.1/-0							

(2) Reel dimensions

Center material : PS Material of the Reel : PS



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