

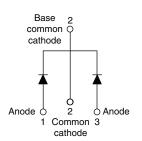
VS-30CTQ...PbF Series, VS-30CTQ...-N3 Series

Vishay Semiconductors

Schottky Rectifier, 2 x 15 A



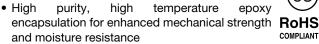
TO-220AB



PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 15 A				
V_{R}	80 V, 100 V				
V _F at I _F	0.67 V				
I _{RM} max.	7.0 mA at 125 °C				
T _J max.	175 °C				
Diode variation	Common cathode				
E _{AS}	7.50 mJ				

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	30	Α			
V _{RRM}		80/100	V			
I _{FSM}	t _p = 5 μs sine	850	Α			
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.67	V			
TJ	Range	- 55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS- 30CTQ080PbF	VS- 30CTQ080-N3	VS- 30CTQ100PbF	VS- 30CTQ100-N3	UNITS
Maximum DC reverse voltage	V_R	80	80	100	100	V
Maximum working peak reverse voltage	V_{RWM}	80	80	100	100	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per device	l=	50 % duty cycle at T _C = 129 °C, rectangular waveform		30	А
See fig. 5	per leg	I _{F(AV)}			15	
Maximum peak one cycle non-repetitive			Following any rated load condition and with rated	850	Α	
surge current per leg See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse			Α
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 60 \text{mH}$		7.50	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		0.50	Α	



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.86	V	
Maximum forward voltage drop per leg See fig. 1		30 A		1.05		
		15 A	T _J = 125 °C	0.67		
		30 A		0.82		
Maximum reverse leakage current per leg		T _J = 25 °C	V _B = Rated V _B	0.55	mA	
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	v _R = nateu v _R	7.0	IIIA	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and stort temperature range	age	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistan junction to case per leg	ce,	Р	DC operation	3.25	°C/W
Maximum thermal resistan junction to case per package	*	- R _{thJC}		1.63	
Typical thermal resistance case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf \cdot in)
Marking device			Coop of the TO 200AB	30CT	Q080
			Case style TO-220AB	30CT	30CTQ100

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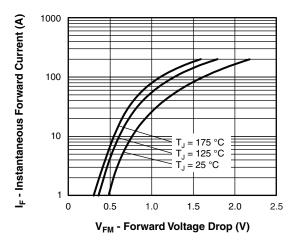


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

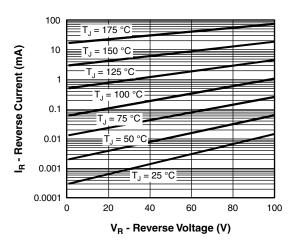


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

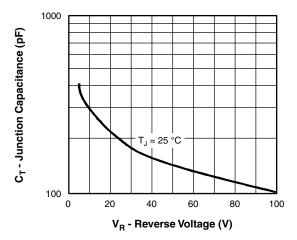


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

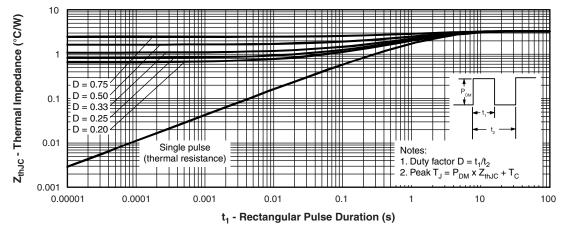


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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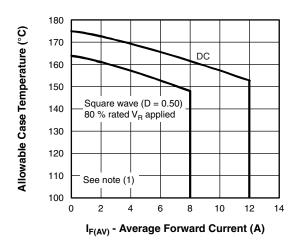


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

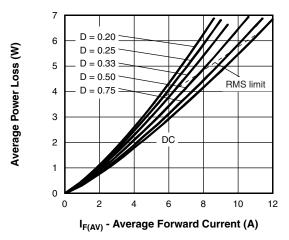


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

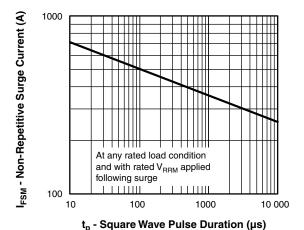


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

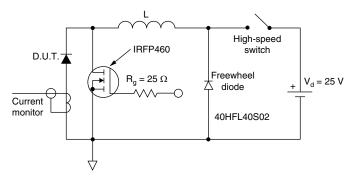


Fig. 8 - Unclamped Inductive Test Circuit

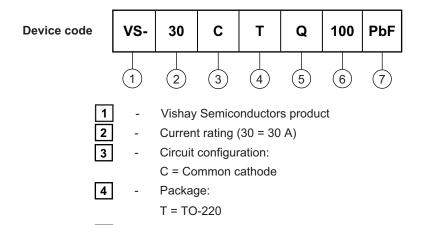
Note

 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at (I_{F(AV)}/D) (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 10 \text{ V}$

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ORDERING INFORMATION TABLE



6 - Voltage ratings - 080 = 80 V 100 = 100 V

Schottky "Q" series

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30CTQ080PbF	50	1000	Antistatic plastic tube		
VS-30CTQ080-N3	50	1000	Antistatic plastic tube		
VS-30CTQ100-N3	50	1000	Antistatic plastic tube		
VS-30CTQ100-N3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95222</u>				
Dout moulting information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000

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Vishay:

<u>30CTQ080GSTRLP</u> <u>30CTQ080GSTRRP</u> <u>30CTQ100GSTRLP</u> <u>30CTQ100GSTRRP</u> <u>VS-30CTQ080G-1PBF</u> <u>VS-30CTQ080GSPBF</u> <u>VS-30CTQ100GSPBF</u> <u>30CTQ080</u> <u>30CTQ080S</u> <u>30CTQ100</u> <u>VS-30CTQ080PBF</u>