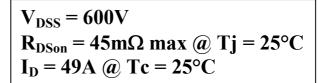
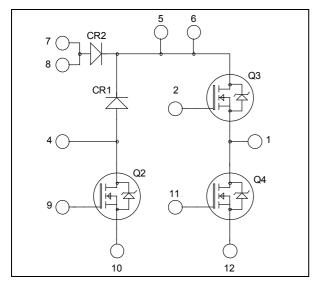


# Boost chopper & Phase Leg Super Junction MOSFET Power Module





#### **Application**

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Solar converter

#### **Features**

#### CoolMOSTM

- Ultra low R<sub>DSon</sub>
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated
- Very rugged

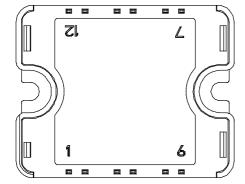
#### • SiC Schottky Diode (CR1)

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF



#### **Benefits**

- Very low stray inductance
- High level of integration
- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant



Pins 7/8; 5/6 must be shorted together

## All ratings @ $T_j = 25$ °C unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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#### 1. Phase leg (Q3 & Q4)

### Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
Ţ	Continuous Drain Current	$T_c = 25$ °C	49	
$I_D$	ip Continuous Diam Current	$T_c = 80^{\circ}C$	38	A
$I_{DM}$	Pulsed Drain current	130		
$V_{GS}$	Gate - Source Voltage	±20	V	
$R_{DSon}$	Drain - Source ON Resistance		45	$m\Omega$
$P_{D}$	Maximum Power Dissipation	$T_c = 25$ °C	250	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		15	A
E <sub>AR</sub>	Repetitive Avalanche Energy		3	m I
$E_{AS}$	Single Pulse Avalanche Energy		1900	- mJ

#### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			250	^
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			500	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$		40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 3mA$	2.1	3	3.9	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA

### **Dynamic Characteristics**

•	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V ; V_{DS} = 25V$		7.2		nF
$C_{oss}$	Output Capacitance	f = 1MHz		8.5		III
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		150		
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 300V$		34		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_{D} = 49A$		51		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C)		21		
$T_{\rm r}$	Rise Time	$V_{GS} = 10V$		30		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 49A$		100		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 5\Omega$		45		
$R_{\text{thJC}}$	Junction to Case Thermal Resistance	2			0.5	°C/W

#### **Source - Drain diode ratings and characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_S$	Continuous Source current		$Tc = 25^{\circ}C$		49		Α
	(Body diode)		$Tc = 80^{\circ}C$		38		Λ
$V_{\mathrm{SD}}$	Diode Forward Voltage	$V_{GS} = 0V, I_S = -49A$				1.2	V
dv/dt	Peak Diode Recovery <b>1</b>					4	V/ns
$t_{rr}$	Reverse Recovery Time	$I_S = -49A$	$T_j = 25^{\circ}C$		600		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$V_R = 350V$ $di_S/dt = 100A/\mu s$	$T_j = 25$ °C		17		μС

• dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 $I_S \le$  - 49A  $di/dt \le 100 A/\mu s$   $V_R \le V_{DSS}$   $T_j \le 150 ^{\circ} C$ 

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## 2. Boost chopper (CR1 & Q2)

## **Absolute maximum ratings**

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
$I_D$	Continuous Drain Current	$T_c = 25$ °C	49	
1D	Ip Continuous Diam Current	$T_c = 80$ °C	38	A
$I_{DM}$	Pulsed Drain current	130		
$V_{GS}$	Gate - Source Voltage	±20	V	
R <sub>DSon</sub>	Drain - Source ON Resistance	45	mΩ	
$P_{D}$	Maximum Power Dissipation	$T_c = 25$ °C	250	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		15	A
$E_{AR}$	Repetitive Avalanche Energy		3	mJ
$E_{AS}$	Single Pulse Avalanche Energy		1900	1113

#### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$	$T_j = 25^{\circ}C$			250	^
		$V_{GS} = 0V, V_{DS} = 600V$	$T_{j} = 125^{\circ}C$			500	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$			40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 3mA$		2.1	3	3.9	V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	V			100	nA

## **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V ; V_{DS} = 25V$		7.2		nF
$C_{oss}$	Output Capacitance	f = 1MHz		8.5		111
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		150		
$Q_{\rm gs}$	Gate – Source Charge	$V_{\text{Bus}} = 300V$		34		nC
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_{D} = 49A$		51		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C)		21		
$T_{\rm r}$	Rise Time	$V_{GS} = 10V$		30		
$T_{d(off)}$	Turn-off Delay Time	$V_{Bus} = 400V$ $I_D = 49A$		100		ns
$T_{\mathrm{f}}$	Fall Time	$R_G = 5\Omega$		45		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		405		цŢ
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 10V ; V_{Bus} = 400V$ $I_D = 49A ; R_G = 5\Omega$		520		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		660		T
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 10V ; V_{Bus} = 400V$ $I_D = 49A ; R_G = 5\Omega$		635		μJ
$R_{\text{thJC}}$	Junction to Case Thermal Resistance	e			0.5	°C/W



### SiC schottky diode ratings and characteristics (CR1)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			600			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_R=600V$	$T_j = 25^{\circ}C$		10	60	μA
-KWI	Transmission for voice Demange Content	· K ccc ·	$T_j = 175$ °C		20	300	pv. 1
$I_{F(AV)}$	Maximum Average Forward Current	50% duty cycle	Tc = 100°C		10		Α
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 10A$	$T_j = 25^{\circ}C$		1.6	1.8	V
V F	Diode Forward Voltage	$T_i = 175^{\circ}C$			2	2.4	<b>,</b>
$Q_{C}$	Total Capacitive Charge	$I_F = 10A, V_R = 300V$ di/dt = 500A/\mus			14		nC
С	Total Capacitance	$f = 1MHz, V_R =$	200V		65		pF
C	Total Capacitance	$f = 1MHz, V_R = 400V$			50		pr
$R_{\text{thJC}}$	Junction to Case Thermal Resistance				2.5	°C/W	

### 3. By pass FRED diode (CR2)

### Diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			600			V
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> =600V	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$			100 350	μΑ
$I_F$	DC Forward Current		$Tc = 80^{\circ}C$		30		A
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 30A$	$T_i = 25^{\circ}C$		1.6	2	V
<b>v</b> F	Diode Forward Voltage	$V_{GE} = 0V$	$T_{i} = 150^{\circ}C$		1.5		v
t <sub>rr</sub>	Reverse Recovery Time		$T_j = 25$ °C		100		ns
۰rr			$T_j = 150$ °C		150		115
Qrr	Reverse Recovery Charge	$I_F = 30A$ $V_R = 300V$	$T_j = 25$ °C		1.5		μС
<b>Q</b> rr	Reverse Recovery Charge	$\frac{V_R - 300 V}{\text{di/dt} = 1800 \text{A/}\mu\text{s}}$ $T_j = 150$	$T_{i} = 150^{\circ}C$		3.1		μС
Б	Davianca Dagaylani, Enginery		$T_j = 25$ °C		0.34		m I
E <sub>rr</sub>	Reverse Recovery Energy	$T_j =$			0.75		mJ
$R_{thJC}$	Junction to Case Thermal Resistance					2.45	°C/W

## 4. Thermal & Package characteristics

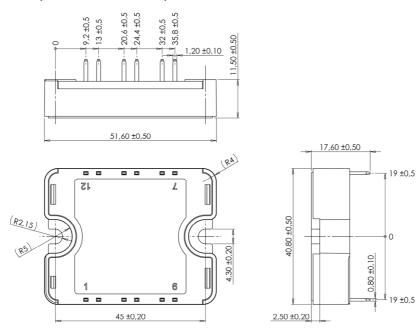
Symbol	Characteristic			Min	Typ	Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
$T_{J}$	Operating junction temperature range			-40		150*	
$T_{STG}$	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature	-40		100			
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight	·				80	g

<sup>\*</sup>  $T_{jmax}$  = 175°C for by pass and SiC diode

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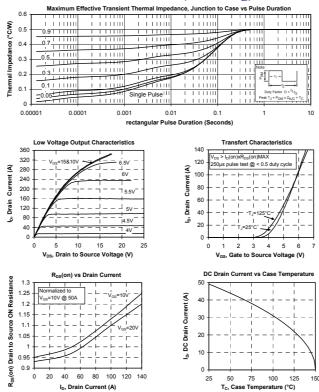


### SP1 Package outline (dimensions in mm)

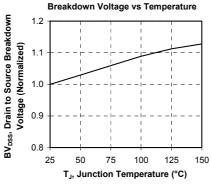


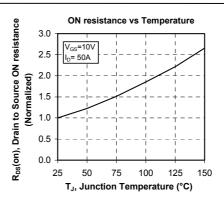
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

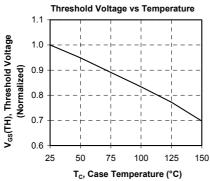
### 5. Typical CoolMOS Performance Curve (Phase leg)

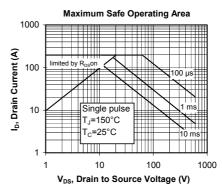


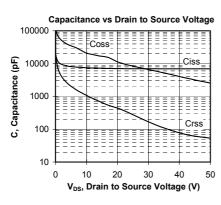


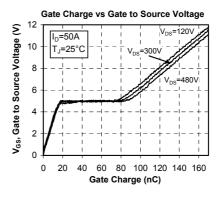


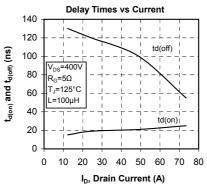


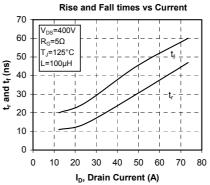








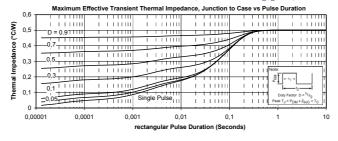


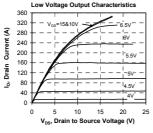


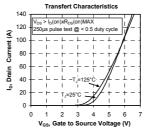
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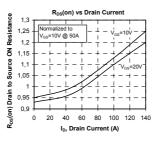


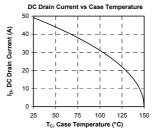
#### 6. Typical CoolMOS Performance Curve (Boost chopper)

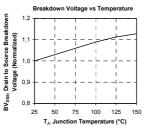


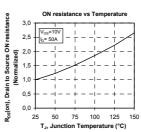


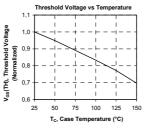


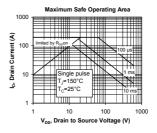


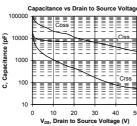


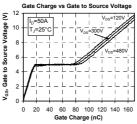




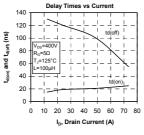


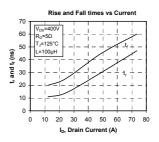


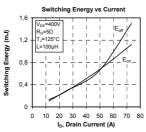


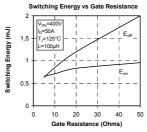


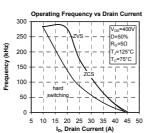


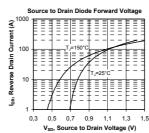




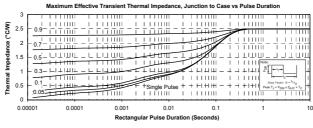


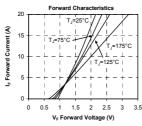


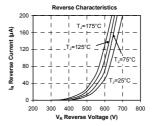




### 7. SiC Typical Performance Curve (CR1)

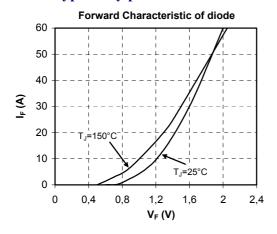


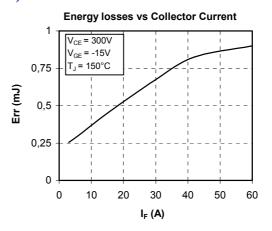


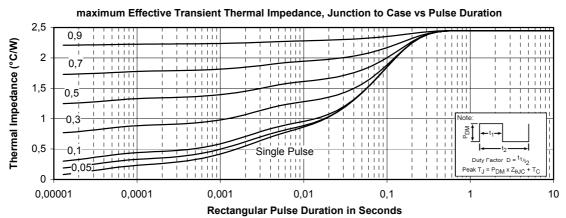




#### 8. Typical By pass Performance Curve (CR2)







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