

# NCE5558K

#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

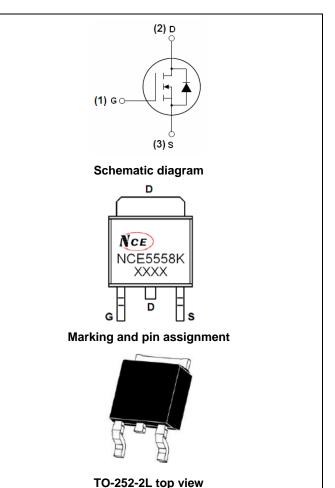
The NCE5558K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

- $V_{DS} = 55V, I_D = 58A$  $R_{DS(ON)} < 13mΩ @ V_{GS} = 10V$  (Typ:10.5mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

#### **Application**

- Synchronous rectifiers for, industrial power supplies
- LED backlighting



#### **Package Marking and Ordering Information**

	J	<u> </u>			
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE5558K	NCE5558K	TO-252-2L	-	-	-

#### Absolute Maximum Ratings (T<sub>c</sub>=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	55	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	58	А	
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	41	А	
Pulsed Drain Current	I <sub>DM</sub>	100	Α	
Maximum Power Dissipation	P <sub>D</sub>	80	W	
Derating factor		0.64	W/°C	
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 2)	R <sub>0</sub> JC	1.6	°C/W



## NCE5558K

#### Electrical Characteristics (T<sub>C</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA 55		-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =55V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	10.5	13	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	30	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	)/ 00)/)/ 0\/	-	2850	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V,	-	258	-	PF
Reverse Transfer Capacitance	$C_{rss}$	F=1.0MHz	-	26	-	PF
Switching Characteristics (Note 4)	<u> </u>					
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30V, $R_L$ =1.5 $\Omega$	-	2	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{G}$ =3 $\Omega$	-	29	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	4	-	nS
Total Gate Charge	Qg	)/ 00\/ L 00A	-	33	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =20A,	-	9	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	4	-	nC
Drain-Source Diode Characteristics	1		•	Į.		
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	58	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF =20A	-	25		nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	50		nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

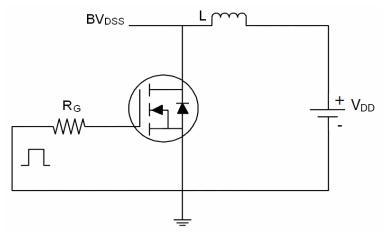
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E<sub>AS</sub> condition: Tj=25  $^{\circ}\text{C}$  ,V<sub>DD</sub>=30V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

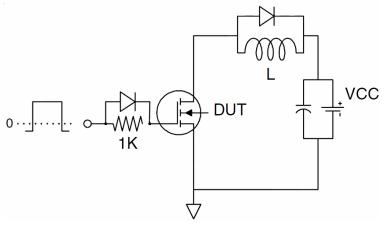
## NCE5558K

#### **Test circuit**

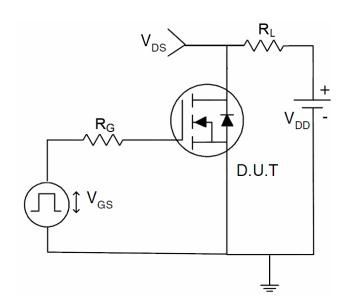
#### 1) E<sub>AS</sub> test Circuits



#### 2) Gate charge test Circuit

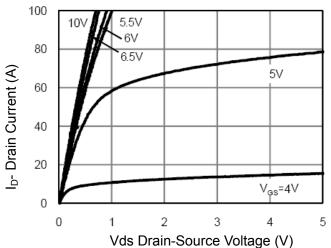


#### 3) Switch Time Test Circuit

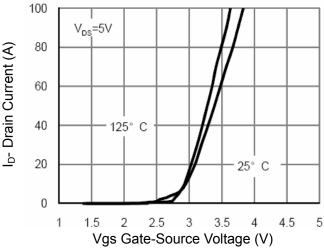




#### Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

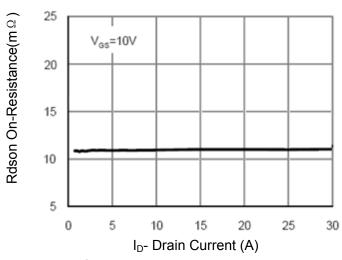


Figure 3 Rdson- Drain Current

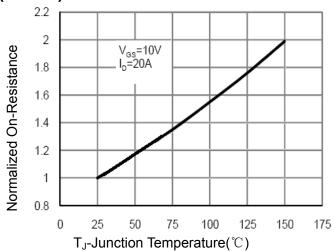


Figure 4 Rdson-JunctionTemperature

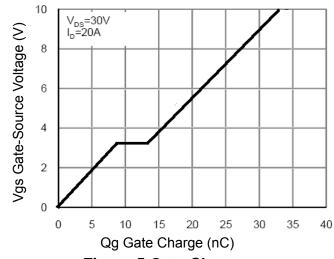


Figure 5 Gate Charge

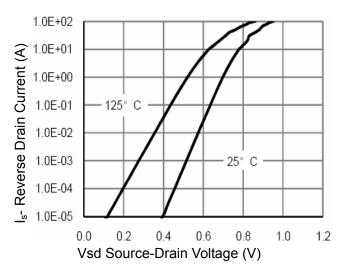


Figure 6 Source- Drain Diode Forward



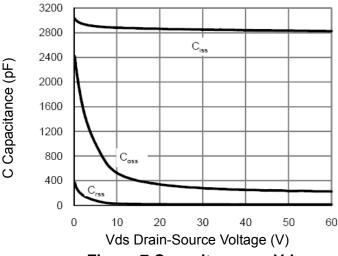
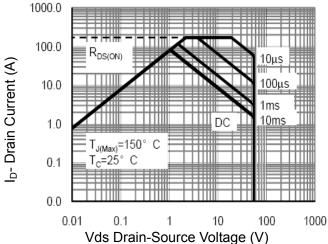


Figure 7 Capacitance vs Vds



**Figure 8 Safe Operation Area** 

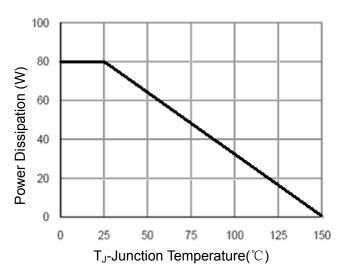
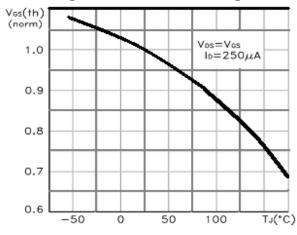
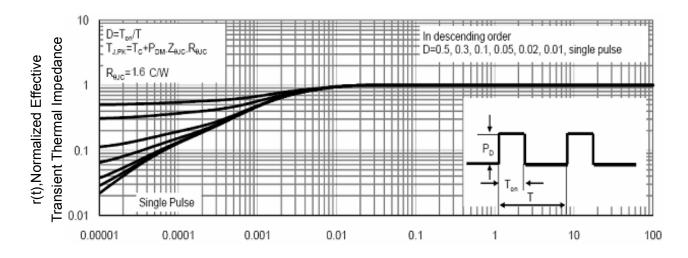


Figure 9 Power De-rating



 $T_J$ -Junction Temperature( $^{\circ}$ C)

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



Square Wave Pluse Duration(sec)

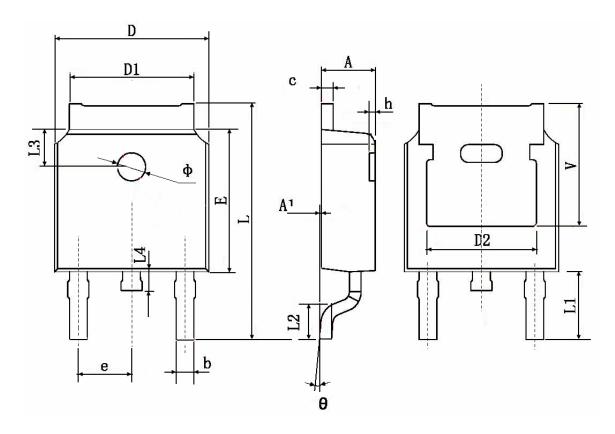
Figure 11 Normalized Maximum Transient Thermal Impedance

**Pb Free Product** 



# NCE5558K

### **TO-252 Package Information**



Symbol	Dimensions I	In Millimeters	Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.483	TYP.	0.190 TYP.			
Е	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	2.900 TYP.		TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	0.063 TYP.		1.600 TYP.		TYP.
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	TYP.	0.211 TYP.			



#### http://www.ncepower.com



### NCE5558K

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