



DMN1003UCA6

Product Summary

BV _{SSS}	R _{SS(ON)} Max	I _S T _A = +25°C
12V	3.2mΩ @ V _{GS} = 4.5V	23.6A
12 V	6.3mΩ @ V _{GS} = 2.5V	16.8A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{SS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

2 $\langle \dots \rangle$ $\langle \cdots \rangle$ \bigcirc -----_____> \odot <_____ 4 5 6 1. Source 1 Top View 2. Gate 1 3. Source 1 4. Source 2 5. Gate 2 6. Source 2

Features

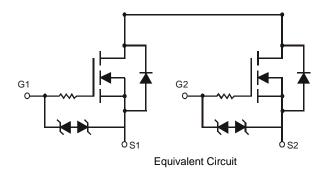
- CSP with Footprint 3.54mm × 1.77mm
- Height = 0.21mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

N-CHANNEL ENHANCEMENT MODE MOSFET

Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X3-DSN3518-6
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu. Solderable per MIL-STD-202, Method 208 @4



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1003UCA6-7	X3-DSN3518-6	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	M 1	
•	ΥM	

M1 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: E= 2017)

M or \overline{M} = Month (ex: 9 = September)

Date Code Key

20.00000000													
Year	201	5	2016		2017		2018			2020		2021	
Code	С		D		E		F			Н		I	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	N	D	



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Source-Source Voltage	V _{SSS}	12	V		
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Source Current (Note 5) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	۱ _S	23.6 18.9	А
Continuous Source Current (Note 5) V _{GS} = 2.5V	IS	16.8 13.4	А		
Pulsed Source Current (Note 6)	I _{SM}	100	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	1.05	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 7)	R _{0JA}	120.7	°C/W
Power Dissipation (Note 5)	PD	2.67	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	46.8	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Cumb al	Min	Tum	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Symbol	WIIN	Тур	wax	Unit	Test Condition	
Source-Source Breakdown Voltage	D \/	12	-	-	V	$V_{GS} = 0V, I_{S} = 1mA$	
o	BV _{SSS}	12	-	1	-		
Zero Gate Voltage Source Current $T_J = +25^{\circ}C$	I _{SSS}	-	-	•	μA	$V_{SS} = 10V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	$V_{GS} = \pm 8V, V_{SS} = 0V$	
ON CHARACTERISTICS (Note 8)				1.0			
Gate Threshold Voltage	V _{GS(TH)}	0.5	-	1.3	V	$V_{SS} = 6V, I_S = 1mA$	
		1.2	2.3	3.2		$V_{GS} = 4.5V, I_S = 5A$	
		1.2	2.4	3.2	mΩ	$V_{GS} = 4.0V, I_{S} = 5A$	
Static Source-Source On-Resistance	R _{SS(ON)}	1.3	2.5	3.2		$V_{GS} = 3.8V, I_{S} = 5A$	
		1.3	2.7	4.4		$V_{GS} = 3.1V, I_{S} = 5A$	
		1.4	3.0	6.3		$V_{GS} = 2.5V, I_S = 5A$	
Diode Forward Voltage	V _{SS}	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 3A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	-	3315	-			
Output Capacitance	C _{oss}	-	850	-	pF	$V_{SS} = 6V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	248	-			
Total Gate Charge	Qq	-	56.5	-			
Gate-Source Charge	Qgs	-	8.8	-	nC	$V_{SS} = 6V, V_{GS} = 4.5V,$	
Gate-Drain Charge	Q _{gd}	-	13.3	-	nc	I _S = 27A	
Gate Charge at V _{TH}	Q _{g(TH)}	-	6.9	-			
Turn-On Delay Time	t _{D(ON)}	-	603	-			
Turn-On Rise Time	t _R	-	1694	-		$V_{SS} = 6V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	4749	-	ns	$I_{\rm S} = 3A$	
Turn-Off Fall Time	t _F	-	6208	-			

Device mounted on FR-4 material with 1-inch² (6.45-cm²), 2-oz. (0.071-mm thick) Cu.
Repetitive rating, pulse width limited by junction temperature.
Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to production testing.

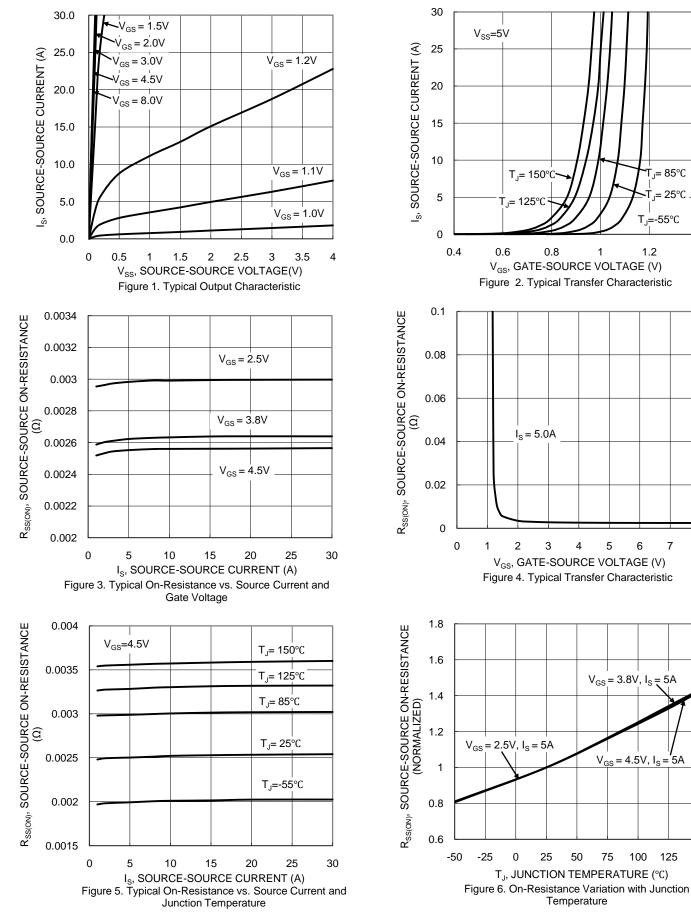
Notes:



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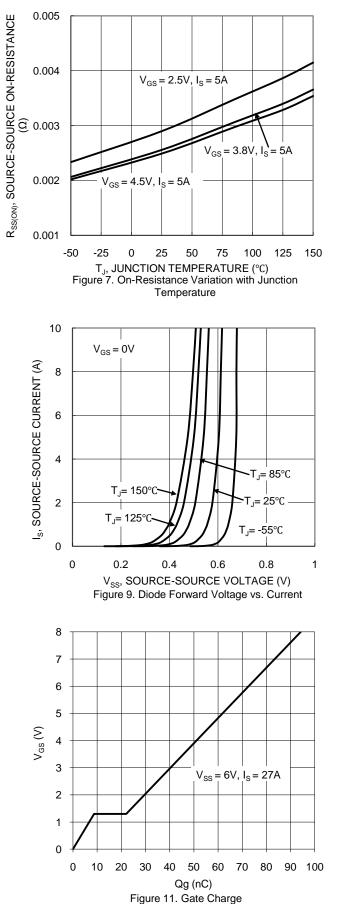
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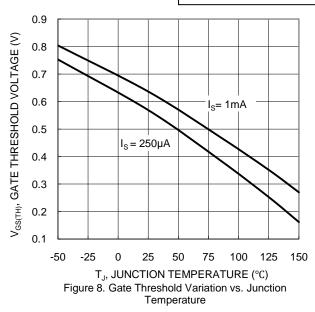


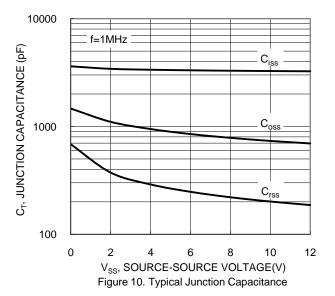
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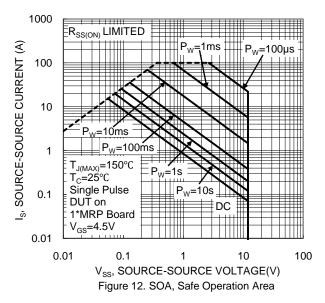


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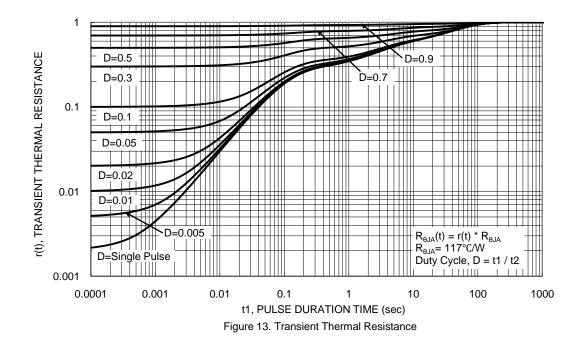






DMN1003UCA6 Document number: DS39389 Rev. 4 - 2



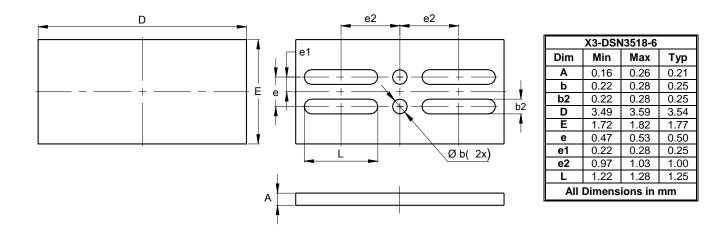




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

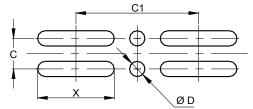
X3-DSN3518-6



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN3518-6



Dimensions	Value (in mm)
С	0.50
C1	2.00
D	0.25
Х	1.25



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