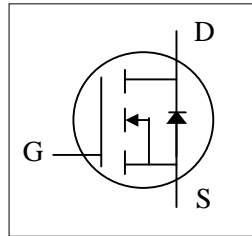
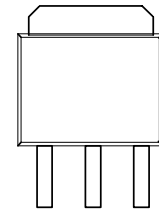




- ▼ 100% R<sub>g</sub> & UIS Test
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



$BV_{DSS}$	60V
$R_{DS(ON)}$	100m $\Omega$
$I_D$	7.5A



G D S  
TO-251VS(JV)

## Description

AP6N100 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-251VS short lead package is preferred for all commercial-industrial through-hole applications without lead-cutted.

## Absolute Maximum Ratings @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Drain Current, V <sub>GS</sub> @ 10V	7.5	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Drain Current, V <sub>GS</sub> @ 10V	4.7	A
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	30	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	12.5	W
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation	1.13	W
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>3</sup>	8.8	mJ
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Units
R <sub>thj-c</sub>	Maximum Thermal Resistance, Junction-case	10	°C/W
R <sub>thj-a</sub>	Maximum Thermal Resistance, Junction-ambient	110	°C/W



# AP6N100JV

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	-	100	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	-	125	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	-	3	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =5A	-	14	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =5A	-	10	16	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =48V	-	1.5	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =10V	-	2	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =30V	-	4	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =5A	-	11	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω	-	13	-	ns
t <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V	-	2	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	420	672	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =30V	-	32	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	24	-	pF
R <sub>g</sub>	Gate Resistance	f=1.0MHz	-	0.9	1.8	Ω

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =5A, V <sub>GS</sub> =0V,	-	11	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	6	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Starting T<sub>j</sub>=25°C , V<sub>DD</sub>=30V , L=1mH , R<sub>G</sub>=25Ω

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

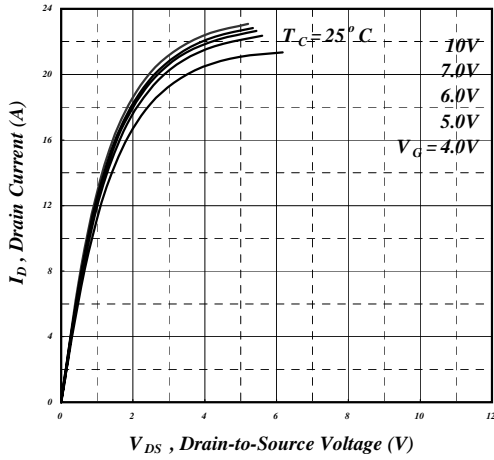


Fig 1. Typical Output Characteristics

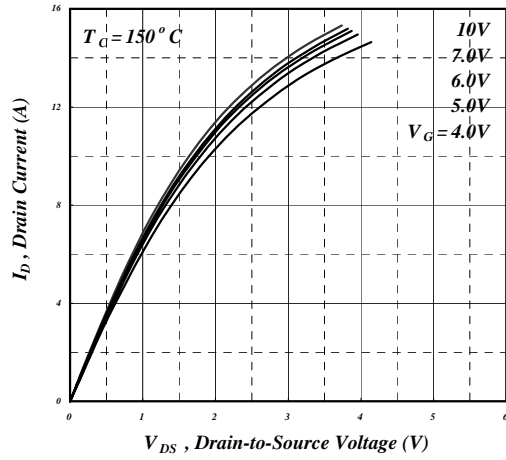


Fig 2. Typical Output Characteristics

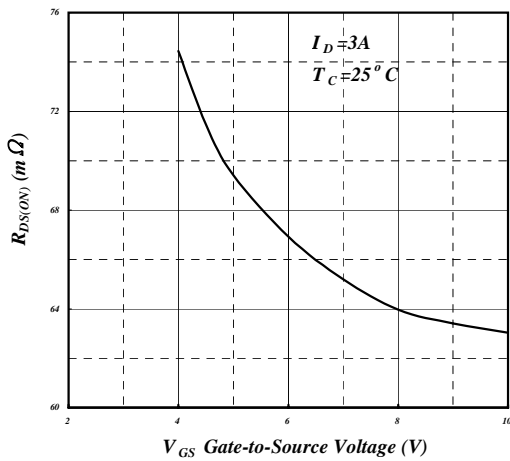


Fig 3. On-Resistance v.s. Gate Voltage

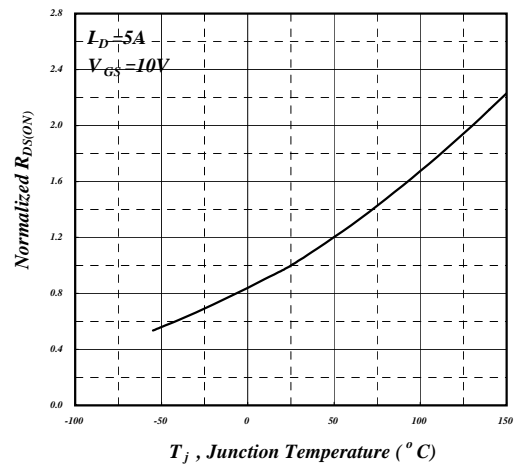


Fig 4. Normalized On-Resistance v.s. Junction Temperature

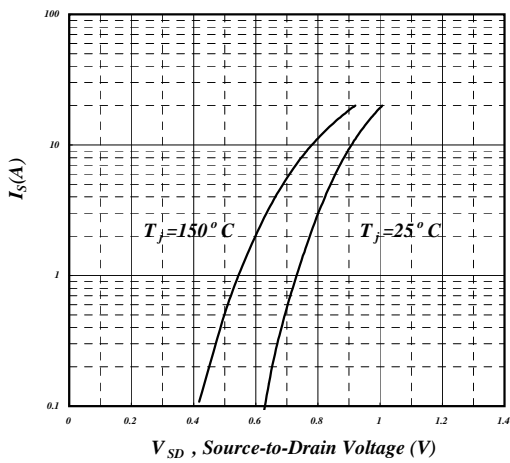


Fig 5. Forward Characteristic of Reverse Diode

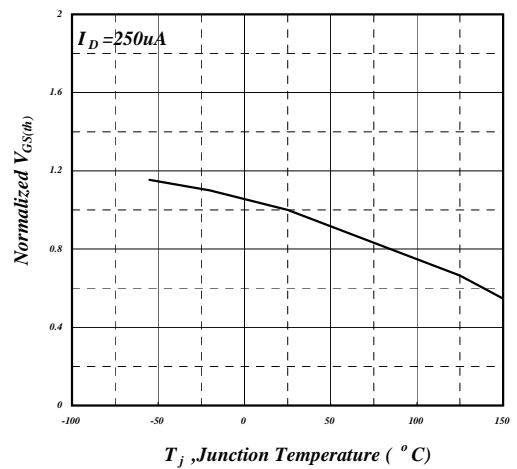


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

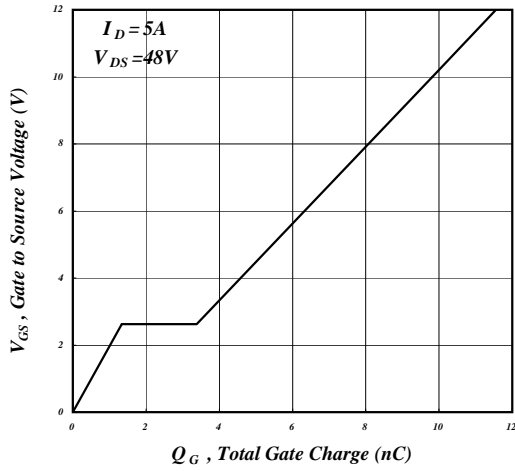


Fig 7. Gate Charge Characteristics

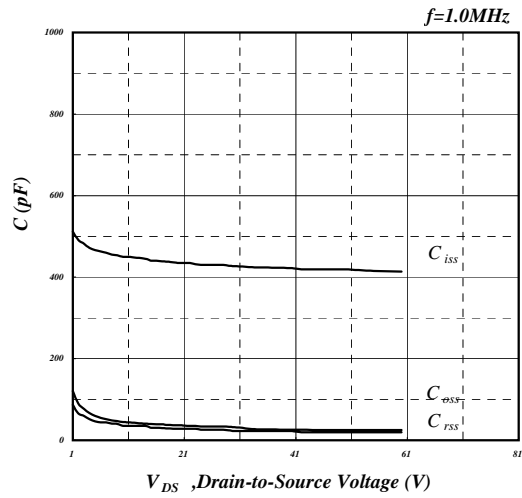


Fig 8. Typical Capacitance Characteristics

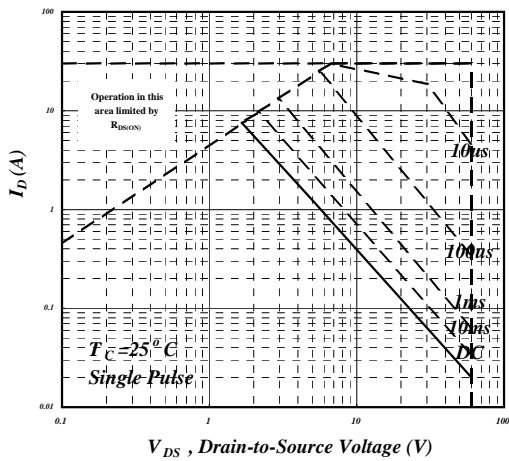


Fig 9. Maximum Safe Operating Area

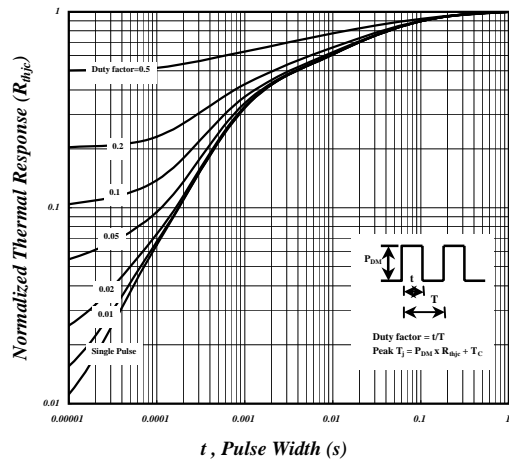


Fig 10. Effective Transient Thermal Impedance

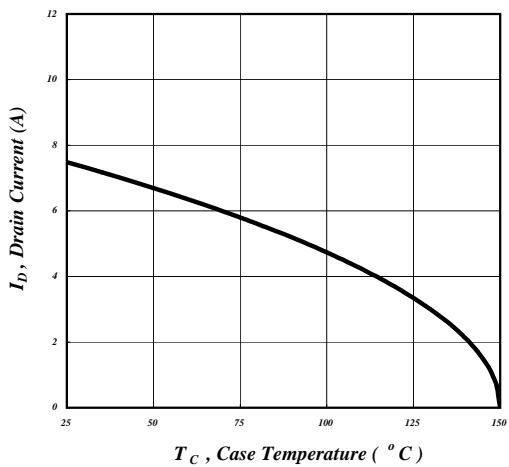


Fig 11. Drain Current v.s. Case Temperature

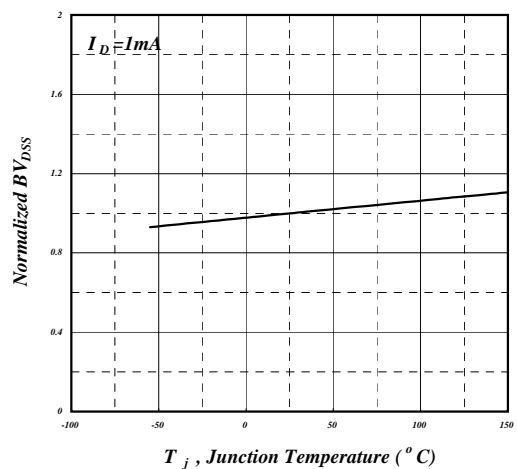


Fig 12. Normalized  $BV_{DSS}$  v.s. Junction Temperature

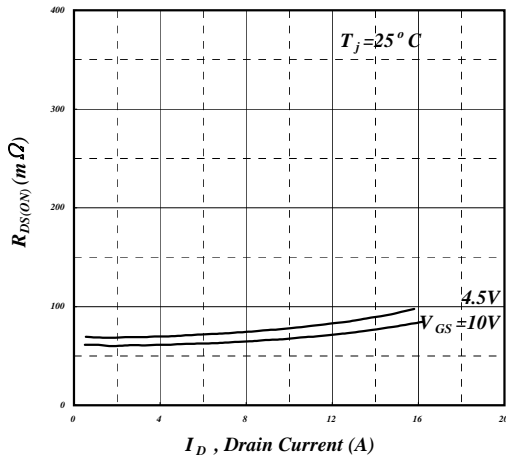


Fig 13. Typ. Drain-Source on State Resistance

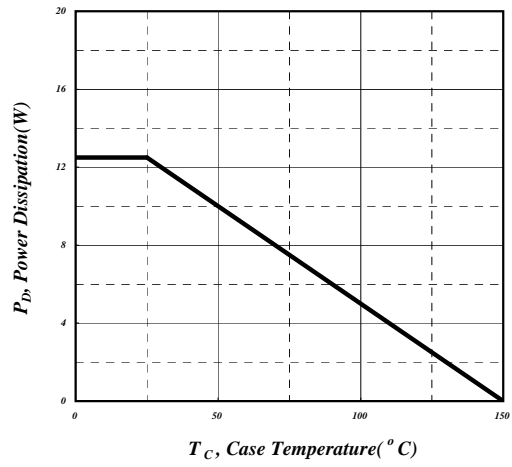


Fig 14. Total Power Dissipation

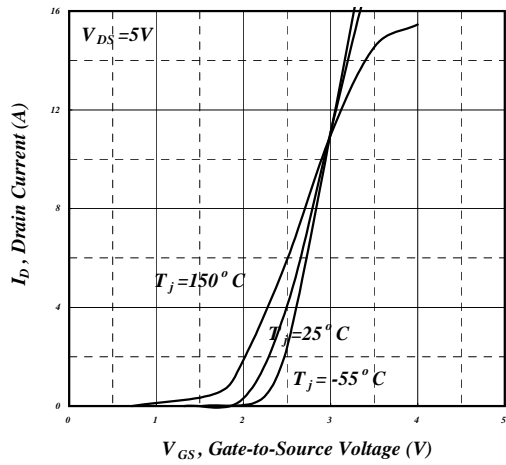


Fig 15. Transfer Characteristics



# AP6N100JV

## MARKING INFORMATION

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