

Description: PLCC Surface Mount Foot

44 position PLCC surface mount foot (J-Leaded) to 44 position high density pin interface. Pin asignment as per top view

Tolerances: diameters ± 0.03 mm [± 0.001 "], PCB perimeters ± 0.13 mm [± 0.005 "], PCB thicknesses ± 0.18 mm [± 0.007 "], pitches (from true position) ± 0.08 mm [± 0.003 "], all other tolerances ± 0.13 mm [± 0.005 "] unless stated otherwise.

