

4V Drive Pch MOSFET

UM6J1N

Structure

Silicon P-channel MOSFET

● Features

- 1) Two RSU002P03 transistors in a single UMT package.
- 2) The MOSFET elements are independent, eliminating mutual interference.
- 3) Mounting cost and area can be cut in half.

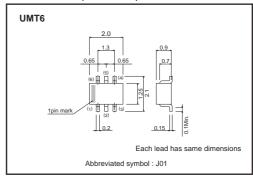
Applications

Switching

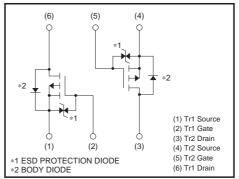
Packaging specifications

	Package	Taping
Type	Code	TN
	Basic ordering unit (pieces)	3000
UM6J1N		0

●Dimensions (Unit: mm)



•Inner circuit



●Absolute maximum ratings (Ta=25°C)

<It is the same ratings for Tr1 and Tr2.>

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	-30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	Continuous	I_D	±0.2	А	
	Pulsed	IDP *1	±0.4	А	
Total power dissipation		D. *2	150	mW / TOTAL	
		P _D *2	120	mW / ELEMENT	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1%

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	833	°C/W / TOTAL
Charmer to ambient		1042	°C/W / ELEMENT

^{*} Each terminal mounted on a recommended land

^{*2} Each terminal mounted on a recommended land

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μА	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	-30	-	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	V _{DS} = -30V, V _{GS} =0V
Gate threshold voltage	V _{GS} (th)	-1.0	-	-2.5	V	V_{DS} = -10V, I_{D} = -1mA
Static drain-source on-state resistance	RDS (on)	_	0.9	1.4	Ω	I _D = -0.2A, V _G s= -10V
		_	1.4	2.1	Ω	I _D = -0.15A, V _G S= -4.5V
		_	1.6	2.4	Ω	I _D = -0.15A, V _G S= -4V
Forward transfer admittance	Y _{fs} *	0.2	-	-	S	V _{DS} = -10V, I _D = -0.15A
Input capacitance	Ciss	_	30	-	pF	V _{DS} = -10V
Output capacitance	Coss	_	4	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	5	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	8	_	ns	V _{DD} ≒ −15V
Rise time	tr *	_	5	_	ns	ID= -0.15A
Turn-off delay time	td (off)*	_	30	-	ns	Vgs= −10V RL≒ 100Ω
Fall time	t _f *	_	40	_	ns	R _G =10Ω

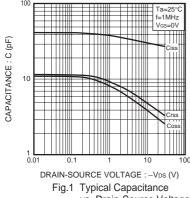
^{*} Pulsed

●Body diode characteristics (source-drain)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	-1.2	V	Is= -0.1A, Vgs=0V

^{*}Pulsed

•Electrical characteristic curves



vs. Drain-Source Voltage

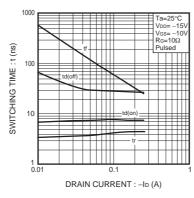


Fig.2 Switching Characteristics

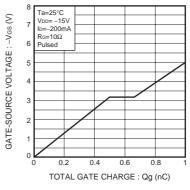


Fig.3 Dynamic Input Characteristics

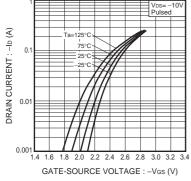


Fig.4 Typical Transfer Characteristics

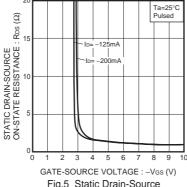


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

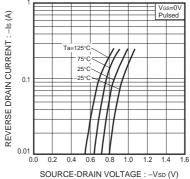


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

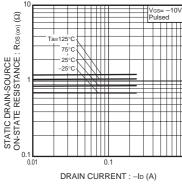


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

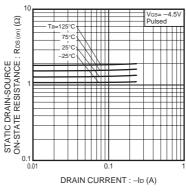


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

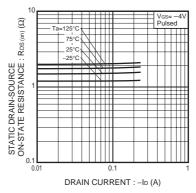


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

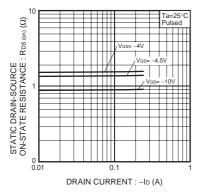


Fig.10 Static Drain-Source On-State Resistance vs. Drain Current (IV)

Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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