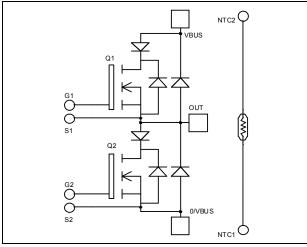
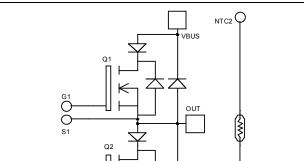


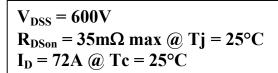
Phase leg Series & SiC parallel diodes Super Junction MOSFET Power Module



O/VBUS

S2





Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

$CoolMOS^{TM}$

- Ultra low R_{DSon}
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated

Parallel SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
- Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- **RoHS Compliant**

All ratings (a) $T_i = 25$ °C unless otherwise specified

Absolute maximum ratings

VBUS

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
Ţ	(Confinitoris Drain Current	$T_c = 25^{\circ}C$	72	
I_D		$T_c = 80$ °C	54	A
I_{DM}	Pulsed Drain current		288	
V_{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		35	mΩ
P_{D}	Maximum Power Dissipation $T_c = 25^{\circ}C$		416	W
I_{AR}	Avalanche current (repetitive and non repetitive)		20	A
E_{AR}	Repetitive Avalanche Energy		1	m I
E_{AS}	Single Pulse Avalanche Energy		1800	mJ

OUT

OUT

NTC2 NTC1

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			50	μА
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			500	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 36A$			35	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2mA$	2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±150	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		14		
C_{oss}	Output Capacitance	$V_{\rm DS} = 25V$		5.13		nF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		0.42		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		518		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 300 \text{V}$		58		nC
Q_{gd}	Gate – Drain Charge	$I_D = 72A$		222		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 400V$ $I_D = 72A$		21		
$T_{\rm r}$	Rise Time			30		ns
$T_{d(off)}$	Turn-off Delay Time			283		
T_{f}	Fall Time	$R_G = 2.5\Omega$		84		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		804		
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 72A, R_G = 2.5\Omega$		1960		μJ
E_{on}	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V$, $V_{Bus} = 400V$ $I_D = 72A$, $R_G = 2.5\Omega$		1315		
E _{off}	Turn-off Switching Energy			2412		μJ
R_{thJC}	Junction to Case Thermal Resistance				0.3	°C/W

Series diode ratings and characteristics

Symbol	Characteristic Test Conditions		Min	Тур	Max	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage					600	V
I_{RM}	Reverse Leakage Current	$V_{R} = 600V$				150	μA
I_{F}	DC Forward current		$Tc = 80^{\circ}C$		100		A
$V_{\rm F}$	Diode Forward Voltage	$I_F = 100A$ $V_{GE} = 0V$	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$		1.6 1.5	2	V
,	Davaga Dagayamy Tima		$T_j = 25$ °C		100		200
t_{rr}	Reverse Recovery Time		$T_{j} = 150^{\circ}C$		150		ns
0	Payarsa Racovary Charga	$I_F = 100A$ $V_R = 300V$	$T_j = 25$ °C		5.1		μС
Q_{rr}	Reverse Recovery Charge $V_R = 300V$ di/dt =2500A/ μ s	$T_{j} = 150^{\circ}C$		10.7		μС	
E_{rr}	Reverse Recovery Energy]	$T_i = 25^{\circ}C$		1.2		mJ
L _{III}	Reverse Recovery Energy		$T_{j} = 150^{\circ}C$		2.4		1113
R_{thJC}	Junction to Case Thermal Resistance					0.71	°C/W



Parallel SiC diode ratings and characteristics

Symbol	Characteristic	racteristic Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage					600	V
I_{RM}	Reverse Leakage Current	V _R =600V	$T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$		200 400	800 4000	μΑ
I_{F}	DC Forward Current		Tc = 125°C		40		A
$V_{\rm F}$	Diode Forward Voltage	$I_F = 40A$	$T_i = 25^{\circ}C$ $T_j = 175^{\circ}C$		1.6 2.0	1.8	V
Qc	Total Capacitive Charge	$I_F = 40A, V_R = 600V$ di/dt =1200A/ μ s			112		nC
f = 1MHz		$f = 1 MHz, V_R$	= 200V		260		E
С	Total Capacitance	$f = 1MHz, V_R = 400V$			200		pF
R _{thJC}	Junction to Case Thermal Resistance				0.8	°C/W	

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V	
T_{J}	Operating junction temperature range			-40	150		
T_{JOP}	Recommended junction temperature under switching conditions			-40	T _J max -25	°C	
T_{STG}	Storage Temperature Range			-40	125		
$T_{\rm C}$	Operating Case Temperature				100		
Torque	Mounting torque	To Heatsink	M5	2.5	4.7	N.m	
Wt	Package Weight				160	g	

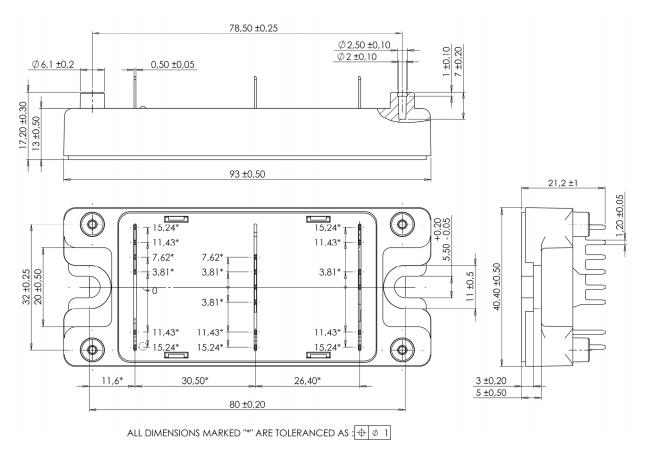
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Тур	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta \mathrm{B/B}$		T _C =100°C		4		%

$$R_{T} = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{1.7}} - \frac{1}{T} \right) \right]}$$
 T: Thermistor temperature R_T: Thermistor value at T



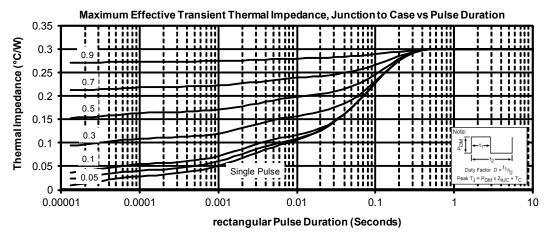
SP4 Package outline (dimensions in mm)

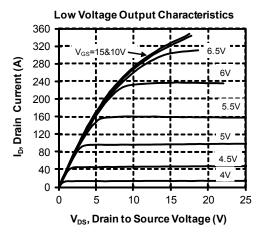


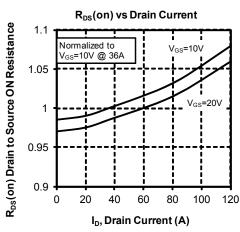
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

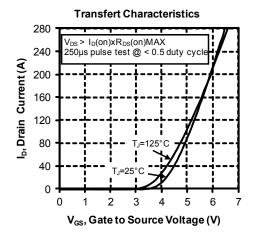


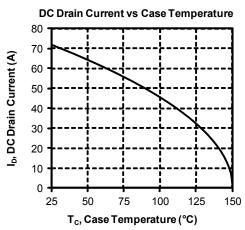
Typical CoolMOS Performance Curve



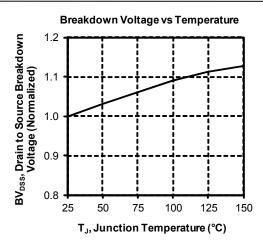


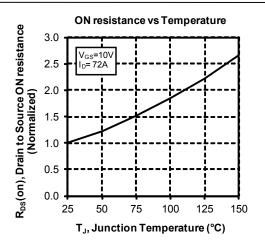


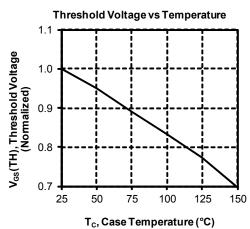


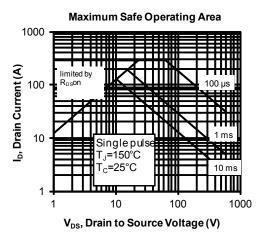


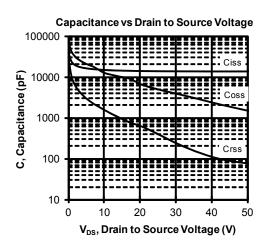


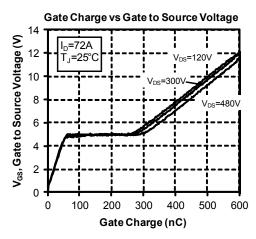




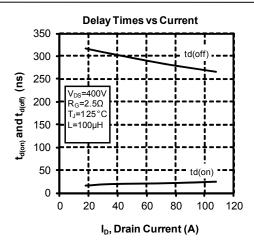


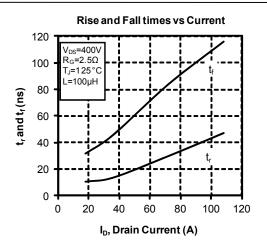


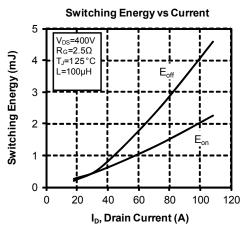


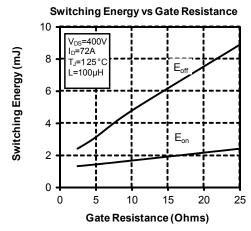


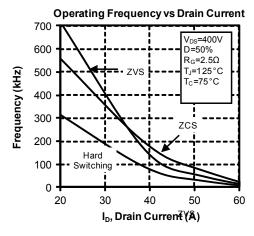






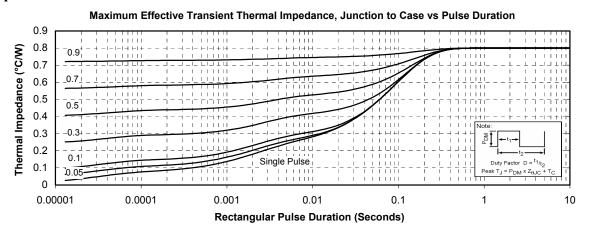


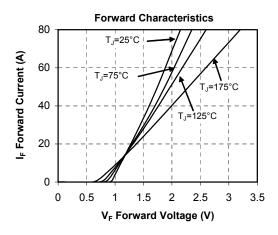


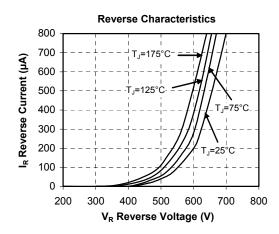


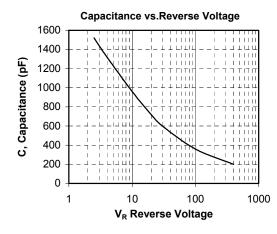


Typical SiC Diode Performance Curve









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