**AEC-Q101 Qualified** 

# 4V Drive Pch MOS FET RSQ035P03FRA

### Structure

Silicon P-channel MOSFET

# ● Features

- 1) Low On-resistance.( $65m\Omega$  at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive. (4V)

# Applications

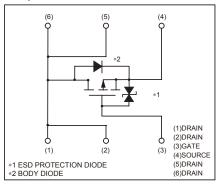
DC-DC converter

# TSMT6 1.0MAX 1.9 0.055, 0.95 0.7 1.0MAX 0.85 0.7 1.0MAX 0.05 0.7 1.0MAX 0.05 0.7 Each lead has same dimensions Abbreviated symbol : TM

# Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
RSQ035P03F	0	

# Equivalent circuit



# ●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit			
Drain-source voltage		VDSS	-30	V			
Gate-source voltage		Vgss	±20	V			
Drain current	Continuous	ΙD	±3.5	A			
	Pulsed	IDP *1	±14	A			
Source current (Body diode)	Continuous	ls	-1	A			
	Pulsed	Isp *1	-4	A			
Total power dissipation		P <sub>D</sub> *2	1.25	W			
Channel temperature		Tch	150	°C			
Range of Strage temperature		Tstg	-55 to +150	°C			

<sup>\*1</sup> Pw≤10μs, Duty cycle≤1%

# \*2 Mounted on a ceramic board Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	100	°C / W

<sup>\*</sup> Mounted on a ceramic board.

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	_	±10	μΑ	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	
Drain-source breakdown voltage	V(BR)DSS	-30	_	_	V	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	
Gate threshold voltage	VGS(th)	-1.0	_	-2.5	V	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	
		-	45	65	mΩ	ID=-3.5A, VGS=-10V	
Static drain-source on-state	RDS(on)*	_	65	90	mΩ	ID=-3.5A, VGS=-4.5V	
resistance		_	70	95	mΩ	I <sub>D</sub> =-1.75A, V <sub>G</sub> s=-4.0V	
Foward transfer admittance	Y <sub>fs</sub>   *	2.0	_	_	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.75A	
Input capacitance	Ciss	_	780	_	pF		
Output capacitance	Coss	-	180	_	pF	V <sub>DS</sub> =-10V,V <sub>GS</sub> =0V f=1MHz	
Reverse transfer capacitance	Crss	_	130	_	pF		
Turn-on delay time	td(on) *	_	15	_	ns	I <sub>D</sub> =-1.75A V <sub>DD</sub> =-15V V <sub>GS</sub> =-10V R <sub>L</sub> =8.6Ω R <sub>G</sub> =10Ω	
Rise time	tr *	_	35	_	ns		
Turn-off delay time	td(off) *	_	45	_	ns		
Fall time	t <sub>f</sub> *	-	25	_	ns		
Total gate charge	Qg	ı	9.2	_	nC	V <sub>DD</sub> ≒-15V V <sub>GS</sub> =-5V I <sub>D</sub> =-3.5A	
Gate-source charge	Qgs	-	2.2	_	nC		
Gate-drain charge	Qgd	-	3.4	-	nC		

<sup>\*</sup>PULSED

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	_	ı	-1.2	V	Is=-1A, V <sub>GS</sub> =0V

### •Electrical characteristic curves

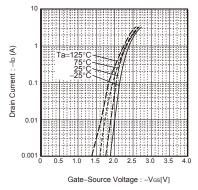


Fig.1 Typical Transfer Characteristics

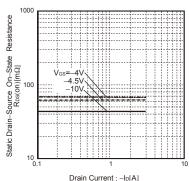


Fig.2 Static Drain–Source On–State Resistance vs.Drain Current

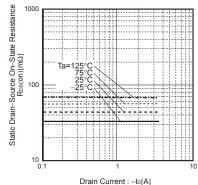


Fig.3 Static Drain–Source On–State Resistance vs.Drain Current

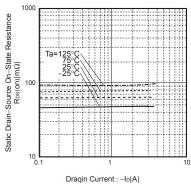


Fig.4 Static Drain–Source On–State vs.Drain–Current

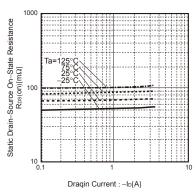


Fig.5 Static Drain–Source On–State vs.Drain–Current

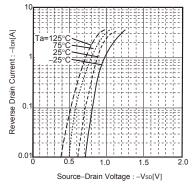


Fig.6 Reverse Drain Current Source-Drain Current

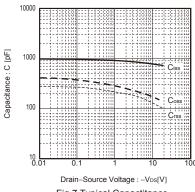


Fig.7 Typical Capactitance vs.Drain-Source Voltage

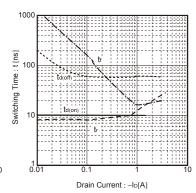
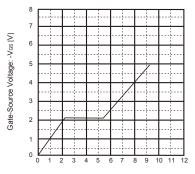


Fig.8 Switching Characteristics



Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

# •Switching characteristics measurement circuits

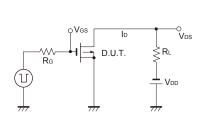


Fig.10 Switching Time Test Circuit

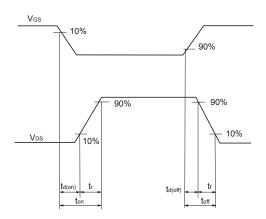


Fig.11 Switching Time Waveforms

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Ì	JÁPAN	USA	EU	CHINA	
Γ	CLASSⅢ	CLACCIII	CLASS II b	СГУССШ	
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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