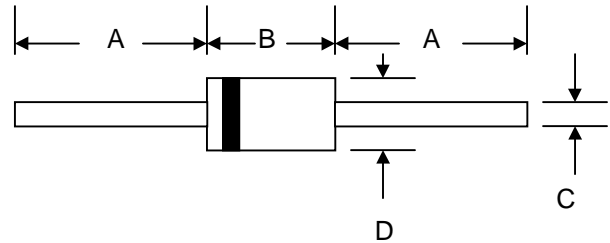


Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications



Mechanical Data

- Case: DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version**

| DO-201AD | | |
|----------------------|------|------|
| Dim | Min | Max |
| A | 24.5 | — |
| B | 7.20 | 9.50 |
| C | 1.10 | 1.30 |
| D | 5.00 | 5.60 |
| All Dimensions in mm | | |

Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

| Characteristic | Symbol | SR 1020L | SR 1030L | SR 1040L | SR 1050L | SR 1060L | SR 1080L | SR 10100L | SR 10150L | SR 10200L | Unit |
|---|-----------------|-------------|----------|----------|----------|----------|----------|-----------|-----------|-----------|--------------------|
| Peak Repetitive Reverse Voltage | V_{RRM} | | | | | | | | | | V |
| Working Peak Reverse Voltage | V_{RWM} | 20 | 30 | 40 | 50 | 60 | 80 | 100 | 150 | 200 | |
| DC Blocking Voltage | V_R | | | | | | | | | | |
| RMS Reverse Voltage | $V_{R(RMS)}$ | 14 | 21 | 28 | 35 | 42 | 56 | 70 | 105 | 140 | V |
| Average Rectified Output Current (Note 1) | I_O | 10.0 | | | | | | | | | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | I_{FSM} | 150 | | | | | | | | | A |
| Forward Voltage @ $I_F = 10.0A$ | V_{FM} | 0.45 | | | 0.55 | | 0.80 | | 0.85 | 0.90 | V |
| Peak Reverse Current At Rated DC Blocking Voltage @ $T_A = 25^\circ\text{C}$ @ $T_A = 100^\circ\text{C}$ | I_{RM} | 0.2 | | | | | | | 0.2 | | mA |
| | | 20 | | | | | | | 10 | | |
| Typical Junction Capacitance (Note 2) | C_j | 250 | | | | | | | | | pF |
| Typical Thermal Resistance (Note 1) | $R_{\theta JA}$ | 2.5 | | | | | | | | | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range | T_j, T_{STG} | -65 to +150 | | | | | | | | | $^\circ\text{C}$ |

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

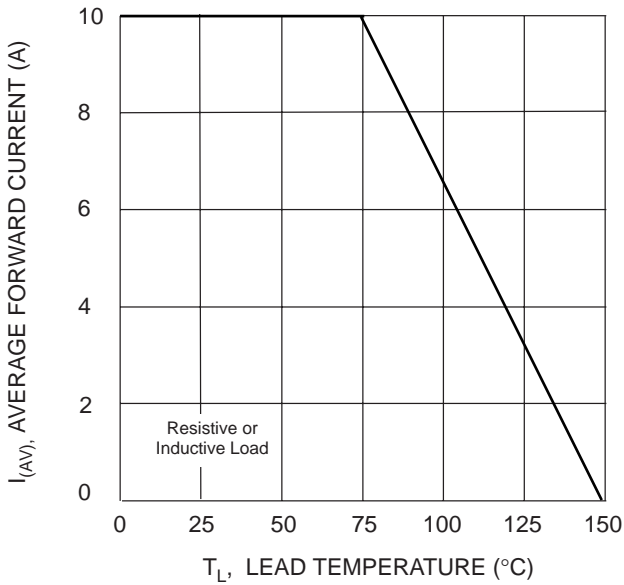


Fig. 1 Forward Current Derating Curve

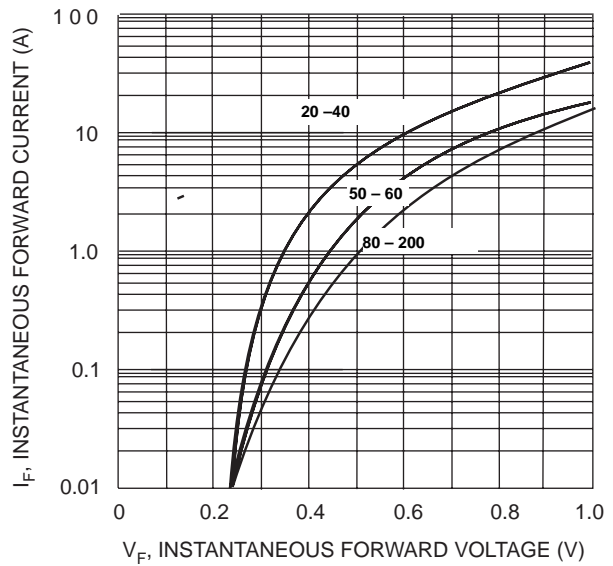


Fig. 2 Typical Forward Characteristics

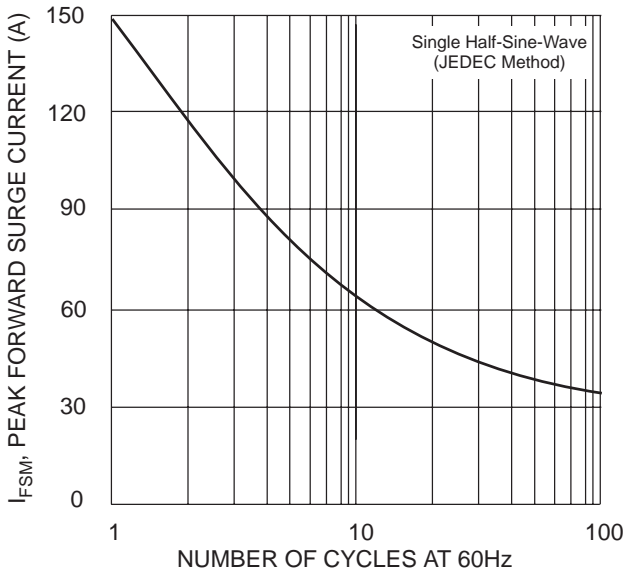


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

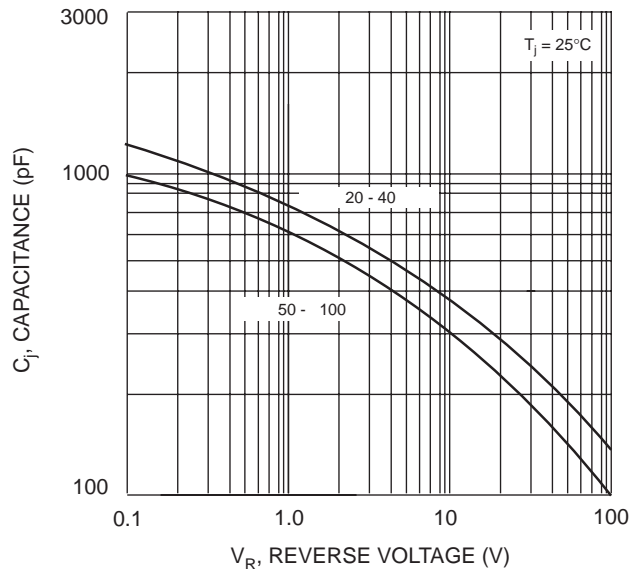


Fig. 4 Typical Junction Capacitance

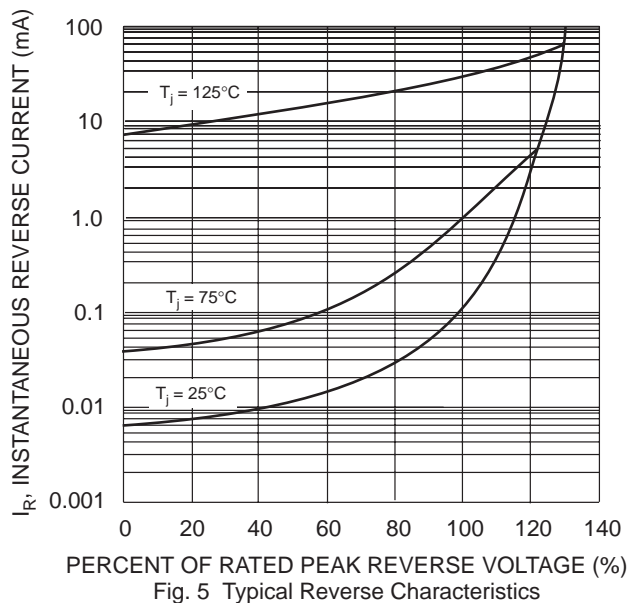


Fig. 5 Typical Reverse Characteristics