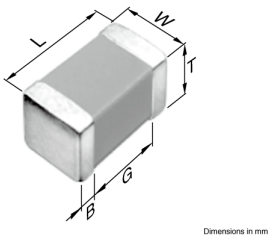
Creation Date : April 03, 2017 (GMT)

Multilayer Ceramic Chip Capacitors

C1005C0G1H221F050BA

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C1005C0G1H221FT****
Applications	Commercial Grade
Feature	General (Up to 50V)
Series	C1005 [EIA 0402]
Status	Production (Not Recommended for New Design)



Size	
Length(L)	1.00mm ±0.05mm
Width(W)	0.50mm ±0.05mm
Thickness(T)	0.50mm ±0.05mm
Terminal Width(B)	0.10mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.30mm to 0.50mm
Recommended Land Pattern (PB)	0.35mm to 0.45mm
Recommended Land Pattern (PC)	0.40mm to 0.60mm

Electrical Characteristics	
Capacitance	220pF ±1%
Rated Voltage	50VDC
Temperature Characteristic	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	10000pcs

! Images are for reference only and show exemplary products.
! This PDF document was created based on the data listed on the TDK Corporation website.
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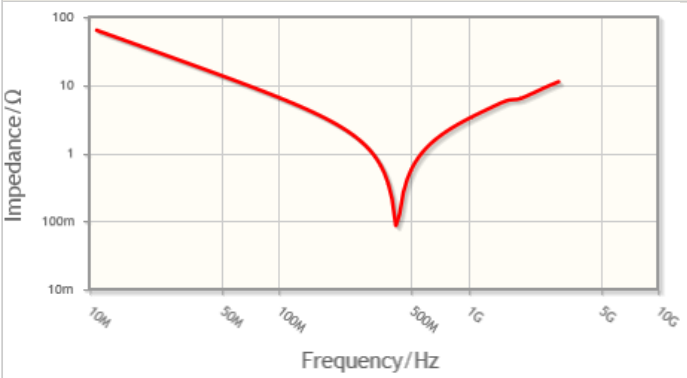
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



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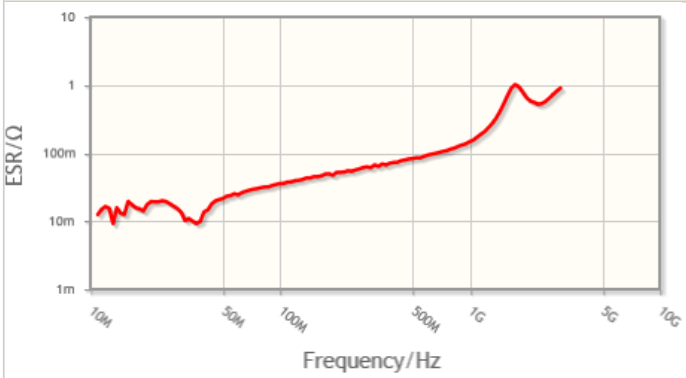
ESR



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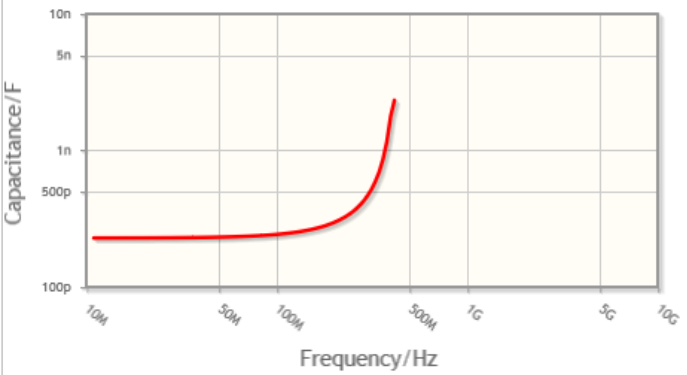
Capacitance



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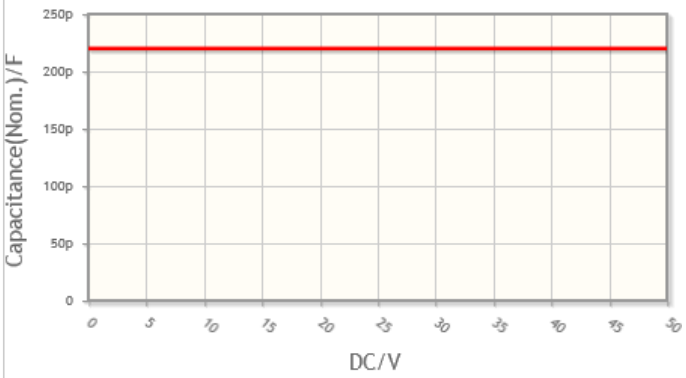
DC Bias Characteristic



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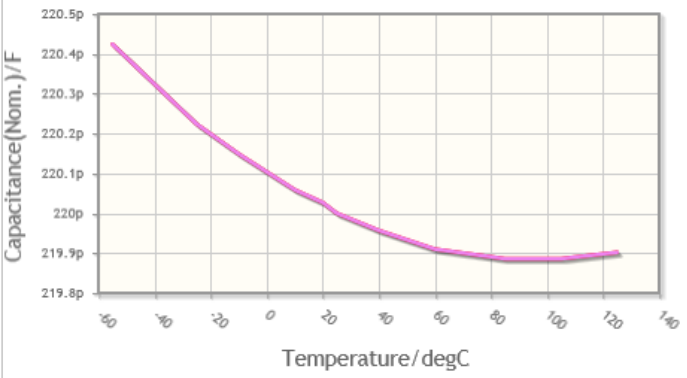
Temperature Characteristic



C1005C0G1H221F050BA(No Bias) C1005C0G1H221F050BA(DC Bias = 25V)

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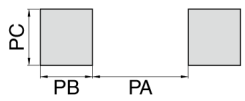
Associated Images

Land Pattern (Terminal Connection)



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