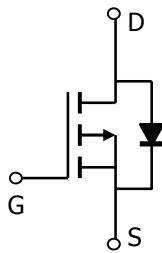
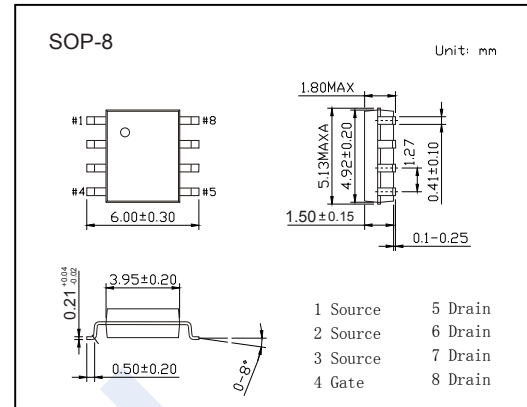


P-Channel MOSFET

AO4443 (KO4443)

■ Features

- $V_{DS} (V) = -40V$
- $I_D = -6.5 A (V_{GS} = -10V)$
- $R_{DS(ON)} < 42m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 63m\Omega (V_{GS} = -4.5V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	-40	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	$T_A = 25^\circ C$	-6.5	A
		$T_A = 70^\circ C$	-5	
Pulsed Drain Current	I_{DM}	-20		
Power Dissipation	P_D	$T_A = 25^\circ C$	3.1	W
		$T_A = 70^\circ C$	2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	40	$^\circ C/W$
		Steady-State	75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	30		
Junction Temperature	T_J	150	$^\circ C$	
Junction Storage Temperature Range	T_{stg}	-55 to 150		

P-Channel MOSFET

AO4443 (KO4443)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μA, V _{GS} =0V	-40			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-32V, V _{GS} =0V			-1	μA
		V _{DS} =-32V, V _{GS} =0V, T _J =55°C			-5	
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1		-3	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-6A			42	mΩ
		V _{GS} =-10V, I _D =-6A T _J =125°C			68	
		V _{GS} =-4.5V, I _D =-5A			63	
On state drain current	I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5V	-20			A
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-6A		14		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-20V, f=1MHz		657		pF
Output Capacitance	C _{oss}			143		
Reverse Transfer Capacitance	C _{rss}			63		
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		6.5		Ω
Total Gate Charge (10V)	Q _g	V _{GS} =-10V, V _{DS} =-20V, I _D =-6A		14.2		nC
Total Gate Charge (4.5V)				7.1		
Gate Source Charge	Q _{gs}			2.2		
Gate Drain Charge	Q _{gd}			4.1		
Turn-On DelayTime	t _{d(on)}			7.7		
Turn-On Rise Time	t _r	V _{GS} =-10V, V _{DS} =-20V, R _L =3.7Ω, R _{GEN} =3Ω		8		ns
Turn-Off DelayTime	t _{d(off)}			26.5		
Turn-Off Fall Time	t _f			11.5		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-6A, di/dt=100A/us		21.9		nC
Body Diode Reverse Recovery Charge	Q _{rr}			14.9		
Maximum Body-Diode Continuous Current	I _S				-6	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1	V

Note :The static characteristics in Figures 1 to 6 are obtained using<300μs pulses, duty cycle 0.5% max.

■ Marking

Marking	4443
	KC****

P-Channel MOSFET AO4443 (KO4443)

■ Typical Characteristics

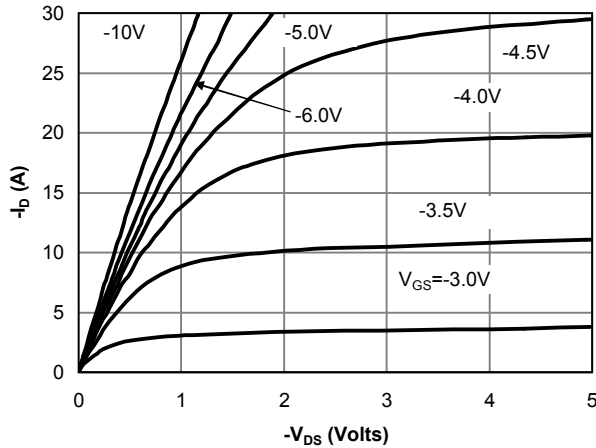


Fig 1: On-Region Characteristics

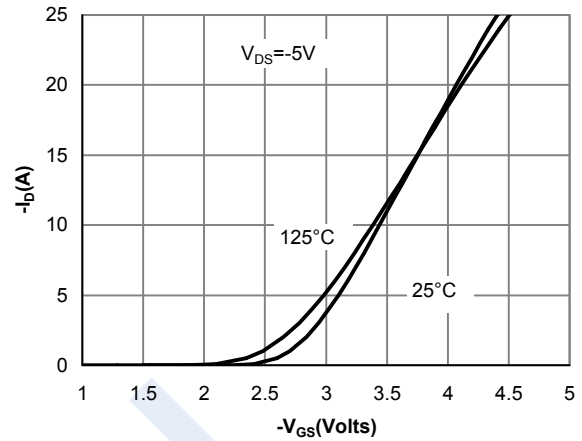


Figure 2: Transfer Characteristics

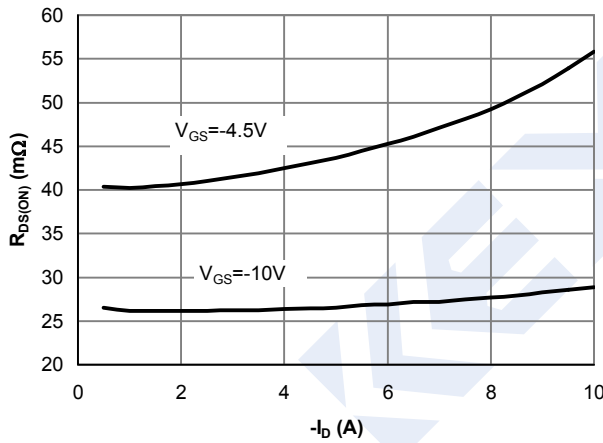


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

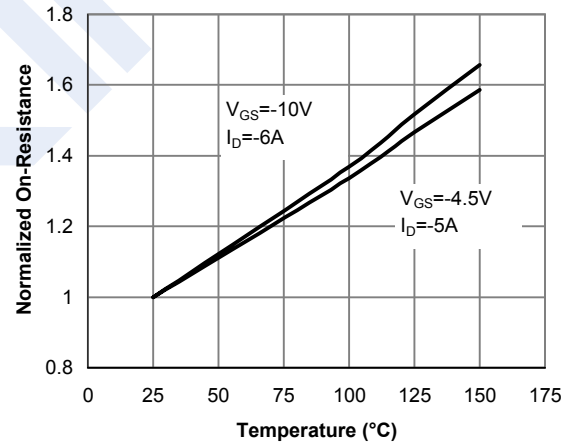


Figure 4: On-Resistance vs. Junction Temperature

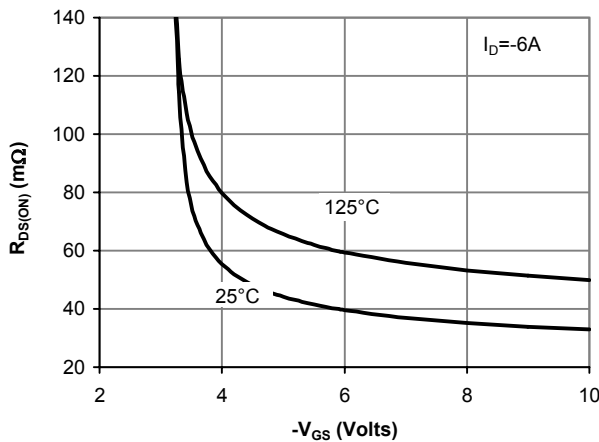


Figure 5: On-Resistance vs. Gate-Source Voltage

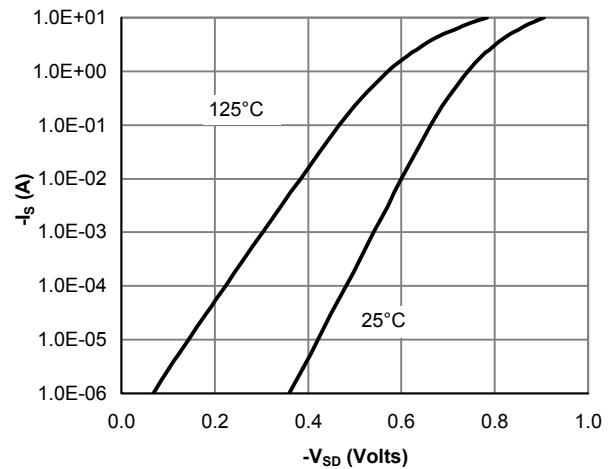


Figure 6: Body-Diode Characteristics

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■ Typical Characteristics

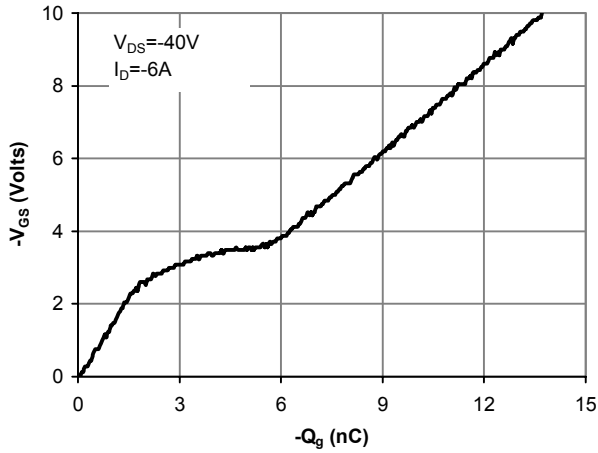


Figure 7: Gate-Charge Characteristics

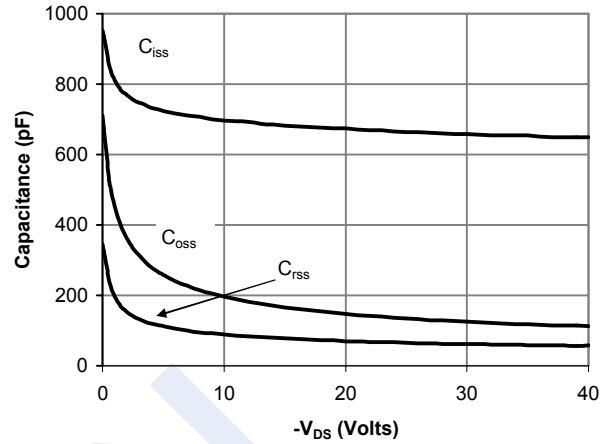


Figure 8: Capacitance Characteristics

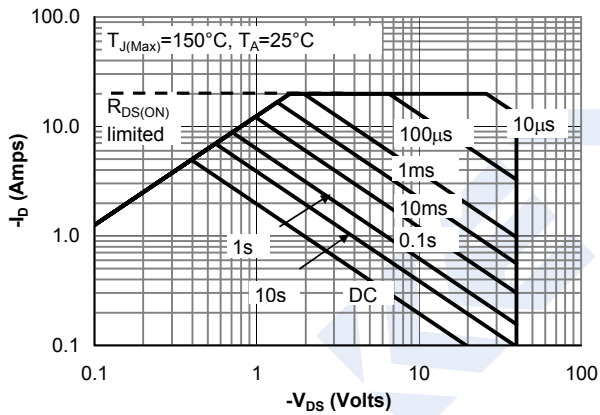


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

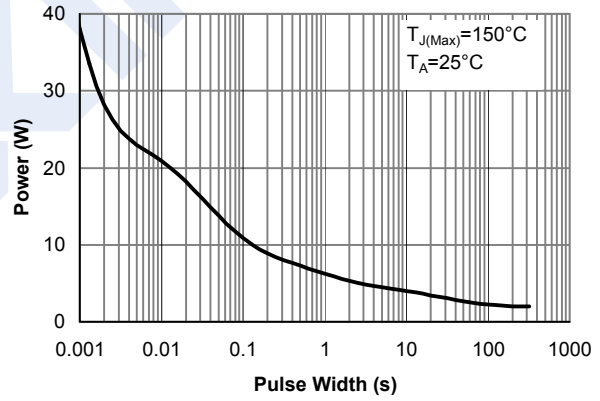


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

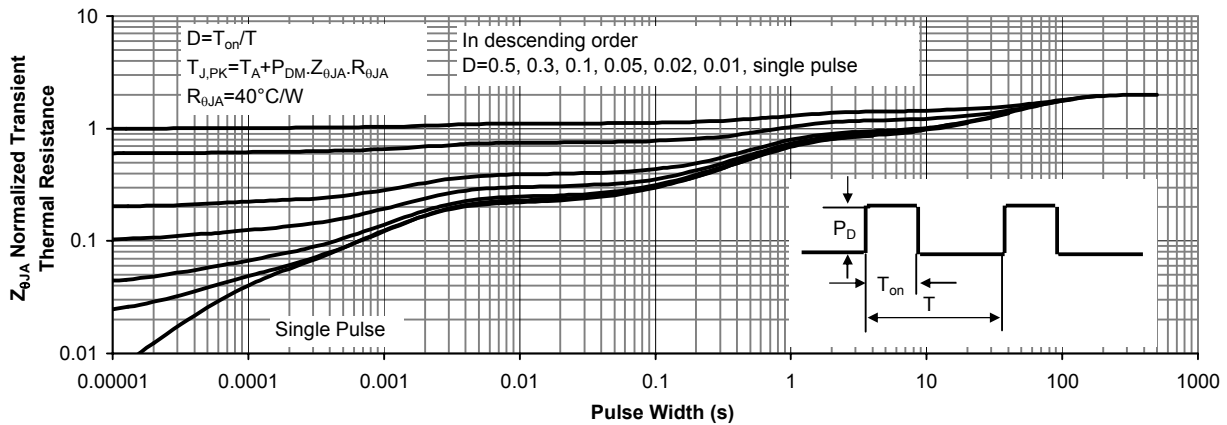


Figure 11: Normalized Maximum Transient Thermal Impedance