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March 2014

# **FBA42060**

# PFC SPM® 45 Series for Single-Phase Boost PFC

### **Features**

- UL Certified No. E209204 (UL1557)
- 600 V 20 A Single-Phase Boost PFC with Integral Gate Driver and Protection
- Low Thermal Resistance Using Ceramic Substrate
- Full-Wave Bridge Rectifier and High-Performance Output Diode
- · Optimized for 20kHz Switching Frequency
- · Built-in NTC Thermistor for Temperature Monitoring
- · Isolation Rating: 2000 Vrms/min.

## **Applications**

· Single-Phase Boost PFC Converter

### **Related Source**

- · AN-9091 Boost PFC Inductor Design Guide
- AN-9072 Motion SPM® 45 Series Mounting Guidance

## **General Description**

The FBA42060 is an advanced PFC SPM® 45 module providing a fully-featured, high-performance Boost PFC (Power Factor Correction) input power stage for consumer, medical, and industrial applications. These modules integrate optimized gate drive of the built-in IGBT to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockout, over-current shutdown, thermal monitoring, and fault reporting. These modules also feature a full-wave rectifier and high-performance output diode for additional space savings and mounting convenience.



Figure 1. Package Overview

## Package Marking & Ordering Information

Device	Device Marking	Package	Packing Type	Quantity
FBA42060	FBA42060	SPMAA-F26	Rail	12

# Integrated Drive, Protection and System Control Functions

- For IGBTs: gate drive circuit, Over-Current Protection (OCP), control supply circuit Under-Voltage Lock-Out (UVLO) Protection
- · Fault signal: corresponding to OC and UV fault
- Built-in NTC thermistor: temperature monitoring
- Input interface: active-HIGH interface, works with 3.3 / 5 V logic, Schmitt trigger input

# **Pin Configuration**

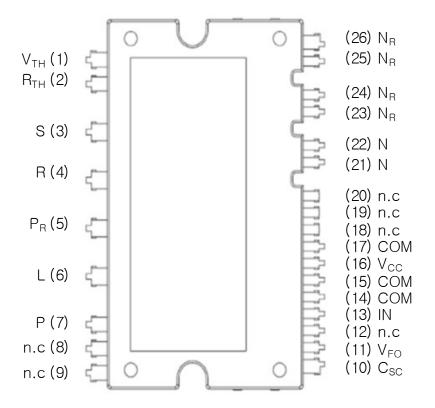


Figure 2. Top View

# **Pin Descriptions**

Pin Number	Pin Name	Pin Description
1	V <sub>TH</sub>	Thermistor Bias Voltage
2	R <sub>TH</sub>	Series Resistor for The Use of Thermistor
3	S	AC Input for S-Phase
4	R	AC Input for R-Phase
5	P <sub>R</sub>	Positive DC-Link of Rectifier
6	L	Inductor Connection
7	Р	Positive DC-Link Input
8, 9	N.C	-
10	C <sub>OC</sub>	Signal Input for Over-Current Detection
11	V <sub>FO</sub>	Fault Output
12	N.C	-
13	IN	PWM Input for IGBT Drive
14	COM	Common Supply Ground
15	COM	Common Supply Ground
16	V <sub>CC</sub>	Common Supply Voltage of IC for IGBT Drive
17	COM	Common Supply Ground
18 ~ 20	N.C	-
21, 22	N	Negative DC-Link Input
23 ~ 26	N <sub>R</sub>	Negative DC-Link of Rectifier Diode

# Internal Equivalent Circuit

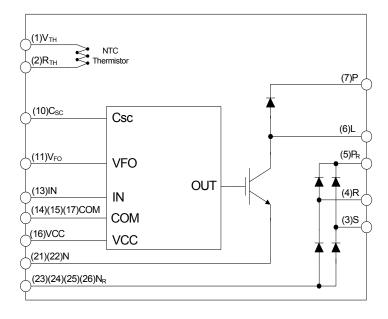


Figure 3. Internal Block Diagram

# **Absolute Maximum Ratings**

### **Converter Part**

Symbol	Parameter	Conditions	Rating	Unit
V <sub>i</sub>	Input Supply Voltage	Applied between R - S	276	V <sub>rms</sub>
V <sub>i(Surge)</sub>	Input Supply Voltage (Surge)	Applied between R - S	500	V
$V_{PN}$	Output Voltage	Applied between P <sub>R</sub> - N <sub>R</sub>	450	V
V <sub>PN(Surge)</sub>	Output Supply Voltage (Surge)	Applied between P <sub>R</sub> - N <sub>R</sub>	500	V
V <sub>CES</sub>	Collector - Emitter Voltage		600	V
$V_{RRM}$	Repetitive Peak Reverse Voltage		600	٧
± I <sub>C</sub>	Each IGBT Collector Current	$T_C = 25^{\circ}C, V_{CC} = 15 \text{ V}$	20	Α
± I <sub>CP</sub>	Each IGBT Collector Current (Peak)	T <sub>C</sub> = 25°C, Under 1 ms Pulse Width	30	Α
I <sub>FSM</sub>	Peak Forward Surge Current	Single Half Sine-Wave	200	Α
$T_J$	Operating Junction Temperature		-40 ~ 150	°C

### **Control Part**

Symbol	Parameter	Conditions	Rating	Unit
V <sub>CC</sub>	Control Supply Voltage	Applied between V <sub>CC</sub> - COM	20	V
V <sub>IN</sub>	Input Signal Voltage	Applied between IN - COM	-0.3 ~ V <sub>CC</sub> + 0.3	V
V <sub>FO</sub>	Fault Output Supply Voltage	Applied between V <sub>FO</sub> - COM	-0.3 ~ V <sub>CC</sub> + 0.3	V
I <sub>FO</sub>	Fault Output Current	Sink Current at V <sub>FO</sub> Pin	1	mA
V <sub>SC</sub>	Current Sensing Input Voltage	Applied between C <sub>SC</sub> - COM	-0.3 ~ V <sub>CC</sub> + 0.3	V

# **Total System**

Symbol	Parameter	Conditions	Rating	Unit
T <sub>STG</sub>	Storage Temperature		-40 ~ 125	°C
V <sub>ISO</sub>	Isolation Voltage	60 Hz, Sinusoidal, AC 1 Minute, Connect Pins to Heat Sink Plate	2000	$V_{rms}$

### **Thermal Resistance**

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Junction to Case Thermal	IGBT	-	-	2.5	°C/W
R <sub>th(j-c)D</sub>	Resistance at Chip Center	FRD	-	-	2.5	°C/W
$R_{th(j-c)R}$		Rectifier	-	-	2.5	°C/W

# **Electrical Characteristics** (T<sub>J</sub> = 25°C, unless otherwise specified.)

### **Converter Part**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CE(SAT)</sub>	IGBT Collector - Emitter Saturation Voltage	V <sub>CC</sub> = 15 V, V <sub>IN</sub> = 5V, I <sub>C</sub> = 20 A	-	2.2	2.7	V
V <sub>FF</sub>	FRD Forward Voltage	I <sub>F</sub> = 20 A	-	2.1	2.6	V
$V_{FR}$	Rectifier Forward Voltage	I <sub>F</sub> = 20 A	1	1.1	1.4	٧
t <sub>ON</sub>	Switching Characteristic	$V_{PN} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_{C} = 20 \text{ A},$	1	770	-	ns
t <sub>OFF</sub>		$V_{IN} = 0 \text{ V} \leftrightarrow 5 \text{ V}$ , Inductive Load (1st Note 1)	-	640	-	ns
t <sub>C(ON)</sub>			1	130	-	ns
t <sub>C(OFF)</sub>			ı	50	-	ns
trr			-	40	-	ns
Irr			-	4.0	-	Α
I <sub>CES</sub>	Collector - Emitter Leakage Current	V <sub>CE</sub> = V <sub>CES</sub>	-	1	1	mA

#### 1st Notes

<sup>1.</sup>  $t_{ON}$  and  $t_{OFF}$  include the propagation delay of the internal drive IC.  $t_{C(ON)}$  and  $t_{C(OFF)}$  are the switching time of IGBT itself under the given gate driving condition internally. For the detailed information, please see Figure 4.

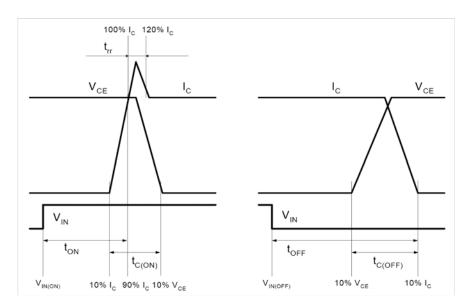


Figure 4. Switching Time Definitions

### **Control Part**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>QCC</sub>	Quiescent V <sub>CC</sub> Supply Current	V <sub>CC</sub> = 15 V, V <sub>IN</sub> = 0V, V <sub>CC</sub> - COM	-	-	2.65	mA
V <sub>FOH</sub>	Fault Output Voltage	$V_{SC}$ = 0 V, $V_{FO}$ Circuit: 4.7 k $\Omega$ to 5 V Pull-up	4.5	-	-	V
V <sub>FOL</sub>		$V_{SC}$ = 1 V, $V_{FO}$ Circuit: 4.7 k $\Omega$ to 5 V Pull-up	-	-	0.8	V
V <sub>SC(ref)</sub>	Over-Current Protection Trip Level Voltage of C <sub>SC</sub> pin	V <sub>CC</sub> = 15 V (1st Note 2)	0.45	0.50	0.55	V
UV <sub>CCD</sub>	Supply Circuit Under-	Detection Level	10.5		13.0	V
UV <sub>CCR</sub>	Voltage Protection	Reset Level	11.0		13.5	V
V <sub>IN(ON)</sub>	ON Threshold Voltage	Applied between IN - COM	-	-	2.6	V
V <sub>IN(OFF)</sub>	OFF Threshold Voltage		0.8	-	-	V
R <sub>TH</sub>	Resistance of Thermistor	T <sub>TH</sub> = 25°C (1st Note 3)	-	47.0	-	kΩ
		T <sub>TH</sub> = 100°C	-	2.9	-	kΩ

### 1st Notes:

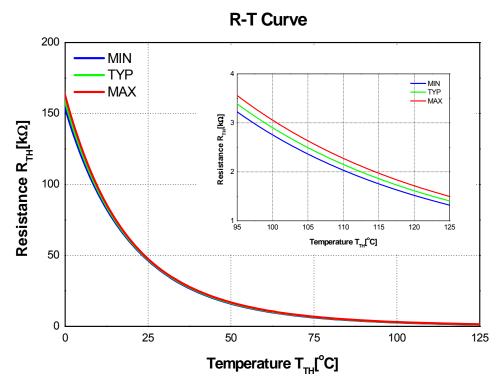


Figure 5. R-T Curve of The Built-in Thermistor

<sup>2.</sup> Over-current protection is functioning on IGBT.

 $<sup>3.\</sup> T_{TH}$  is the temperature of thermister itself. To know case temperature ( $T_{C}$ ), please make the experiment considering your application.

# **Recomended Operating Conditions**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Vi	Input Supply Voltage	Applied between R - S	198	220	242	V <sub>rms</sub>
V <sub>PN</sub>	Supply Voltage	Applied between P <sub>R</sub> - N	-	360	400	V
l <sub>i</sub>	Input Current	$V_{DC}$ = 360 V, $F_{SW}$ = 20 kHz, $V_{CC}$ = 15 V, $T_C$ = 90°C, $T_J \le 150$ °C	-	20	-	A <sub>peak</sub>
V <sub>CC</sub>	Supply Voltage for inverter	Applied between V <sub>CC</sub> - COM	13.5	15.0	16.5	V
P <sub>WIN(ON)</sub>	Minimum Input Pulse Width	(1st Note 4)	0.5	-	-	μS
P <sub>WIN(OFF)</sub>			0.5	-	-	μS
dV <sub>CC</sub> /dt	Supply Variation		-1	-	1	V/μs
f <sub>PWM</sub>	PWM Input Frequency	T <sub>J</sub> ≤ 150°C	-	20	-	kHz
V <sub>SEN</sub>	Voltage for Current Sensing	Applied between N - COM (Including surge voltage)	-4	-	4	V

### 1st Notes:

# **Mechanical Characteristics and Ratings**

Parameter	Cor	nditions	Min.	Тур.	Max.	Unit
Mounting Torque	Mounting Screw: M3	Recommended 0.7 N•m	0.6	0.7	0.8	N•m
Device Flatness		See Figure 6	0	-	+120	μm
Weight			-	11	-	g

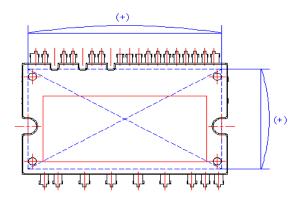
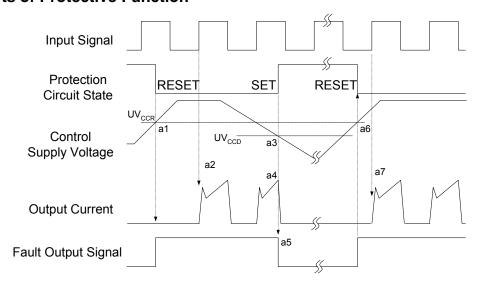


Figure 6. Flatness Measurement Position

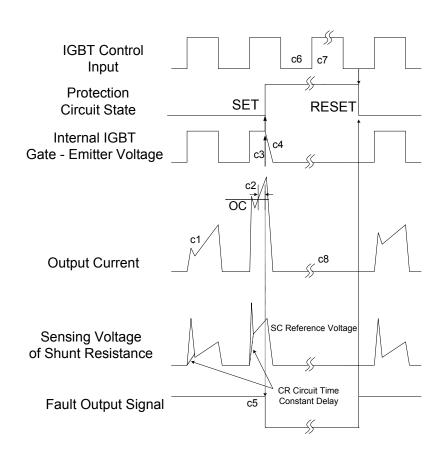
<sup>4.</sup> The PFC SPM® product might not make response if input pulse width is less than the recommended value.

### **Time Charts of Protective Function**



- a1 : Control supply voltage rises: after the voltage rises UV<sub>CCR</sub>, the circuits start to operate when the next input is applied.
- a2: Normal operation: IGBT ON and carrying current.
- a3 : Under-voltage detection (UV<sub>CCD</sub>).
- a4 : IGBT OFF in spite of control input condition.
- ${\tt a5:Fault\ output\ operation\ starts.}$
- a6 : Under-voltage reset (UV<sub>CCR</sub>).
- a7: Normal operation: IGBT ON and carrying current.

Figure 7. Under-Voltage Protection



(with the external shunt resistance and CR connection)

c1: Normal operation: IGBT ON and carrying current.

c2 : Over-current detection (OC trigger).

c3: Hard IGBT gate interrupt.

c4: IGBT turns OFF.

c5 : Fault output timer operation starts.

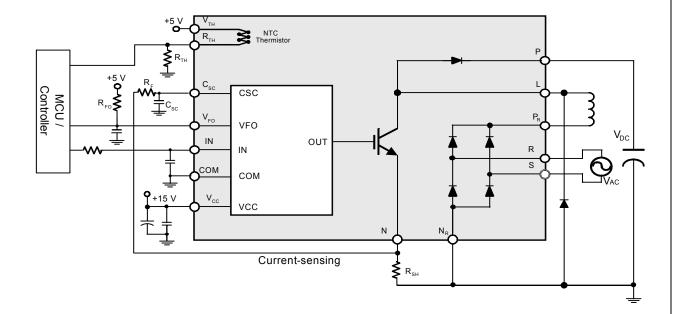
c6: Input "LOW": IGBT OFF state.

c7 : Input "HIGH": IGBT ON state, but during the active period of fault output the IGBT doesn't turn ON.

c8: IGBT OFF state

**Figure 8. Over Current Protection** 

# **Recommand circuit for Application**

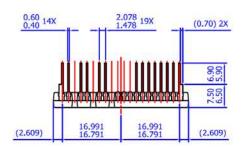


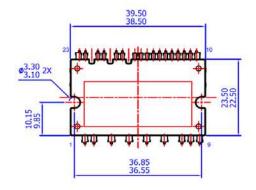
**Figure 9. Typical Application Circuit** 

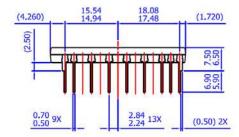
### 2nd Notes

- 1. To avoid malfunction, the wiring of each input should be as short as possible (less than 2 3 cm).
- 2. V<sub>FO</sub> output is open-drain type. This signal line should be pulled up to the positive side of the MCU or PFC controller power supply with a resistor that makes I<sub>FO</sub> up to 1 mA.
- 3. Input signal is active-HIGH type. There is a 5 k $\Omega$  resistor inside the IC to pull-down each input signal line to GND. RC coupling circuits is recommanded for the prevention of input signal oscillation. R<sub>S</sub>C<sub>PS</sub> time constant should be selected in the range 50 ~ 150 ns (recommended R<sub>S</sub> = 100  $\Omega$ , C<sub>PS</sub> = 1 nF).
- 4. To prevent errors of the protection function, the wiring around  $R_F$  and  $C_{SC}$  should be as short as possible.
- 5. In the over-current protection circuit, please select the R<sub>F</sub>, C<sub>SC</sub> time constant in the range 1~2  $\mu s$ .
- 6. Each capacitors should be mounted as close to the pins as possible.
- 7. Relays are used in almost every systems of electrical equipment in home appliances. In these cases, there should be sufficient distance between the MCU and the relays.
- 8. Internal NTC thermistor can be used for monitoring the case temperature and protecting the device from the over-heating operation. Please select an appropriate resistor RTH according to the application. For example, use  $R_{TH} = 4.7 \text{ k}\Omega$  that will make the voltage across  $R_{TH}$  to be 2.5 V at 85°C of the case temperature.
- 9. Please use an appropriate shunt resistor  $R_{SH}$  to protect the intenal IGBT from the over-current operation.
- 10. It's recommended that anti-parallel diode should be connected with IGBT.

## **Detailed Package Outline Drawings**

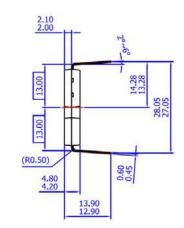


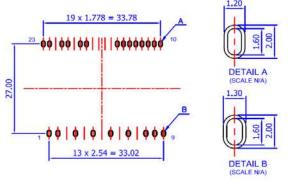






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